

```

1  ┌────────────────────────── MODULE SafeRegister ───────────────────────────┐
2  CONSTANTS
3      Writer,
4      Reader,
5      T
7  notValue  $\triangleq$  CHOOSE  $t : t \notin T$ 
8
9  *****
10 --algorithm Register{
11     variables  $r = notValue, result = notValue$ ;
12
13     process (  $W \in Writer$  )
14     {
15          $a: r := notValue$ ;
16          $b: \text{with } (v \in T)$ 
17             {  $r := v$  }
18     }
19
20     process (  $R \in Reader$  ) {
21          $c: \text{with } (v \in \text{IF } r = notValue \text{ THEN } T \text{ ELSE } \{r\})$ 
22             {  $result := v$  }
23     }
24 }
25 *****
26 BEGIN TRANSLATION
27 VARIABLES  $r, result, pc$ 
28
29  $vars \triangleq \langle r, result, pc \rangle$ 
30
31  $ProcSet \triangleq (Writer) \cup (Reader)$ 
32
33  $Init \triangleq$  Global variables
34      $\wedge r = notValue$ 
35      $\wedge result = notValue$ 
36      $\wedge pc = [self \in ProcSet \mapsto \text{CASE } self \in Writer \rightarrow \text{"a"}$ 
37          $\square \quad self \in Reader \rightarrow \text{"c"}]$ 
38
39  $a(self) \triangleq$   $\wedge pc[self] = \text{"a"}$ 
40      $\wedge r' = notValue$ 
41      $\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"b"}]$ 
42      $\wedge \text{UNCHANGED } result$ 
43
44  $b(self) \triangleq$   $\wedge pc[self] = \text{"b"}$ 
45      $\wedge \exists v \in T :$ 
46      $r' = v$ 
47      $\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"Done"}]$ 

```

