```
1 
                                            MODULE SafeRegister
    CONSTANTS
2
          Writer.
3
          Reader,
 4
 5
    notValue \stackrel{\triangle}{=} CHOOSE \ t: t \notin T
      *************************
    --algorithm Register{
10
         variables r = notValue, result = notValue;
11
         process ( W \in Writer )
13
14
               a: r := notValue;
15
              b: with (v \in T)
16
                          \{ r := v \}
17
          }
18
         process ( R \in Reader ) {
20
              c: with ( v \in \text{IF } r = notValue \text{ Then } T \text{ else } \{r\} )
21
                          \{ result := v \}
22
23
     }
24
      BEGIN TRANSLATION
26
    Variables r, result, pc
27
    vars \stackrel{\triangle}{=} \langle r, result, pc \rangle
     ProcSet \triangleq (Writer) \cup (Reader)
     Init \stackrel{\triangle}{=} Global variables
33
                \wedge r = notValue
34
                \land result = notValue
35
                \land \ pc = [\mathit{self} \in \mathit{ProcSet} \mapsto \mathtt{CASE} \ \mathit{self} \in \mathit{Writer} \to \mathtt{``a"}
36
                                                     \square self \in Reader \rightarrow "c"]
37
                \stackrel{\Delta}{=} \wedge pc[self] = "a"
     a(self)
39
                      \wedge r' = not Value
40
                      \land pc' = [pc \text{ EXCEPT } ! [self] = \text{"b"}]
41
                      \land UNCHANGED result
42
                \stackrel{\Delta}{=} \wedge pc[self] = \text{"b"}
     b(self)
44
                      \wedge \exists v \in T:
45
                           r' = v
46
                      \land pc' = [pc \text{ EXCEPT } ! [self] = \text{"Done"}]
47
```

```
\land UNCHANGED result
48
     W(self) \stackrel{\triangle}{=} a(self) \lor b(self)
50
                 \stackrel{\Delta}{=} \wedge pc[self] = \text{``c''}
52
                       \land \exists v \in \text{IF } r = notValue \text{ THEN } T \text{ ELSE } \{r\}:
53
54
                       \land pc' = [pc \text{ EXCEPT } ! [self] = "Done"]
55
                       \wedge \ r' = r
56
    R(self) \stackrel{\Delta}{=} c(self)
     Next \stackrel{\Delta}{=} (\exists self \in Writer : W(self))
60
                    \vee (\exists self \in Reader : R(self))
61
                     V Disjunct to prevent deadlock on termination
62
                       ((\forall self \in ProcSet : pc[self] = "Done") \land UNCHANGED vars)
63
    Spec \triangleq Init \wedge \Box [Next]_{vars}
     Termination \triangleq \Diamond(\forall self \in ProcSet : pc[self] = "Done")
      END TRANSLATION
69
     \ * Last modified Sat Jul 06 09:21:10 CST 2019 by hengxin
```

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