



On Symmetry and Quantification: A New Approach to Verify Distributed Protocols

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Abstract. Proving that an unbounded distributed protocol satisfies a given safety property amounts to finding a quantified inductive invariant that implies the property for all possible instance sizes of the protocol. Existing methods for solving this problem can be described as search procedures for an invariant whose quantification prefix fits a particular template. We propose an alternative *constructive* approach that does not prescribe, *a priori*, a specific quantifier prefix. Instead, the required prefix is automatically inferred without any search by carefully analyzing the structural symmetries of the protocol. The key insight underlying this approach is that symmetry and quantification are closely related concepts that express protocol *invariance* under different re-arrangements of its components. We propose *symmetric incremental induction*, an extension of the finite-domain IC3/PDR algorithm, that automatically derives the required *quantified inductive invariant* by exploiting the connection between symmetry and quantification. While various attempts have been made to exploit symmetry in verification applications, to our knowledge, this is the first demonstration of a direct link between symmetry and quantification in the context of clause learning during incremental induction. We also describe a procedure to automatically find a minimal finite size, the *cutoff*, that yields a quantified invariant proving safety for any size.

Our approach is implemented in IC3PO, a new verifier for distributed protocols that significantly outperforms the state-of-the-art, scales orders of magnitude faster, and robustly derives compact inductive invariants fully automatically.

1 Introduction

Our focus in this paper is on *parameterized verification*, specifically proving *safety* properties of distributed systems, such as protocols that are often modeled above the code level (e.g., [49, 63]), consisting of arbitrary numbers of *identical* components that are instances of a small set of different *sorts*. For example, a client server protocol[1] $CS(i, j)$ is a two-sort parameterized system with parameters $i \geq 1$ and $j \geq 1$ denoting, respectively, the number of clients and servers. Protocol correctness proofs are critical for establishing the correctness of actual system implementations in established methodologies such as [42, 68]. Proving safety

properties for such systems requires the derivation of inductive invariants that are expressed as state predicates quantified over the system parameters. While, in general, this problem is undecidable [7], certain restricted forms have been shown to yield to algorithmic solutions [16]. Key to these solutions is appealing to the problem’s inherent symmetry. In this paper, we exclusively focus on protocols whose sorts represent sets of indistinguishable domain constants. The behavior of this restricted class of protocols remains invariant under all possible permutations of the domain constants. We leave the exploration of other features, such as totally-ordered sorts, integer arithmetic, etc., for future work.

Our proposed symmetry-based solution is best understood by briefly reviewing earlier efforts. Initially, the pressing issue was the inevitable *state explosion* when verifying a finite, but large, parameterized system [11, 28, 36, 60, 65, 67]. Thus, instead of verifying the “full” system, these approaches verified its *symmetry-reduced quotient*, mostly using BDD-based symbolic image computation [18, 19, 56]. The Mur ϕ verifier [60] was a notable exception in that it a) generated a C++ program that enumerated the system’s symmetry-reduced reachable states, and b) allowed for the verification of unbounded systems by taking advantage of *data saturation* which happens when the size of the symmetry-reduced reachable states become constant regardless of system size.

The idea that an unbounded *symmetric* system can, under certain data-independence assumptions, be verified by analyzing small finite instances evolved into the approach of verification by *invisible invariants* [8, 9, 24, 64, 69]. In this approach, assuming they exist, inductive invariants that are universally-quantified over the system parameters are automatically derived by analyzing instances of the system up to a *cutoff* size N_0 using a combination of symbolic reachability and symmetry-based abstraction. Noting that an invariant is an over-approximation of the reachable states, the restriction to universal quantification may fail in some cases, rendering the approach incomplete. The invisible invariant verifier IIV [9] employs some heuristics to derive invariants that use combinations of universal and existential quantifiers, but as pointed out in [58], it may still fail and is not guaranteed to be complete.

The development of SAT-based incremental induction algorithms [17, 26] for verifying the safety of finite transition systems was a major advance in the field of model checking and has, for the most part, replaced BDD-based approaches. These algorithms leverage the capacity and performance of modern CDCL SAT solvers [10, 27, 55, 57] to produce *clausal strengthening assertions* A that, conjoined with a specified safety property P , form an automatically-generated inductive invariant $Inv = A \wedge P$ if the property holds. The AVR hardware verifier [38–40] was adapted in [53] to produce quantifier-free inductive invariants for small instances of unbounded protocols that are subsequently generalized with universal quantification, in analogy with the invisible invariants approach, to arbitrary sizes. The resulting assertions tended, in some cases, to be quite large, and the approach was also incomplete due to the restriction to universal quantification.

In this paper we introduce IC3PO, a novel symmetry-based verifier that builds on these previous efforts while removing most of their limitations. Rather

than search for an invariant with a prescribed quantifier prefix, IC3PO constructively *discovers* the required quantified assertions by performing *symmetric incremental induction* and analyzing the symmetry patterns in learned clauses to infer the corresponding quantifier prefix. Our main contributions are:

- An extension to finite incremental induction algorithms that uses protocol symmetry to boost clause learning from a *single* clause φ to a set of symmetrically-equivalent clauses, φ 's *orbit*.
- A quantifier inference procedure that expresses φ 's orbit by an automatically-derived *compact* quantified predicate Φ . The inference procedure is based on a simple analysis of φ 's *syntactic structure* and yields a quantified form with both universal and existential quantifiers.
- A systematic *finite convergence* procedure for determining a minimal instance size sufficient for deriving a quantified inductive invariant that holds for all sizes.

We also demonstrate the effectiveness of IC3PO on a diverse set of benchmarks and show that it significantly advances the current state-of-the-art.

The paper is structured as follows: Sect. 2 presents preliminaries. Section 3 formalizes protocol symmetries. The next three sections detail our key contributions: symmetry boosting during incremental induction in Sect. 4, relating symmetry to quantification in Sect. 5, and checking for convergence in Sect. 6. Section 7 describes the IC3PO algorithm and implementation details. Section 8 presents our experimental evaluation. The paper concludes with a brief survey of related work in Sect. 9, and a discussion of future directions in Sect. 10.

2 Preliminaries

Figure 1 describes a toy consensus protocol from [5] in the TLA+ language [49].¹ The protocol has three named sorts $S = [\text{node}, \text{quorum}, \text{value}]$ introduced by the CONSTANTS declaration, and two relations $R = \{\text{vote}, \text{decision}\}$, introduced by the VARIABLES declaration, that are defined on these sorts. Each of the sorts is understood to represent an unbounded domain of distinct elements with the relations serving as the protocol's state variables. The global axiom (line 3) defines the elements of the **quorum** sort to be subsets of the **node** sort and restricts them further by requiring them to be pair-wise non-disjoint. We will refer to **node** (resp. **quorum**) as an *independent* (resp. *dependent*) sort. The protocol transitions are specified by the actions *CastVote* and *Decide* (lines 6–7) which are expressed using the current- and next-state variables as well as the definitions *didNotVote* and *chosenAt* (lines 4–5) which serve as *auxiliary non-state* variables. Lines 8–10 specify the protocol's initial states, transition relation, and safety property.

Viewed as a parameterized system, the *template* of an arbitrary n -sort distributed protocol \mathcal{P} will be expressed as $\mathcal{P}(s_1, \dots, s_n)$ where $S = [s_1, \dots, s_n]$

¹ The description in [5] is in the Ivy [63] language and encodes set operations in relational form with a *member* relation representing \in .

3 Protocol Symmetries

The symmetry group of $\hat{\mathcal{P}}$ is $G(\hat{\mathcal{P}}) = \times_{\mathbf{s} \in S} \text{Sym}(\mathbf{s})$, where $\text{Sym}(\mathbf{s})$ is the symmetric group, i.e., the set of $|\mathbf{s}|!$ permutations of the constants of the set \mathbf{s} .² In what follows we will use G instead of $G(\hat{\mathcal{P}})$ to reduce clutter. Given a permutation $\gamma \in G$ and an arbitrary protocol relation ρ instantiated with specific sort constants, the *action* of γ on ρ , denoted ρ^γ , is the relation obtained from ρ by permuting the sort constants in ρ according to γ ; it is referred to as the γ -*image* of ρ . Permutation $\gamma \in G$ can also act on any formula involving the protocol relations. In particular, the invariance of protocol behavior under permutation of sort constants implies that the action of γ on the (finite) initial state, transition relation, and property formulas causes a syntactic re-arrangement of their sub-formulas while preserving their logical equivalence:

$$\hat{Init}^\gamma \equiv \hat{Init} \qquad \hat{T}^\gamma \equiv \hat{T} \qquad \hat{\mathcal{P}}^\gamma \equiv \hat{\mathcal{P}} \qquad (2)$$

Consider next a clause φ which is a disjunction of literals, namely, instantiated protocol relations or their negations. The *orbit* of φ under G , denoted φ^G , is the set of its images φ^γ for all permutations $\gamma \in G$, i.e., $\varphi^G = \{\varphi^\gamma \mid \gamma \in G\}$. The γ -image of a clause can be viewed as a *syntactic* transformation that will either yield a new logically-distinct clause on different literals or simply re-arrange the literals in the clause without changing its logical behavior (by the commutativity and associativity of disjunction). We define the *logical action* of a permutation γ on a clause φ , denoted $\varphi^{L(\gamma)}$, as:

$$\varphi^{L(\gamma)} = \begin{cases} \varphi^\gamma & \text{if } \varphi^\gamma \not\equiv \varphi \\ \varphi & \text{if } \varphi^\gamma \equiv \varphi \end{cases}$$

and the *logical orbit* of φ as $\varphi^{L(G)} = \{\varphi^{L(\gamma)} \mid \gamma \in G\}$. With a slight abuse of notation, logical orbit can also be viewed as the conjunction of the logical images:

$$\varphi^{L(G)} = \bigwedge_{\gamma \in G} \varphi^{L(\gamma)}$$

To illustrate these concepts, consider *ToyConsensus*(3, 3, 3) from (1). Its symmetries in cycle notation are as follows:

$$\begin{aligned} \text{Sym}(\text{node}_3) &= \{(), (\mathbf{n}_1 \ \mathbf{n}_2), (\mathbf{n}_1 \ \mathbf{n}_3), (\mathbf{n}_2 \ \mathbf{n}_3), (\mathbf{n}_1 \ \mathbf{n}_2 \ \mathbf{n}_3), (\mathbf{n}_1 \ \mathbf{n}_3 \ \mathbf{n}_2)\} \\ \text{Sym}(\text{value}_3) &= \{(), (\mathbf{v}_1 \ \mathbf{v}_2), (\mathbf{v}_1 \ \mathbf{v}_3), (\mathbf{v}_2 \ \mathbf{v}_3), (\mathbf{v}_1 \ \mathbf{v}_2 \ \mathbf{v}_3), (\mathbf{v}_1 \ \mathbf{v}_3 \ \mathbf{v}_2)\} \\ G &= \text{Sym}(\text{node}_3) \times \text{Sym}(\text{value}_3) \end{aligned} \qquad (3)$$

The symmetry group (3) of *ToyConsensus*(3, 3, 3) has 36 symmetries corresponding to the $6 \text{ node}_3 \times 6 \text{ value}_3$ permutations. The permutations on **quorum**₃

² We assume familiarity with basic notions from *group theory* including *permutation groups*, *cycle notation*, *group action* on a set, *orbits*, etc., which can be readily found in standard textbooks on Abstract Algebra [32].

are *implicit* and based on the permutations of node_3 since quorum_3 is a dependent sort. Now, consider the example clause:

$$\varphi_1 = \text{vote}(\mathbf{n}_1, \mathbf{v}_1) \vee \text{vote}(\mathbf{n}_1, \mathbf{v}_2) \vee \text{vote}(\mathbf{n}_1, \mathbf{v}_3) \quad (4)$$

The orbit of φ_1 consists of 36 syntactically-permuted clauses. However, many of these images are logically equivalent yielding the following logical orbit of just 3 logically-distinct clauses:

$$\begin{aligned} \varphi_1^{L(G)} = & [\text{vote}(\mathbf{n}_1, \mathbf{v}_1) \vee \text{vote}(\mathbf{n}_1, \mathbf{v}_2) \vee \text{vote}(\mathbf{n}_1, \mathbf{v}_3)] \wedge \\ & [\text{vote}(\mathbf{n}_2, \mathbf{v}_1) \vee \text{vote}(\mathbf{n}_2, \mathbf{v}_2) \vee \text{vote}(\mathbf{n}_2, \mathbf{v}_3)] \wedge \\ & [\text{vote}(\mathbf{n}_3, \mathbf{v}_1) \vee \text{vote}(\mathbf{n}_3, \mathbf{v}_2) \vee \text{vote}(\mathbf{n}_3, \mathbf{v}_3)] \end{aligned} \quad (5)$$

4 *SymIC3*: Symmetric Incremental Induction

SymIC3 is an extension of the standard IC3 algorithm [17, 26] that takes advantage of the symmetries in a finite instance $\hat{\mathcal{P}}$ of an unbounded protocol \mathcal{P} to *boost learning* during backward reachability. Specifically, it refines the current frame, in a *single* step, with *all* clauses in the logical orbit $\varphi^{L(G)}$ of a newly-learned quantifier-free clause φ . In other words, having determined that the backward 1-step check $F_{i-1} \wedge \hat{T} \wedge [\neg\varphi]'$ is unsatisfiable (i.e., that states in cube $\neg\varphi$ in frame F_i are unreachable from the previous frame F_{i-1}), *SymIC3* refines F_i with $\varphi^{L(G)}$, i.e., $F_i := F_i \wedge \varphi^{L(G)}$, rather than with just φ . Thus, at each refinement step, *SymIC3* not only blocks cube $\neg\varphi$, but also all symmetrically-equivalent cubes $[\neg\varphi]^\gamma$ for all $\gamma \in G$. This simple change to the standard incremental induction algorithm significantly improves performance since the extra clauses used to refine F_i a) are derived *without* making additional backward 1-step queries, and b) provide stronger refinement in each step of backward reachability leading to faster convergence with fewer counterexamples-to-induction (CTIs). The proof of correctness of symmetry boosting can be found in [37].

5 Quantifier Inference

The key insight underlying our overall approach is that the explicit logical orbit, in a finite protocol instance, of a learned clause φ can be exactly, and systematically, captured by a corresponding quantified predicate Φ . In retrospect, this should not be surprising since symmetry and quantification can be seen as different ways of expressing invariance under permutation of the sort constants in the clause. To motivate the connection between symmetry and quantification, consider the following quantifier-free clause from our running example and a proposed quantified predicate that *implicitly* represents its logical orbit:

$$\begin{aligned} \varphi_2 &= \neg\text{decision}(\mathbf{v}_1) \vee \text{decision}(\mathbf{v}_2) \\ \Phi_2 &= \forall X_1, X_2 \in \text{value}_3 : (\text{distinct } X_1 \ X_2) \rightarrow [\neg\text{decision}(X_1) \vee \text{decision}(X_2)] \end{aligned} \quad (6)$$

Table 1. Correlation between symmetry and quantification for Φ_2 from (6), Highlighted clauses represent the logical orbit $\varphi_2^{L(G)}$, none indicates the clause has no corresponding permutation $\gamma \in \text{Sym}(\text{value}_3)$

(X_1, X_2)	Instantiation of Φ_2	Permutation
$(\mathbf{v}_1, \mathbf{v}_1)$	$(\text{distinct } \mathbf{v}_1 \ \mathbf{v}_1) \rightarrow [\neg \text{decision}(\mathbf{v}_1) \vee \text{decision}(\mathbf{v}_1)]$	none
$(\mathbf{v}_1, \mathbf{v}_2)$	$(\text{distinct } \mathbf{v}_1 \ \mathbf{v}_2) \rightarrow [\neg \text{decision}(\mathbf{v}_1) \vee \text{decision}(\mathbf{v}_2)]$	$()$
$(\mathbf{v}_1, \mathbf{v}_3)$	$(\text{distinct } \mathbf{v}_1 \ \mathbf{v}_3) \rightarrow [\neg \text{decision}(\mathbf{v}_1) \vee \text{decision}(\mathbf{v}_3)]$	$(\mathbf{v}_2 \ \mathbf{v}_3)$
$(\mathbf{v}_2, \mathbf{v}_1)$	$(\text{distinct } \mathbf{v}_2 \ \mathbf{v}_1) \rightarrow [\neg \text{decision}(\mathbf{v}_2) \vee \text{decision}(\mathbf{v}_1)]$	$(\mathbf{v}_1 \ \mathbf{v}_2)$
$(\mathbf{v}_2, \mathbf{v}_2)$	$(\text{distinct } \mathbf{v}_2 \ \mathbf{v}_2) \rightarrow [\neg \text{decision}(\mathbf{v}_2) \vee \text{decision}(\mathbf{v}_2)]$	none
$(\mathbf{v}_2, \mathbf{v}_3)$	$(\text{distinct } \mathbf{v}_2 \ \mathbf{v}_3) \rightarrow [\neg \text{decision}(\mathbf{v}_2) \vee \text{decision}(\mathbf{v}_3)]$	$(\mathbf{v}_1 \ \mathbf{v}_2 \ \mathbf{v}_3)$
$(\mathbf{v}_3, \mathbf{v}_1)$	$(\text{distinct } \mathbf{v}_3 \ \mathbf{v}_1) \rightarrow [\neg \text{decision}(\mathbf{v}_3) \vee \text{decision}(\mathbf{v}_1)]$	$(\mathbf{v}_1 \ \mathbf{v}_3 \ \mathbf{v}_2)$
$(\mathbf{v}_3, \mathbf{v}_2)$	$(\text{distinct } \mathbf{v}_3 \ \mathbf{v}_2) \rightarrow [\neg \text{decision}(\mathbf{v}_3) \vee \text{decision}(\mathbf{v}_2)]$	$(\mathbf{v}_1 \ \mathbf{v}_3)$
$(\mathbf{v}_3, \mathbf{v}_3)$	$(\text{distinct } \mathbf{v}_3 \ \mathbf{v}_3) \rightarrow [\neg \text{decision}(\mathbf{v}_3) \vee \text{decision}(\mathbf{v}_3)]$	none

As shown in Table 1, the logical orbit $\varphi_2^{L(G)}$ consists of 6 logically-distinct clauses corresponding to the 6 permutations of the 3 constants of the value_3 sort. Evaluating Φ_2 by substituting all $3 \times 3 = 9$ assignments to the variable pair $(X_1, X_2) \in \text{value}_3 \times \text{value}_3$ yields 9 clauses, 3 of which (shown faded) are trivially true since their “distinct” antecedents are false, with the remaining 6 corresponding to each of the clauses obtained through permutations of the 3 value_3 constants. Similarly, we can show that the 3-clause logical orbit $\varphi_1^{L(G)}$ in (5) can be succinctly expressed by the quantified predicate:

$$\Phi_1 = \forall Y \in \text{node}_3, \exists X \in \text{value}_3 : \text{vote}(Y, X) \quad (7)$$

which employs universal *and* existential quantification. And, finally, φ_3 and Φ_3 below illustrate how a clause whose logical orbit is just itself can also be expressed as an existentially-quantified predicate.

$$\begin{aligned} \varphi_3 &= \text{decision}(\mathbf{v}_1) \vee \text{decision}(\mathbf{v}_2) \vee \text{decision}(\mathbf{v}_3) \\ \Phi_3 &= \exists X \in \text{value}_3 : \text{decision}(X) \end{aligned} \quad (8)$$

We will first describe basic quantifier inference for protocols with independent sorts. This is done by analyzing the syntactic structure of each quantifier-free clause learned during incremental induction to derive a quantified form that expresses the clause’s logical orbit. We later discuss extensions to this approach that consider protocols with dependent sorts, such as *ToyConsensus*, for which the basic single-clause quantifier inference may be insufficient.

5.1 Basic Quantifier Inference

Given a quantifier-free clause φ , quantifier inference seeks to derive a *compact* quantified predicate that *implicitly* represents, rather than explicitly enumerates, its logical orbit. The procedure must satisfy the following conditions:

Correctness – The inferred quantified predicate Φ should be logically-equivalent to the explicit logical orbit $\varphi^{L(G)}$.

Compactness – The number of quantified variables in Φ for each sort $\mathbf{s} \in S$ should be independent of the sort size $|\mathbf{s}|$. Intuitively, this condition ensures that the size of the quantified predicate, measured as the number of its quantifiers, remains bounded for *any* finite protocol instance, and more importantly, for the unbounded protocol.

SymIC3 constructs the orbit’s quantified representation by a) inferring the required quantifiers for each sort separately, and b) stitching together the inferred quantifiers for the different sorts to form the final result. The key to capturing the logical orbit and deriving its compact quantified representation is a simple analysis of the *structural distribution* of each sort’s constants in the target clause. Let $\pi(\varphi, \mathbf{s})$ be a partition of the constants of sort \mathbf{s} in φ based on whether or not they appear *identically* in the literals of φ . Two constants c_i and c_j are identically-present in φ if they occur in φ and swapping them results in a logically-equivalent clause, i.e., $\varphi^{(c_i \ c_j)} \equiv \varphi$. Let $\#(\varphi, \mathbf{s})$ be the number of constants of \mathbf{s} that appear in φ , and let $|\pi(\varphi, \mathbf{s})|$ be the number of classes/cells in $\pi(\varphi, \mathbf{s})$. Consider the following scenarios for quantifier inference on sort \mathbf{s} :

A. $\#(\varphi, \mathbf{s}) < |\mathbf{s}|$ (**infer** \forall)

In this case, clause φ contains a strict subset of constants from sort \mathbf{s} , indicating that the number of literals in φ parameterized by \mathbf{s} constants is *independent* of the sort size $|\mathbf{s}|$. Increasing sort size simply makes the orbit *longer* by adding more symmetrically-equivalent but logically-distinct clauses. An example of this case is φ_2 and Φ_2 in (6). The quantified predicate representing such an orbit requires $\#(\varphi, \mathbf{s})$ universally-quantified sort variables corresponding to the $\#(\varphi, \mathbf{s})$ sort constants in the clause, and expresses the orbit as an implication whose antecedent is a “distinct” constraint that ensures that the variables cannot be instantiated with identical constants.

B. $\#(\varphi, \mathbf{s}) = |\mathbf{s}|$

When all constants of a sort \mathbf{s} appear in a clause, the above universal quantification yields a predicate with $|\mathbf{s}|$ quantified variables and fails the compactness requirement since the number of quantified variables becomes unbounded as the sort size increases. Correct quantification in this case must be inferred by examining the partition of the sort constants in the clause.

I. Single-cell Partition i.e., $|\pi(\varphi, \mathbf{s})| = 1$ (infer \exists)

When all sort constants appear *identically* in φ , $\pi(\varphi, \mathbf{s})$ is a unit partition. Applying *any* permutation $\gamma \in \text{Sym}(\mathbf{s})$ to φ yields a logically-equivalent clause, i.e., the logical orbit in this case is just a single clause. Increasing the size of sort \mathbf{s} simply yields a *wider* clause and suggests that such an orbit can be encoded as a predicate with a single existentially-quantified variable that ranges over all the sort constants. For example, the partition of the value_3 sort constants in φ_1 from (4) is $\pi(\varphi_1, \text{value}_3) = \{\{\mathbf{v}_1, \mathbf{v}_2, \mathbf{v}_3\}\}$ since all three constants appear identically in φ_1 . The orbit of this clause is just itself and can be encoded as:

$$\Phi_1(\text{value}_3) = \exists X \in \text{value}_3 : \text{vote}(\mathbf{n}_1, X)$$

Also, since $\#(\varphi_1, \text{node}_3) < |\text{node}_3|$, universal quantification (as in Sect. 5.1.A) correctly captures the dependence of the clause’s logical orbit on the node_3 sort to get the overall quantified predicate Φ_1 in (7).

II. Multi-cell Partition i.e., $|\pi(\varphi, \mathbf{s})| > 1$ (infer $\forall\exists$)

In this case, a fixed number of the constants of sort \mathbf{s} appear differently in φ with the remaining constants appearing identically, resulting in a multi-cell partition. Specifically, assume that a number $0 < k < |\mathbf{s}|$ exists that is independent of $|\mathbf{s}|$ such that $\pi(\varphi, \mathbf{s})$ has $k + 1$ cells in which one cell has $|\mathbf{s}| - k$ identically-appearing constants and each of the remaining k cells contains one of the differently-appearing constants. It can be shown that the logical orbit in this case can be expressed by a quantified predicate with k universal quantifiers and a single existential quantifier. For example, the partition of the value_3 constants in the clause:

$$\varphi_4 = \neg \text{decision}(\mathbf{v}_1) \vee \text{decision}(\mathbf{v}_2) \vee \text{decision}(\mathbf{v}_3)$$

is $\pi(\varphi_4, \text{value}_3) = \{\{\mathbf{v}_1\}, \{\mathbf{v}_2, \mathbf{v}_3\}\}$ since \mathbf{v}_1 appears differently from \mathbf{v}_2 and \mathbf{v}_3 . The logical orbit of this clause is:

$$\begin{aligned} \varphi_4^{L(G)} = & [\neg \text{decision}(\mathbf{v}_1) \vee \text{decision}(\mathbf{v}_2) \vee \text{decision}(\mathbf{v}_3)] \wedge \\ & [\neg \text{decision}(\mathbf{v}_2) \vee \text{decision}(\mathbf{v}_1) \vee \text{decision}(\mathbf{v}_3)] \wedge \\ & [\neg \text{decision}(\mathbf{v}_3) \vee \text{decision}(\mathbf{v}_2) \vee \text{decision}(\mathbf{v}_1)] \end{aligned} \quad (9)$$

and can be compactly encoded with an outer universally-quantified variable corresponding to the sort constant in the singleton cell, and an inner existentially-quantified variable corresponding to the other $|\text{value}_3| - 1$ identically-present sort constants. A “distinct” constraint must also be conjoined with the literals involving the existentially-quantified variable to exclude the constant corresponding to the universally-quantified variable from the inner quantification. $\varphi_4^{L(G)}$ can thus be shown to be logically-equivalent to:

$$\Phi_4 = \forall Y \in \text{value}_3, \exists X \in \text{value}_3 : \neg \text{decision}(Y) \vee [(\text{distinct } Y \ X) \wedge \text{decision}(X)] \quad (10)$$

Combining Quantifier Inference for Different Sorts— The complete quantified predicate Φ representing the logical orbit of clause φ can be obtained by applying the above inference procedure to each sort in φ separately and in any order. This is possible since the sorts are assumed to be independent: the constants of one sort do not permute with the constants of a different sort. This will yield a predicate Φ that has the quantified prenex form $\forall^*\exists^* < \text{CNF expression} >$, where all universals for each sort are collected together and precede all the existential quantifiers.

It is interesting to note that this connection between symmetry and quantification suggests that an orbit can be visualized as a two-dimensional object whose height and width correspond, respectively, to the number of universally- and existentially-quantified variables. A proof of the correctness of this quantifier inference procedure can be found in [37].

5.2 Quantifier Inference Beyond $\forall^*\exists^*$

We observed that for some protocols, particularly those that have dependent sorts such as *ToyConsensus*, the above inference procedure violates the compactness requirement. In other words, restricting inference to a $\forall^*\exists^*$ quantifier prefix causes the number of quantifiers to become unbounded as sort sizes increase. Recalling that the $\forall^*\exists^*$ pattern is inferred from the symmetries of a *single* clause, whose literals are the protocol’s state variables, suggests that inference of more complex quantification patterns may necessitate that we examine the structural distribution of sort constants across *sets of clauses*. While this is an interesting possible direction for further exploration of the connection between symmetry and quantification, an alternative approach is to take advantage of the *formula structure* of the protocol’s transition relation. For example, the transition relation of *ToyConsensus* is specified in terms of two quantified sub-formulas, *didNoteVote* and *chosenAt*, that can be viewed, in analogy with a sequential hardware circuit, as internal auxiliary non-state variables that act as “combinational” functions of the state variables. By allowing such auxiliary variables to appear explicitly in clauses learned during incremental induction, the quantified predicates representing the logical orbits of these clauses (according to the basic inference procedure in Sect. 5.1) will *implicitly* incorporate the quantifiers used in the auxiliary variable definitions and automatically have a quantifier prefix that generalizes the basic $\forall^*\exists^*$ template.

Revisiting ToyConsensus— When *SymIC3* is run on the finite instance *ToyConsensus*(3,3,3), it terminates with the following two strengthening assertions:

$$A_1 = \forall N \in \text{node}_3, V_1, V_2 \in \text{value}_3 : (\text{distinct } V_1 \ V_2) \rightarrow \neg \text{vote}(N, V_1) \vee \neg \text{vote}(N, V_2) \quad (11)$$

$$\begin{aligned} A_2 &= \forall V \in \text{value}_3, \exists Q \in \text{quorum}_3. \neg \text{decision}(V) \vee \text{chosenAt}(Q, V) \\ &= \forall V \in \text{value}_3, \exists Q \in \text{quorum}_3. \neg \text{decision}(V) \vee [\forall N \in Q : \text{vote}(N, V)] \end{aligned} \quad (12)$$

which, together with \hat{P} , serve as an inductive invariant proving that \hat{P} holds for this instance. Both assertions are obtained using the basic quantifier inference procedure in Sect. 5.1 that produces a $\forall^*\exists^*$ quantifier prefix in terms of the clause variables. Note, however, that A_2 is expressed in terms of the auxiliary variable *chosenAt*. Substituting the definition of *chosenAt* yields an assertion with a $\forall\exists\forall$ quantifier prefix exclusively in terms of the protocol's state variables.

6 Finite Convergence Checks

Given a safe finite instance $\hat{\mathcal{P}} \triangleq \mathcal{P}(|s_1|, \dots, |s_n|)$, let $Inv_{|s_1|, \dots, |s_n|}$ denote the inductive invariant derived by *SymIC3* to prove that \hat{P} holds in $\hat{\mathcal{P}}$. What remains is to determine the instance size $|s_1|, \dots, |s_n|$ needed so that $Inv_{|s_1|, \dots, |s_n|}$ is also an inductive invariant for all sizes. If the instance size is too small, $\hat{\mathcal{P}}$ may not include all protocol behaviors and $Inv_{|s_1|, \dots, |s_n|}$ will not be inductive at larger sizes. As shown in the invisible invariant approach [8, 9, 58, 64, 69], increasing the instance size becomes necessary to include new protocol behaviors missing in $\hat{\mathcal{P}}$, until protocol behaviors *saturate*. We propose an *automatic* way to update the instance size and reach saturation by starting with an initial *base size* and iteratively increasing the size until *finite convergence* is achieved.

The initial base size can be chosen to be any non-trivial instance size and can be easily determined by a simple analysis of the protocol description. For example, any non-trivial instance of the *ToyConsensus* protocol should have $|node| \geq 3$, $|quorum| \geq 3$, and $|value| \geq 2$.

Our finite convergence procedure can be seen as an integration of symmetry saturation and a stripped-down form of multi-dimensional mathematical induction, and has similarities with previous works on structural induction [34, 47] and proof convergence [24]. To determine if $Inv_{|s_1|, \dots, |s_n|}$ is inductive for any size, the procedure performs the following checks for $1 \leq i \leq n$:

$$a) \text{Init}(|s_1|..|s_i| + 1..|s_n|) \rightarrow Inv_{|s_1|, \dots, |s_n|}(|s_1|..|s_i| + 1..|s_n|) \quad (13)$$

$$b) Inv_{|s_1|, \dots, |s_n|}(|s_1|..|s_i| + 1..|s_n|) \wedge T(|s_1|..|s_i| + 1..|s_n|) \rightarrow Inv'_{|s_1|, \dots, |s_n|}(|s_1|..|s_i| + 1..|s_n|) \quad (14)$$

where $Inv_{|s_1|, \dots, |s_n|}(|s_1|..|s_i| + 1..|s_n|)$ denotes the application of $Inv_{|s_1|, \dots, |s_n|}$ to an instance in which the size of sort s_i is increased by 1 while the sizes of the other sorts are unchanged.³

If all of these checks pass, we can conclude that $Inv_{|s_1|, \dots, |s_n|}$ is not specific to the instance size used to derive it and that we have reached *cutoff*, i.e., that $Inv_{|s_1|, \dots, |s_n|}$ is an inductive invariant for *any* size. Intuitively, this suggests that adding a new protocol component (e.g., client, server, node, proposer, acceptor) does not add any unseen unique behavior, and hence proving safety till the cutoff is sufficient to prove safety for any instance size. While we believe these

³ Sort dependencies, if any, should be considered when increasing a sort size.

checks are sufficient, we still do not have a formal convergence proof. In our implementation, we confirm convergence by performing the unbounded induction checks a) $Init \rightarrow Inv_{|s_1|, \dots, |s_n|}$, and b) $Inv_{|s_1|, \dots, |s_n|} \wedge T \rightarrow Inv'_{|s_1|, \dots, |s_n|}$ noting that they may lie outside the decidable fragment of first-order logic.

On the other hand, failure of these checks, say for sort s_i , implies that $Inv_{|s_1| \dots |s_n|}$ will fail for larger sizes and cannot be inductive in the unbounded case, and we need to repeat *SymIC3* on a finite instance with an increased size for sort s_i , i.e., $\hat{\mathcal{P}}_{new} \triangleq \mathcal{P}(|s_1|, \dots, |s_i| + 1, \dots, |s_n|)$, to include new protocol behaviors that are missing in $\hat{\mathcal{P}}$.

Recall from (11) and (12), running *SymIC3* on *ToyConsensus*(3, 3, 3) produces $Inv_{3,3,3} = A_1 \wedge A_2 \wedge \hat{P}$. $Inv_{3,3,3}$ passes checks (13) and (14) for instances *ToyConsensus*(4, 4, 3) and *ToyConsensus*(3, 3, 4), indicating finite convergence.⁴ $Inv_{3,3,3}$ passes standard induction checks in the unbounded domain as well, establishing it as a proof certificate that proves the property as safe in *ToyConsensus*.

7 IC3PO: IC3 for Proving Protocol Properties

Given a protocol specification \mathcal{P} , IC3PO iteratively invokes *SymIC3* on finite instances of increasing size, starting with a given initial base size. Upon termination, IC3PO either a) reaches convergence on an inductive invariant $Inv_{|s_1|, \dots, |s_n|}$ that proves P for the unbounded protocol \mathcal{P} , or b) produces a counterexample trace $Cex_{|s_1|, \dots, |s_n|}$ that serves as a finite witness to its violation in both the finite instance and the unbounded protocol. The detailed pseudo code of IC3PO is available in [37].

We also explored a number of simple enhancements to IC3PO, such as strengthening the inferred quantified predicates whenever safely possible to do during incremental induction by a) dropping the “distinct” antecedent, and b) rearranging the quantifiers if the strengthened predicate is still unreachable from the previous frame. We describe these enhancements in the extended version of the paper [37]. The results presented in this paper were obtained without these enhancements.

Implementation— Our implementation of IC3PO is publicly available at <https://github.com/aman-goel/ic3po>. The implementation accepts protocol descriptions in the Ivy language [63] and uses the Ivy compiler to extract a quantified, logical formulation \mathcal{P} in a customized VMT [21] format. We use a modified version [4] of the pySMT [33] library to implement our prototype, and use the Z3 [23] solver for all SMT queries. We use the SMT-LIB [13] theory of free sorts and function symbols with datatypes and quantifiers (UFDT), which allows formulating SMT queries for both, the finite and the unbounded domains. For a safe protocol, the inductive proof is printed in the Ivy format as an *independently check-able* proof certificate, which can be further validated with the Ivy verifier.

⁴ Since **quorum** is a dependent sort on **node**, it is increased together with the **node** sort.

8 Evaluation

We evaluated IC3PO on a total of 29 distributed protocols including 4 problems from [53], 13 from [46], and 12 from [2]. This evaluation set includes fairly complex models of consensus algorithms as well as protocols such as two-phase commit, chord ring, hybrid reliable broadcast, etc. Several studies [15, 31, 42, 46, 53, 63] have indicated the challenges involved in verifying these protocols.

All 29 protocols are safe based on manual verification. Even though finding counterexample traces is equally important, we limit our evaluation to safe protocols where the property holds, since inferring inductive invariants is the main bottleneck of existing techniques for verifying distributed protocols [29, 30, 63].

We compared IC3PO against the following 3 verifiers that implement state-of-the-art IC3-style techniques for automatic verification of distributed protocols:

- I4 [53] performs finite-domain IC3 (without accounting for symmetry) using the AVR model checker [39], followed by iteratively generalizing and checking the inductive invariant produced by AVR using Ivy.
- UPDR is the implementation of the PDR^\forall /UPDR algorithm [44] for verifying distributed protocols, from the *mypyvy* [3] framework.
- fol-ic3 [46] is a recent technique implemented in *mypyvy* that extends IC3 with the ability to infer inductive invariants with quantifier alternations.

All experiments were performed on an Intel (R) Xeon CPU (X5670). For each run, we used a timeout of 1 h and a memory limit of 32 GB. All tools were executed in their respective default configurations. We used Z3 [23] version 4.8.9, Yices 2 [25] version 2.6.2, and CVC4 [12] version 1.7.

8.1 Results

Table 2 summarizes the experimental results. Apart from the number of problems solved, we compared the tools on 3 metrics: run time in seconds, proof size measured by the number of assertions in the inductive invariant for the unbounded protocol, and the total number of SMT queries made. Each tool uses SMT queries differently (e.g., I4 uses **QF_UF** for finite, **UF** for unbounded). Comparing the number of SMT queries still helps in understanding the run time behavior.

IC3PO solved all 29 problems, while 10 protocols were solved by all the tools. The 5 rows at the bottom of Table 2 provide a summary of the comparison. Overall, compared to the other tools IC3PO is faster, requires fewer SMT queries, and produces shorter inductive proofs even for problems requiring inductive invariants with quantifier alternations (marked with \AE in Table 2).

We did a more extensive comparison between the two finite-domain incremental induction verifiers IC3PO and I4, performed a statistical analysis using multiple runs with different solver seeds to account for the effect of randomness in

Table 2. Comparison of IC3PO against other state-of-the-art verifiers, Time: run time (seconds), Inv: # assertions in inductive proof, SMT: # SMT queries, Column “info” provides information on the strengthening assertions (i.e., A) in IC3PO’s inductive proof: \mathbb{A} indicates A has quantifier alternations, \triangleq means A has definitions, and \sqsubset means A adds quantifier-alternation cycles

Protocol (#29)	<i>Human</i>		IC3PO			I4			UPDR			fol-ic3		
	Inv	info	Time	Inv	SMT	Time	Inv	SMT	Time	Inv	SMT	Time	Inv	SMT
tla-consensus	1		0	1	17	4	1	7	0	1	38	1	1	29
tla-tcommit	3		1	2	31	unknown		71	1	3	214	2	3	162
i4-lock-server	2		1	2	37	2	2	35	1	2	133	1	2	66
ex-quorum-leader-election	3		3	5	129	32	14	15429	11	3	1007	24	8	1078
pyv-toy-consensus-forall	4		3	4	105	unknown		5949	10	3	590	11	5	587
tla-simple	8		6	3	285	4	3	1319	timeout			timeout		
ex-lockserv-automaton	2		7	12	594	3	15	1731	21	9	3855	10	12	1181
tla-simpleregular	9		8	4	346	unknown		14787	timeout			57	9	314
pyv-sharded-kv	5		10	8	590	4	15	2101	6	7	784	22	10	522
pyv-lockserv	9		11	12	702	3	15	1606	14	9	3108	8	11	1044
tla-twophase	12		14	10	984	unknown		10505	67	14	12031	9	12	1635
i4-learning-switch	8		14	9	589	22	11	26345	timeout			timeout		
ex-simple-decentralized-lock	5		19	15	2219	14	22	5561	4	2	677	4	8	291
i4-two-phase-commit	11		27	11	2541	4	16	4045	16	9	2799	8	9	1083
pyv-consensus-wo-decide	5		50	9	1886	1144	42	41137	100	4	8563	168	26	5692
pyv-consensus-forall	7		99	10	3445	1006	44	156838	490	6	24947	2461	27	16182
pyv-learning-switch	8		127	13	3388	387	49	51021	278	11	3210	timeout		
i4-chord-ring-maintenance	18		229	12	6418	timeout			timeout			timeout		
pyv-sharded-kv-no-lost-keys	2	\mathbb{A}	3	2	57	unknown		1232	unknown		73	3	2	51
ex-naive-consensus	4	\mathbb{A}	6	4	239	unknown		15141	unknown		1325	73	18	414
pyv-client-server-ae	2	$\mathbb{A} \triangleq$	2	2	49	unknown		1483	unknown		132	877	15	700
ex-simple-election	3	$\mathbb{A} \triangleq$	7	4	268	unknown		2747	unknown		1147	32	10	222
pyv-toy-consensus-epr	4	$\mathbb{A} \triangleq$	9	4	370	unknown		5944	unknown		473	70	14	217
ex-toy-consensus	3	$\mathbb{A} \triangleq$	10	3	209	unknown		2797	unknown		348	21	8	124
pyv-client-server-db-ae	5	$\mathbb{A} \triangleq$	17	6	868	unknown		81509	unknown		422	timeout		
pyv-hybrid-reliable-broadcast	8	$\mathbb{A} \triangleq$	587	4	1474	unknown		34764	unknown		713	1360	23	3387
pyv-firewall	2	$\mathbb{A} \sqsubset$	2	3	131	unknown		344	unknown		130	7	8	116
ex-majorityset-leader-election	5	$\mathbb{A} \sqsubset$	72	7	1552	error			unknown		2350	timeout		
pyv-consensus-epr	7	$\mathbb{A} \triangleq \sqsubset$	1300	9	29601	unknown		177189	unknown		7559	1468	30	3355
No. of problems solved (out of 29)			29			13			14			23		
Uniquely solved			3			0			0			0		
For 10 cases solved by all: \sum Time			232			2221			667			2711		
\sum Inv			85			186			52			114		
\sum SMT			12160			228490			45911			27168		

SMT solving, compared the inductive proofs produced by IC3PO against human-written invariants, and performed a preliminary exploration of distributed protocols with totally-ordered domains and ring topologies. Due to space constraints, we describe these experiments in the extended version of the paper [37].

8.2 Discussion

Comparing IC3PO and I4 clearly reveals the benefits of symmetric incremental induction. For example, I4 requires 7814 SMT queries to eliminate 443 CTIs when solving *ToyConsensus*(3,3,3), compared to 192 SMT calls and 13 CTIs for IC3PO. Even though both techniques perform finite incremental induction, symmetry-aware clause boosting in IC3PO leads to a factorial reduction in the number of SMT queries and yields compact inductive proofs.

Comparing IC3PO and UPDR reveals the benefits of finite-domain reasoning methods compared to direct unbounded verification. Even in cases where existential quantifier inference isn't necessary, symmetry-aware finite-domain reasoning gives IC3PO an edge both in terms of run time and the number of SMT queries.

Comparing IC3PO and fol-ic3, the only two verifiers that can infer invariants with a combination of universal and existential quantifiers, highlights the advantage of IC3PO's approach over the separators-based technique [46] used in fol-ic3. The significant performance edge that IC3PO has over fol-ic3 is due to the fact that a) reasoning in IC3PO is primarily in a (small) finite domain compared to fol-ic3's unbounded reasoning, and b) unlike fol-ic3 which enumeratively searches for specific quantifier patterns, IC3PO finds the required invariants without search by automatically inferring their patterns from the symmetry of the protocol.

Overall, the evaluation confirms the main hypothesis of this paper, that it is possible to use the relationship between symmetry and quantification to scale the verification of distributed protocols beyond the current state-of-the-art.

9 Related Work

Introduced by Lamport, TLA+ is a widely-used language for the specification and verification of distributed protocols [14, 59]. The accompanying TLC model checker can perform automatic verification on a finite instance of a TLA+ specification, and can also be configured to employ symmetry to improve scalability. However, TLC is primarily intended as a debugging tool for small finite instances and not as a tool for inferring inductive invariants.

Several manual or semi-automatic verification techniques (e.g., using interactive theorem proving or compositional verification) have been proposed for deriving system-level proofs [20, 35, 42, 43, 62, 68]. These techniques generally require a deep understanding of the protocol being verified and significant manual effort to guide proof development. The Ivy [63] system improves on these techniques by graphically displaying CTIs and interactively asking the user to provide strengthening assertions that can eliminate them.

Verification of parameterized systems using SMT solvers is further explored in MCMT [66], Cubicle [22], and paraVerifier [52]. Abdulla et al. [6] proposed *view abstraction* to compute the reachable set for finite instances using forward reachability until cutoff is reached. Our technique builds on these works with the capability to automatically infer the required quantified inductive invariant using the latest advancements in model checking, by combining symmetry-aware clause

learning and quantifier inference in finite-domain incremental induction. The use of derived/ghost variables has been recognized as important in [48, 58, 61]. IC3PO utilizes protocol structure, namely auxiliary definitions in the protocol specification, to automatically infer inductive invariants with complex quantifier alternations.

Several recent approaches (e.g., UPDR [45], QUIC3 [41], Phase-UPDR [31], fol-ic3 [46]) extend IC3/PDR to automatically infer quantified inductive invariants. Unlike IC3PO, these techniques rely heavily on unbounded SMT solving.

Our work is closest in spirit to FORHULL-N [24] and I4 [53, 54]. Similar to IC3PO, these techniques perform incremental induction over small finite instances of a parameterized system and employ a generalization procedure that transforms finite-domain proofs to quantified inductive invariants that hold for all parameter values. Dooley and Somenzi proposed FORHULL-N to verify parameterized reactive systems by running bit-level IC3 and generalizing the learnt clauses into candidate universally-quantified proofs through a process of proof saturation and convex hull computation. These candidate proofs involve modular linear arithmetic constraints as antecedents in a way such that they approximate the protocol behavior beyond the current finite instance, and their correctness is validated by checking them until the cutoff is reached. I4 uses an ad hoc generalization procedure to obtain universally-quantified proofs from the finite-domain inductive invariants generated by the AVR model checker [39].

10 Conclusions and Future Work

IC3PO is, to our knowledge, the first verification system that uses the synergistic relationship between symmetry and quantification to automatically infer the quantified inductive invariants required to prove the safety of symmetric protocols. Recognizing that symmetry and quantification are alternative ways of capturing invariance, IC3PO extends the incremental induction algorithm to learn clause orbits, and encodes these orbits with corresponding logically-equivalent and compact quantified predicates. IC3PO employs a systematic procedure to check for finite convergence, and outputs quantified inductive invariants, with both universal and existential quantifiers, that hold for all protocol parameters. Our evaluation demonstrates that IC3PO is a significant improvement over the current state-of-the-art.

Future work includes exploring methods to utilize the regularity in totally-ordered domains during reachability analysis, investigating techniques to counter undecidability in practical distributed systems verification, and exploring enhancements to further improve the scalability to complex distributed protocols and their implementations. As a long-term goal, we aim towards automatically inferring inductive invariants for complicated distributed protocols, such as Paxos [50, 51], by building further on this initial work.

Data Availability Statement and Acknowledgments

The software and data sets generated and analyzed during the current study, including all experimental data, evaluation scripts, and IC3PO source code are

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References

1. Client server protocol in ivy. http://microsoft.github.io/ivy/examples/client_server_example.html
2. A collection of distributed protocol verification problems. <https://github.com/aman-goel/ivybench>
3. mypyvy (github). <https://github.com/wilcoxjay/mypyvy>
4. pySMT: A library for SMT formulae manipulation and solving. <https://github.com/aman-goel/pysmt>
5. Toy consensus protocol. https://github.com/microsoft/ivy/blob/master/examples/ivy/toy_consensus.ivy
6. Abdulla, P., Haziza, F., Holík, L.: Parameterized verification through view abstraction. *Int. J. Softw. Tools Technol. Transfer* **18**(5), 495–516 (2016)
7. Apt, K.R., Kozen, D.: Limits for automatic verification of finite-state concurrent systems. *Inf. Process. Lett.* **22**(6), 307–309 (1986)
8. Arons, T., Pnueli, A., Ruah, S., Xu, Y., Zuck, L.: Parameterized verification with automatically computed inductive assertions? In: Berry, G., Comon, H., Finkel, A. (eds.) CAV 2001. LNCS, vol. 2102, pp. 221–234. Springer, Heidelberg (2001). https://doi.org/10.1007/3-540-44585-4_19
9. Balaban, I., Fang, Y., Pnueli, A., Zuck, L.D.: IIV: an invisible invariant verifier. In: Etessami, K., Rajamani, S.K. (eds.) CAV 2005. LNCS, vol. 3576, pp. 408–412. Springer, Heidelberg (2005). https://doi.org/10.1007/11513988_39
10. Balyo, T., Froleys, N., Heule, M.J., Iser, M., Järvisalo, M., Suda, M.: Proceedings of SAT Competition 2020: Solver and Benchmark Descriptions (2020)
11. Barner, S., Grumberg, O.: Combining symmetry reduction and under-approximation for symbolic model checking. In: Brinksma, E., Larsen, K.G. (eds.) CAV 2002. LNCS, vol. 2404, pp. 93–106. Springer, Heidelberg (2002). https://doi.org/10.1007/3-540-45657-0_8
12. Barrett, C., et al.: CVC4. In: Gopalakrishnan, G., Qadeer, S. (eds.) CAV 2011. LNCS, vol. 6806, pp. 171–177. Springer, Heidelberg (2011). https://doi.org/10.1007/978-3-642-22110-1_14
13. Barrett, C., Fontaine, P., Tinelli, C.: The Satisfiability Modulo Theories Library (SMT-LIB). www.SMT-LIB.org (2016)
14. Beers, R.: Pre-RTL formal verification: an intel experience. In: Proceedings of the 45th Annual Design Automation Conference, pp. 806–811 (2008)
15. Berkovits, I., Lazic, M., Losa, G., Padon, O., Shoham, S.: Verification of threshold-based distributed algorithms by decomposition to decidable logics. *CoRR abs/1905.07805* (2019). <http://arxiv.org/abs/1905.07805>
16. Bloem, R.: Decidability of parameterized verification. *Synth. Lect. Distrib. Comput. Theory* **6**(1), 1–170 (2015). <https://doi.org/10.2200/S00658ED1V01Y201508DCT013>
17. Bradley, A.R.: SAT-based model checking without unrolling. In: Jhala, R., Schmidt, D. (eds.) VMCAI 2011. LNCS, vol. 6538, pp. 70–87. Springer, Heidelberg (2011). https://doi.org/10.1007/978-3-642-18275-4_7

18. Burch, J.R., Clarke, E.M., McMillan, K.L., Dill, D.L., Hwang, L.J.: Symbolic model checking: 10^{20} states and beyond. In: Proceedings Fifth Annual IEEE Symposium on Logic in Computer Science, pp. 428–439 (1990)
19. Burch, J.R., Clarke, E.M., McMillan, K.L., Dill, D.L., Hwang, L.J.: Symbolic model checking: 10^{20} states and beyond. *Inf. Comput.* **98**(2), 142–170 (1992)
20. Chaudhuri, K., Doligez, D., Lamport, L., Merz, S.: Verifying safety properties with the TLA^+ proof system. In: Giesl, J., Hähnle, R. (eds.) *IJCAR 2010*. LNCS (LNAI), vol. 6173, pp. 142–148. Springer, Heidelberg (2010). https://doi.org/10.1007/978-3-642-14203-1_12
21. Cimatti, A., Roveri, M., Griggio, A., Irfan, A.: Verification Modulo Theories (2011). <http://www.vmt-lib.org>
22. Conchon, S., Goel, A., Krstić, S., Mebsout, A., Zaïdi, F.: Cubicle: a parallel SMT-based model checker for parameterized systems. In: Madhusudan, P., Seshia, S.A. (eds.) *CAV 2012*. LNCS, vol. 7358, pp. 718–724. Springer, Heidelberg (2012). https://doi.org/10.1007/978-3-642-31424-7_55
23. de Moura, L., Bjørner, N.: Z3: an efficient SMT solver. In: Ramakrishnan, C.R., Rehof, J. (eds.) *TACAS 2008*. LNCS, vol. 4963, pp. 337–340. Springer, Heidelberg (2008). https://doi.org/10.1007/978-3-540-78800-3_24
24. Dooley, M., Somenzi, F.: Proving parameterized systems safe by generalizing clausal proofs of small instances. In: Chaudhuri, S., Farzan, A. (eds.) *CAV 2016*. LNCS, vol. 9779, pp. 292–309. Springer, Cham (2016). https://doi.org/10.1007/978-3-319-41528-4_16
25. Dutertre, B.: Yices 2.2. In: Biere, A., Bloem, R. (eds.) *CAV 2014*. LNCS, vol. 8559, pp. 737–744. Springer, Cham (2014). https://doi.org/10.1007/978-3-319-08867-9_49
26. Een, N., Mishchenko, A., Brayton, R.: Efficient implementation of property directed reachability. In: *Formal Methods in Computer Aided Design (FMCAD 2011)*, pp. 125–134, October 2011
27. Eén, N., Sörensson, N.: An extensible SAT-solver. In: Giunchiglia, E., Tacchella, A. (eds.) *SAT 2003*. LNCS, vol. 2919, pp. 502–518. Springer, Heidelberg (2004). https://doi.org/10.1007/978-3-540-24605-3_37
28. Emerson, E.A., Sistla, A.P.: Symmetry and model checking. *Formal Methods Syst. Des.* **9**(1–2), 105–131 (1996)
29. Feldman, Y.M.Y., Sagiv, M., Shoham, S., Wilcox, J.R.: Learning the boundary of inductive invariants. *CoRR abs/2008.09909* (2020). <https://arxiv.org/abs/2008.09909>
30. Feldman, Y.M., Immerman, N., Sagiv, M., Shoham, S.: Complexity and information in invariant inference. In: *Proceedings of the ACM on Programming Languages*, vol. 4, no. POPL, pp. 1–29 (2019)
31. Feldman, Y.M.Y., Wilcox, J.R., Shoham, S., Sagiv, M.: Inferring inductive invariants from phase structures. In: Dillig, I., Tasiran, S. (eds.) *CAV 2019*. LNCS, vol. 11562, pp. 405–425. Springer, Cham (2019). https://doi.org/10.1007/978-3-030-25543-5_23
32. Fraleigh, J.B.: *A First Course in Abstract Algebra*, 6th edn. Addison Wesley Longman, Reading (2000)
33. Gario, M., Micheli, A.: PySMT: a solver-agnostic library for fast prototyping of SMT-based algorithms. In: *SMT Workshop*, vol. 2015 (2015)
34. German, S.M., Sistla, A.P.: Reasoning about systems with many processes. *J. ACM (JACM)* **39**(3), 675–735 (1992)

35. Gleissenthall, K.v., Kıcı, R.G., Bakst, A., Stefan, D., Jhala, R.: Pretend synchrony: synchronous verification of asynchronous distributed programs. In: *Proceedings of the ACM on Programming Languages*, vol. 3, no. POPL, pp. 1–30 (2019)
36. Godefroid, P.: Exploiting symmetry when model-checking software. In: Wu, J., Chanson, S.T., Gao, Q. (eds.) *Formal Methods for Protocol Engineering and Distributed Systems*. IAICT, vol. 28, pp. 257–275. Springer, Boston, MA (1999). https://doi.org/10.1007/978-0-387-35578-8_15
37. Goel, A., Sakallah, K.A.: On Symmetry and Quantification: A New Approach to Verify Distributed Protocols. CoRR. abs/2103.14831 (2021). <https://arxiv.org/abs/2103.14831>
38. Goel, A., Sakallah, K.: Model checking of Verilog RTL using IC3 with syntax-guided abstraction. In: Badger, J.M., Rozier, K.Y. (eds.) *NFM 2019*. LNCS, vol. 11460, pp. 166–185. Springer, Cham (2019). https://doi.org/10.1007/978-3-030-20652-9_11
39. Goel, A., Sakallah, K.: AVR: abstractly verifying reachability. TACAS 2020. LNCS, vol. 12078, pp. 413–422. Springer, Cham (2020). https://doi.org/10.1007/978-3-030-45190-5_23
40. Goel, A., Sakallah, K.A.: Empirical evaluation of IC3-based model checking techniques on Verilog RTL designs. In: *Proceedings of the Design, Automation and Test in Europe Conference (DATE)*, Florence, Italy, March 2019, pp. 618–621 (2019)
41. Gurfinkel, A., Shoham, S., Vizel, Y.: Quantifiers on demand. In: Lahiri, S.K., Wang, C. (eds.) *ATVA 2018*. LNCS, vol. 11138, pp. 248–266. Springer, Cham (2018). https://doi.org/10.1007/978-3-030-01090-4_15
42. Hawblitzel, C., et al.: IronFleet: proving practical distributed systems correct. In: *Proceedings of the 25th Symposium on Operating Systems Principles*, pp. 1–17. ACM (2015)
43. Hoenicke, J., Majumdar, R., Podelski, A.: Thread modularity at many levels: a pearl in compositional verification. *ACM SIGPLAN Not.* **52**(1), 473–485 (2017)
44. Karbyshev, A., Bjørner, N., Itzhaky, S., Rinetzky, N., Shoham, S.: Property-directed inference of universal invariants or proving their absence. *J. ACM* **64**(1) (2017). <https://doi.org/10.1145/3022187>
45. Karbyshev, A., Bjørner, N., Itzhaky, S., Rinetzky, N., Shoham, S.: Property-directed inference of universal invariants or proving their absence. *J. ACM (JACM)* **64**(1), 1–33 (2017)
46. Koenig, J.R., Padon, O., Immerman, N., Aiken, A.: First-order quantified separators. In: *Proceedings of the 41st ACM SIGPLAN Conference on Programming Language Design and Implementation, PLDI 2020*, pp. 703–717. Association for Computing Machinery, New York (2020). <https://doi.org/10.1145/3385412.3386018>
47. Kurshan, R.P., McMillan, K.: A structural induction theorem for processes. In: *Proceedings of the Eighth Annual ACM Symposium on Principles of Distributed Computing*, pp. 239–247 (1989)
48. Lamport, L.: Proving the correctness of multiprocess programs. *IEEE Trans. Softw. Eng.* **2**, 125–143 (1977)
49. Lamport, L.: *Specifying Systems: The TLA+ Language and Tools for Hardware and Software Engineers*. Addison-Wesley Longman Publishing Co., Inc., Boston (2002)
50. Lamport, L.: The part-time parliament. In: *Concurrency: The Works of Leslie Lamport*, pp. 277–317 (2019)
51. Lamport, L., et al.: Paxos made simple. *ACM Sigact News* **32**(4), 18–25 (2001)

52. Li, Y., Pang, J., Lv, Y., Fan, D., Cao, S., Duan, K.: *ParaVerifier: an automatic framework for proving parameterized cache coherence protocols*. In: Finkbeiner, B., Pu, G., Zhang, L. (eds.) *ATVA 2015*. LNCS, vol. 9364, pp. 207–213. Springer, Cham (2015). https://doi.org/10.1007/978-3-319-24953-7_15
53. Ma, H., Goel, A., Jeannin, J.B., Kapritsos, M., Kasikci, B., Sakallah, K.A.: *I4: incremental inference of inductive invariants for verification of distributed protocols*. In: *Proceedings of the 27th Symposium on Operating Systems Principles*. ACM (2019)
54. Ma, H., Goel, A., Jeannin, J.B., Kapritsos, M., Kasikci, B., Sakallah, K.A.: *Towards automatic inference of inductive invariants*. In: *Proceedings of the Workshop on Hot Topics in Operating Systems*, pp. 30–36. ACM (2019)
55. Marques-Silva, J.P., Sakallah, K.A.: *GRASP: a search algorithm for propositional satisfiability*. *IEEE Trans. Comput.* **48**(5), 506–521 (1999)
56. McMillan, K.L.: *Symbolic Model Checking*. Kluwer Academic Publishers, Norwell (1993)
57. Moskewicz, M.W., Madigan, C.F., Zhao, Y., Zhang, L., Malik, S.: *Chaff: engineering an efficient SAT solver*. In: *DAC*, pp. 530–535 (2001)
58. Namjoshi, K.S.: *Symmetry and completeness in the analysis of parameterized systems*. In: Cook, B., Podelski, A. (eds.) *VMCAI 2007*. LNCS, vol. 4349, pp. 299–313. Springer, Heidelberg (2007). https://doi.org/10.1007/978-3-540-69738-1_22
59. Newcombe, C., Rath, T., Zhang, F., Munteanu, B., Brooker, M., Deardeuff, M.: *How amazon web services uses formal methods*. *Commun. ACM* **58**(4), 66–73 (2015)
60. Ip, C.N., Dill, D.L.: *Better verification through symmetry*. *Formal Methods Syst. Des.* **9**(1), 41–75 (1996). <https://doi.org/10.1007/BF00625968>
61. Owicki, S., Gries, D.: *Verifying properties of parallel programs: an axiomatic approach*. *Commun. ACM* **19**(5), 279–285 (1976)
62. Owre, S., Rushby, J.M., Shankar, N.: *PVS: a prototype verification system*. In: Kapur, D. (ed.) *CADE 1992*. LNCS, vol. 607, pp. 748–752. Springer, Heidelberg (1992). https://doi.org/10.1007/3-540-55602-8_217
63. Padon, O., McMillan, K.L., Panda, A., Sagiv, M., Shoham, S.: *Ivy: safety verification by interactive generalization*. In: *Proceedings of the 37th ACM SIGPLAN Conference on Programming Language Design and Implementation, PLDI 2016*, pp. 614–630. ACM, New York (2016). <https://doi.org/10.1145/2908080.2908118>
64. Pnueli, A., Ruah, S., Zuck, L.: *Automatic deductive verification with invisible invariants*. In: Margaria, T., Yi, W. (eds.) *TACAS 2001*. LNCS, vol. 2031, pp. 82–97. Springer, Heidelberg (2001). https://doi.org/10.1007/3-540-45319-9_7
65. Pong, F., Dubois, M.: *A new approach for the verification of cache coherence protocols*. *IEEE Trans. Parallel Distrib. Syst.* **6**(8), 773–787 (1995)
66. Ranise, S., Ghilardi, S.: *Backward reachability of array-based systems by SMT solving: termination and invariant synthesis*. *Logical Methods Comput. Sci.* **6**(4) (2010). [https://doi.org/10.2168/LMCS-6\(4:10\)2010](https://doi.org/10.2168/LMCS-6(4:10)2010)
67. Sistla, A.P., Gyrus, V., Emerson, E.A.: *SMC: a symmetry-based model checker for verification of safety and liveness properties*. *ACM Trans. Softw. Eng. Methodol.* (TOSEM) **9**(2), 133–166 (2000)
68. Wilcox, J.R., et al.: *Verdi: a framework for implementing and formally verifying distributed systems*. In: *Proceedings of the 36th ACM SIGPLAN Conference on Programming Language Design and Implementation, PLDI 2015*, pp. 357–368. ACM, New York (2015). <https://doi.org/10.1145/2737924.2737958>
69. Zuck, L., Pnueli, A.: *Model checking and abstraction to the aid of parameterized systems (a survey)*. *Comput. Lang. Syst. Struct.* **30**(3–4), 139–169 (2004)