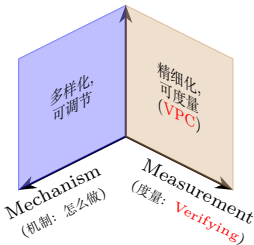


Semantics

(模型: **Pipelined-RAM**)



R/W REGISTER