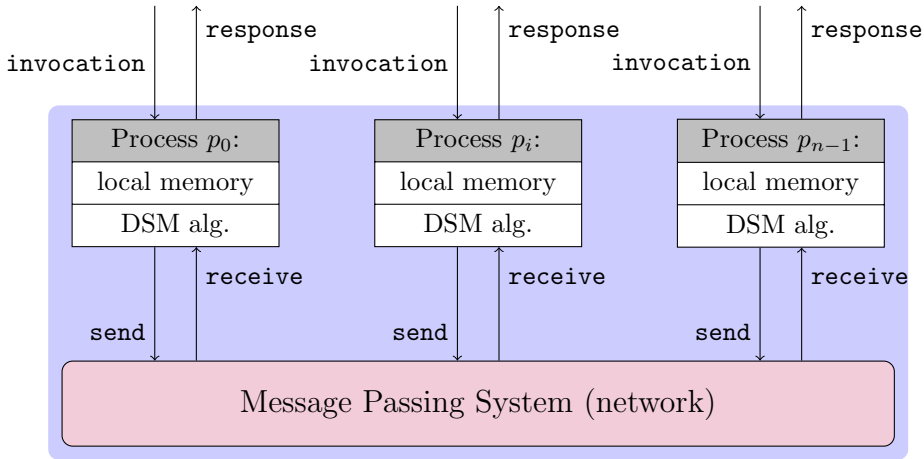


read/write operations on shared registers/variables



Distributed Shared Memory (shared registers/variables)