

MODULE <i>CC</i>	
EXTENDS <i>TLC</i> , <i>Sequences</i>	
VARIABLES	<i>Keys</i> ,    the set of keys <i>Values</i> the set of values
<i>InitVal</i>	$\triangleq$ CHOOSE $v : v \notin \text{Values}$
<i>Operation</i>	$\triangleq$ [ $op : \{\text{"read"}, \text{"write"}\}, key : \text{Keys}, val : \text{Values}$ ]
<i>R</i> ( $k, v$ )	$\triangleq$ [ $op \mapsto \text{"read"}, key \mapsto k, val \mapsto v$ ]
<i>W</i> ( $k, v$ )	$\triangleq$ [ $op \mapsto \text{"write"}, key \mapsto k, val \mapsto v$ ]
<i>Session</i>	$\triangleq$ <i>Seq</i> ( <i>Operation</i> )
<i>History</i>	$\triangleq$ SUBSET <i>Session</i>
Sequential semantics of read-write registers	
<i>Ops</i> ( $h$ )	$\triangleq$ Get all operations of history $h \in \text{History}$ FALSE
<i>CCv</i> ( $h$ )	$\triangleq$ Check whether $h \in \text{History}$ satisfies <i>CCv</i> (Causal Convergence)
$\wedge$ LET $ops \triangleq Ops(h)$ IN $\wedge \exists co \in \text{SUBSET } (ops \times ops) :$ $\exists arb \in \text{SUBSET } (ops \times ops) :$ $\forall op \in ops : \text{TRUE}$	
$\wedge$ FALSE	