

# COS

# Computersystemer

Lektion #5

# Pointgivende aktivitet.

Den 1. oktober 2021 kl.: 12:15-13:00 afholder vi den 1. pointgivende aktivitet i jeres klasselokale. Det er vigtigt, at du er klar kl.: 12:15, da du ellers ikke kan deltage i prøven.

- Aktiviteten er *ikke* obligatorisk, men ved at deltage kan du score op til 5 point, som du kan lægge oven i din præstation til eksamen.
- Hvis du ikke deltager i den pointgivende aktivitet, kan du **ikke** få et andet forsøg. Dette gælder også i tilfælde af evt. Sygdom.
- Aktiviteten udføres som en *Multiple Choice Test* under *itslearning*.
- De udleverede pdf-dokumenter med *ASCII-tabel*, *Boolsk Algebra*, *Logic Gates* og *Tal repræsentationer*, *slides fra de fagets lektioner*, *lærebogen* samt egne noter, må benyttes under testen. Udover kladdepapir og blyant, er andre hjælpemidler **ikke** tilladt.
- Instruktorerne vil agere "eksamens vagter".

# Pointgivende aktivitet.

Den pointgivende test bliver tilgængelig kl.: 12:15 på:  
"itslearning/resourcer/pointgivende aktivitet/pointgivende aktivitet 1"

- Vær på plads til tiden.
- I har 45 minutter til testen.
- Sørg for at have tisset af inden starten, så du undgår at forstyrre de andre. (Testen varer kun 45 min.)
- Lad Venligst lad vær med at snakke og larme, hvis du bliver færdig før de 45 min. er gået. Bliv siddende stille på din plads af hensyn til dem, som ikke er færdige.

# De logiske operationer er "bit-wise"!

01010101  
AND 11110000  
-----  
01010000

01010101  
OR 11110000  
-----  
11110101

01010101  
XOR 11110000  
-----  
10100101

# Bitwise Problems as Python Code

```
print(bin(0b10011010 & 0b11001001))
```

```
# Prints '0b10001000'
```

AND

```
print(bin(0b10011010 | 0b11001001))
```

```
# Prints '0b11011011'
```

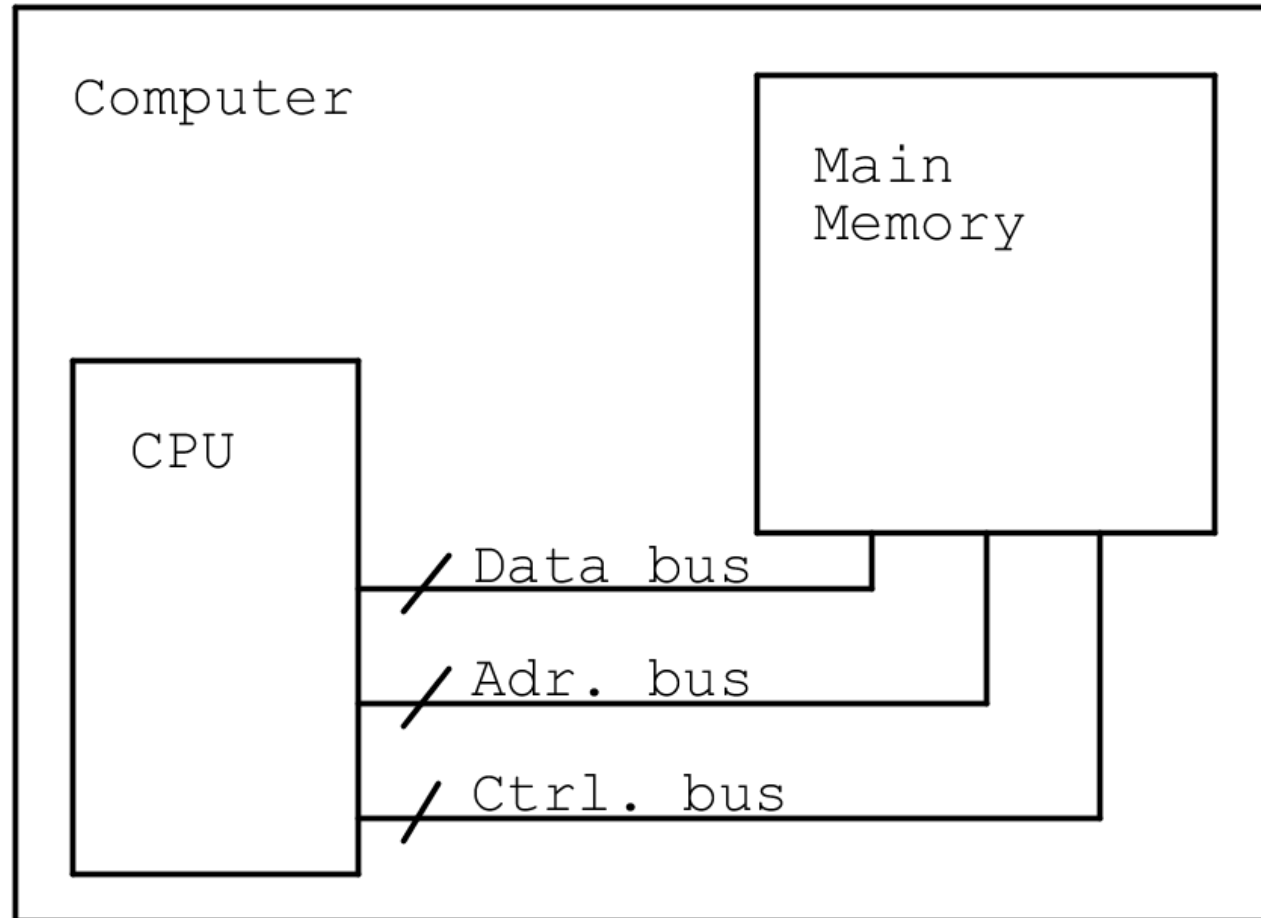
OR

```
print(bin(0b10011010 ^ 0b11001001))
```

```
# Prints '0b1010011'
```

XOR

# Computerens arkitektur.



# Main memory.

1111 1111 1111 1111b

FFFFH

Main memory

64 kB

Main memory kan indeholde:

- Data i forskellige repræsentationer.
- Maskininstruktioner (Program).

0000 0000 0000 0000b

0000H

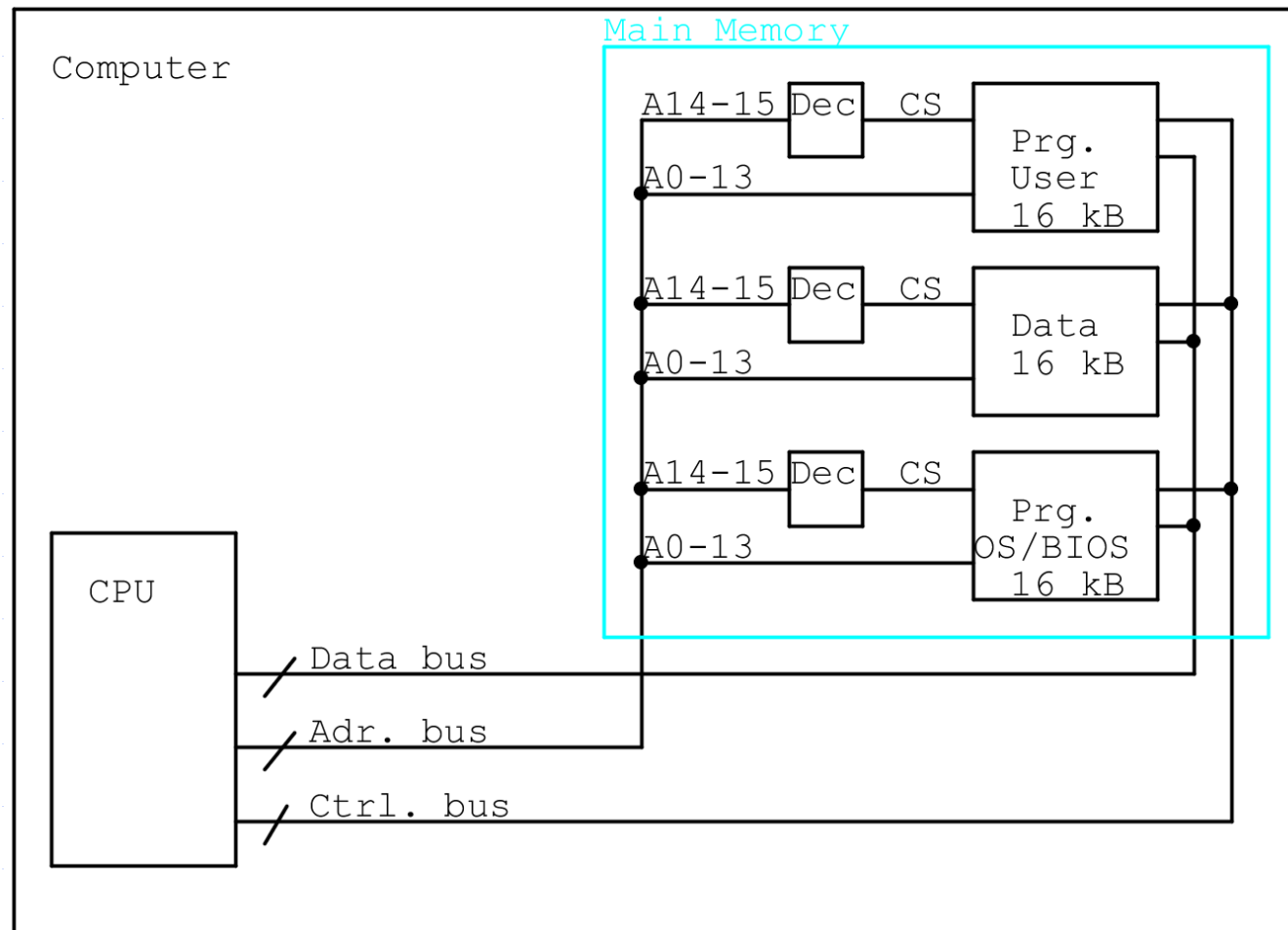
# Memory Map.

**Main memory 64 kB.**

1111	1111	1111	1111b	FFFFh	Prog. User 16 kB
1100	0000	0000	0000b	C000h	ROM/RAM
1011	1111	1111	1111b	BFFFh	Res. 16 kB
1000	0000	0000	0000b	8000h	
0111	1111	1111	1111b	7FFFh	Data 16 kB
0100	0000	0000	0000b	4000h	RAM
0011	1111	1111	1111b	3FFFh	Prog. OS/BIOS 16 kB
0000	0000	0000	0000b	0000h	ROM

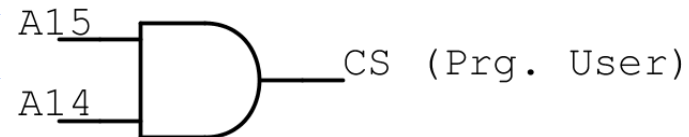


# Adress decoding

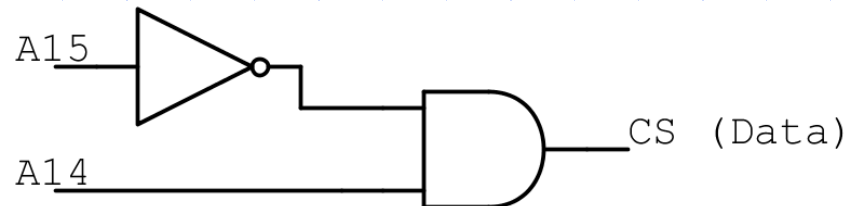


# Adress decoder

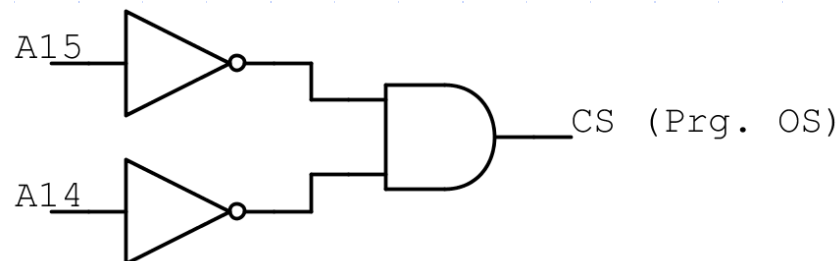
Adress decoder Prg. User: Adr.: 11xx xxxx xxxx xxxxb, A15,A14 = 1,1



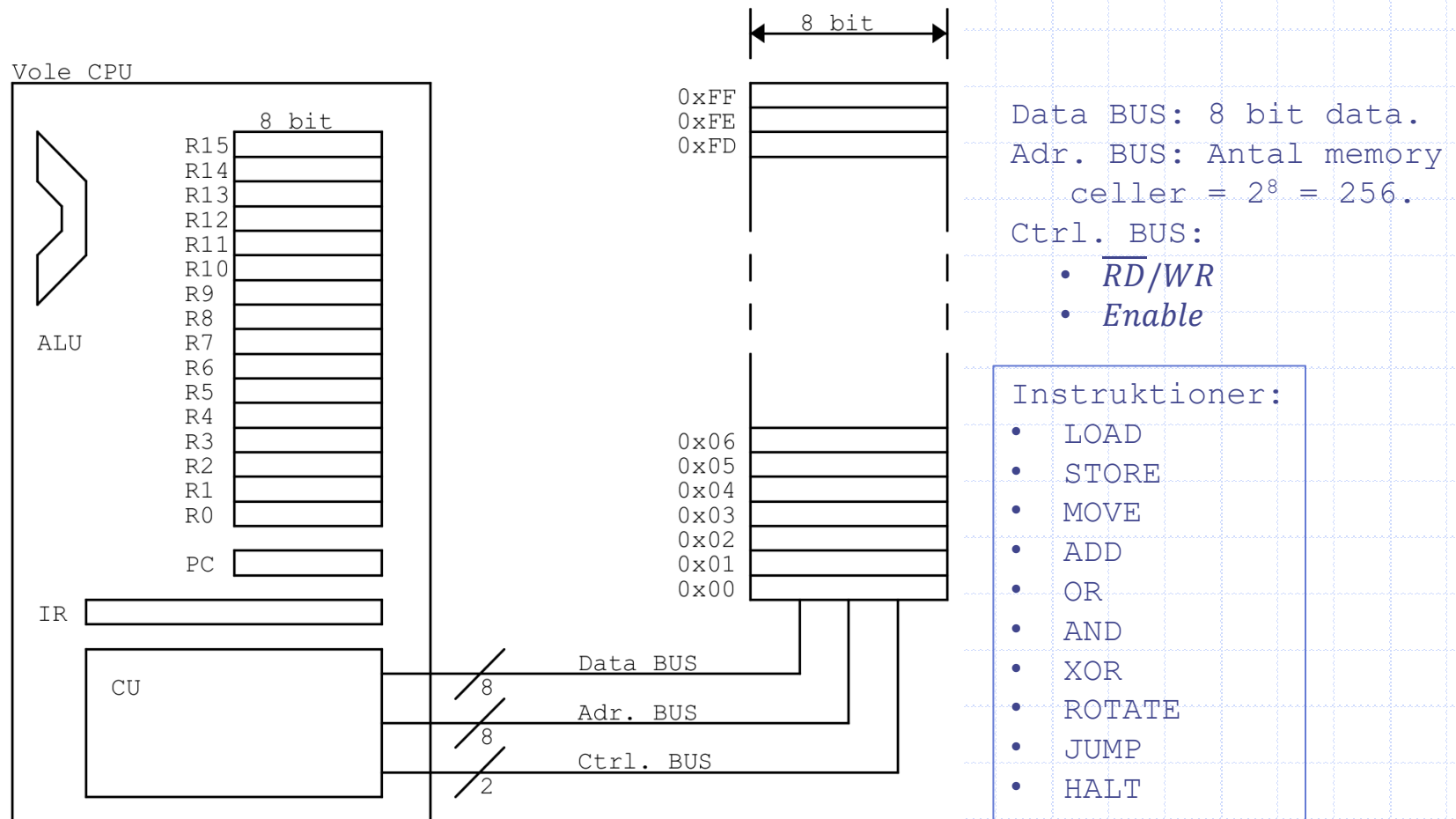
Adress decoder Data: Adr.: 01xx xxxx xxxx xxxxb, A15,A14 = 0,1



Adress decoder Prg. OS/BIOS: Adr.: 11xx xxxx xxxx xxxxb, A15,A14 = 0,0



# VOLE i Appendix C



# Vores Vole computer (simulator).

Ingen runtime input/output

Input:  
Læg værdier i  
memory

Run

Output:  
Aflæs værdier i  
memory

Simple Computer

Data Input Window

[40] 15 16 17 18

CPU

R0	00
R1	00
R2	00
R3	00
R4	00
R5	00
R6	00
R7	PC 00
R8	IR 0000
R9	00
...	...
RC	00
RD	00
RE	00
RF	00

Main Memory

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
1	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
2	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
3	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
4	15	16	17	18	00	00	00	00	00	00	00	00	00	00	00	00
5	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
6	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
7	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
8	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
9	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
A	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
B	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
C	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
D	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
E	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
F	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00

Clear Memory Load Data Run Single Step Halt Help

# Input / Output

```
# Calculates the hypotenuse of a right triangle
import math

# Inputting the side lengths, first try
sideA = int(input('Length of side A? '))
sideB = int(input('Length of side B? '))

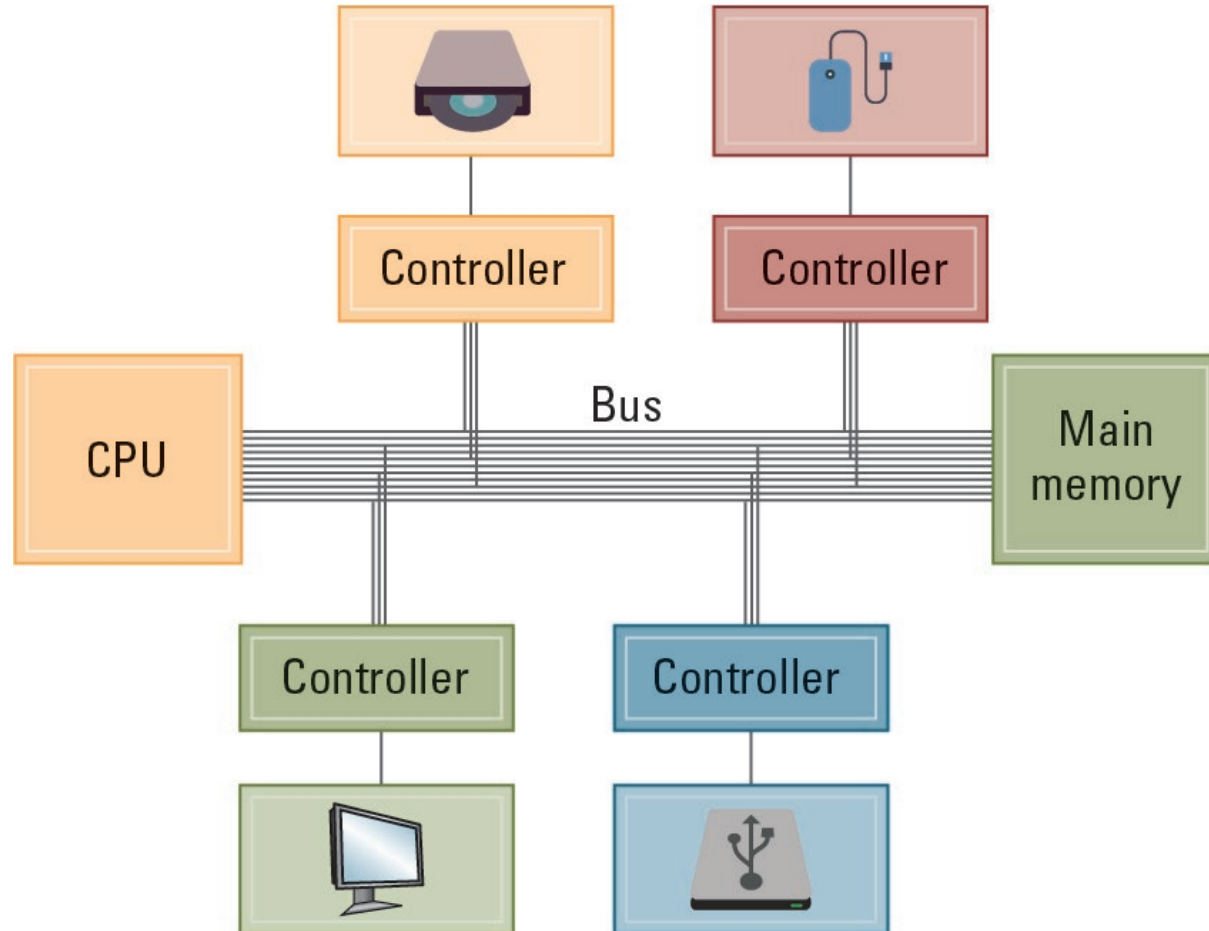
# Calculate third side via Pythagorean Theorem
hypotenuse = math.sqrt(sideA**2 + sideB**2)

print(hypotenuse)
```

## 2.5 Communicating with Other Devices

- **Controller:** handles communication between the computer and other devices
  - Specialized (by type of device)
  - General purpose (USB, HDMI)
- **Port:** The point at which a device connects to a computer
- **Memory-mapped I/O:** devices appear to the CPU as though they were memory locations

## Figure 2.13 Controllers attached to a machine's bus



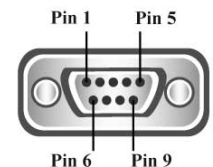
# Eksempler på ydre enheder

- Lagerenheder
  - HD
  - CD/DVD
  - Memory sticks
- HMI (Human Machine Interface)
  - Mus
  - Tastatur
  - Game controllers
- Multimediale
  - Skærme
  - Højtalere
  - VR
- Datakommunikation
  - RS232 Serial Port
  - Internet
    - Ethernet
    - Wifi
    - Mobildata
  - Bluetooth
  - USB

## RS232

Pin 1	DCD
Pin 2	RXD
Pin 3	TXD
Pin 4	DTR
Pin 5	GND
Pin 6	DSR
Pin 7	RTS
Pin 8	CTS
Pin 9	RI

RS232 Pinout (9 Pin Male)





# Eksempler på USB enheder

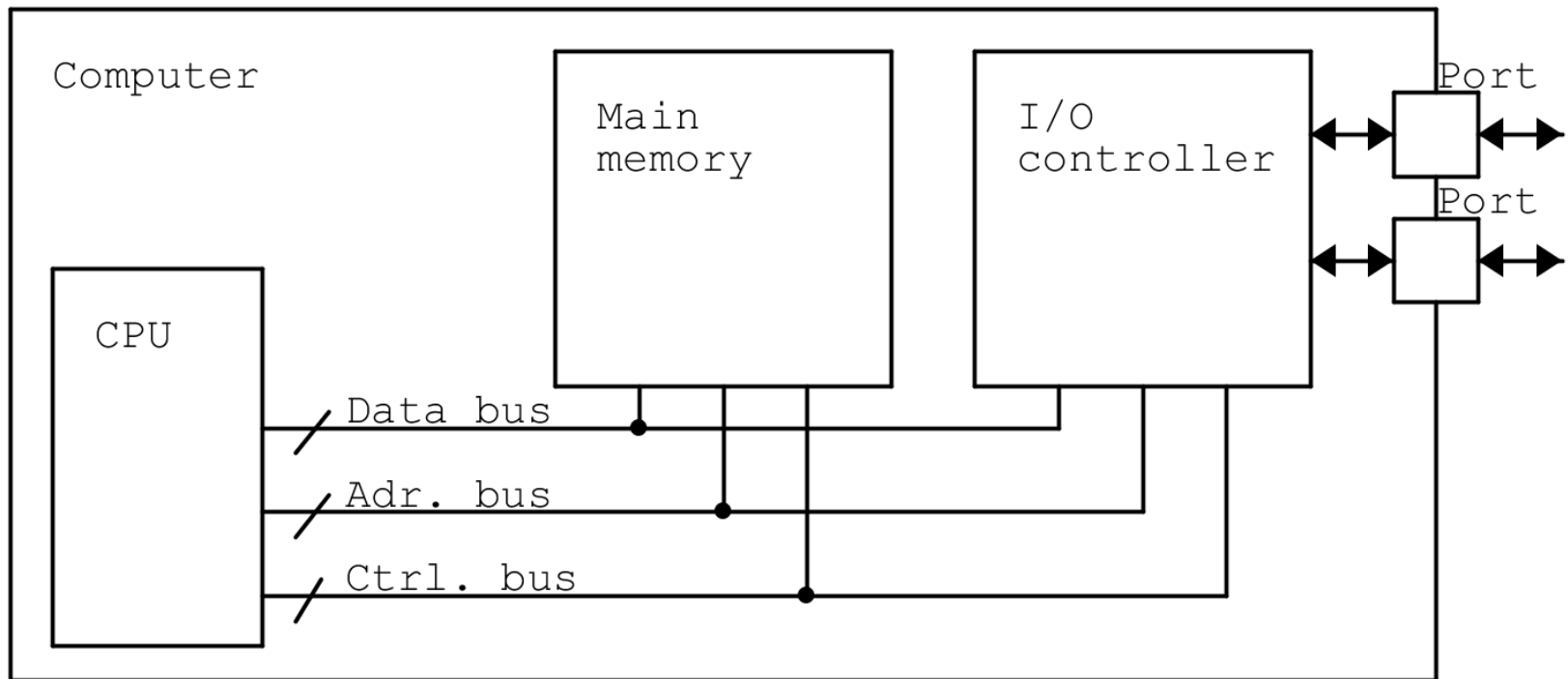
- Mus.
- Tastaturer.
- Printere.
- Scannere.
- Eksterne diske.
- Flash drives.
- Digitale kameraer.
- Højtalere.
- Head sets.
- Smart watches.
- Smart phones.
- Serial port.

USB (Universal Serial Bus):

- Plug-And-Play
- Op til 127 enheder pr. port



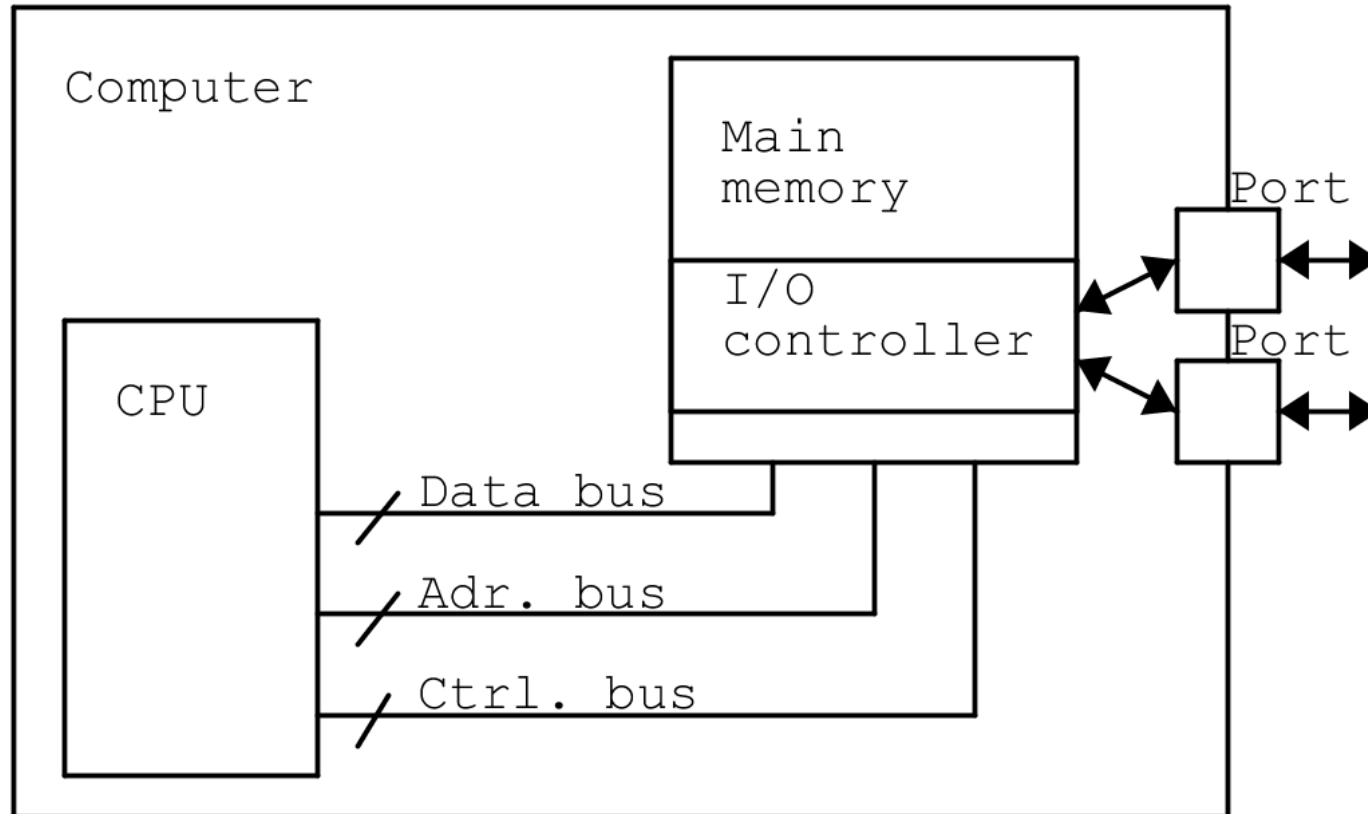
# Computer med I/O. (I/O mapped I/O).



## Control BUS:

- 1 bit til angivelse af READ eller WRITE.
- 1 bit til at angive timing i dataoverførslen.
- 1 bit til angivelse af Main memory eller I/O.

# Memory mapped I/O



# Memory mapped I/O. Memory Map.

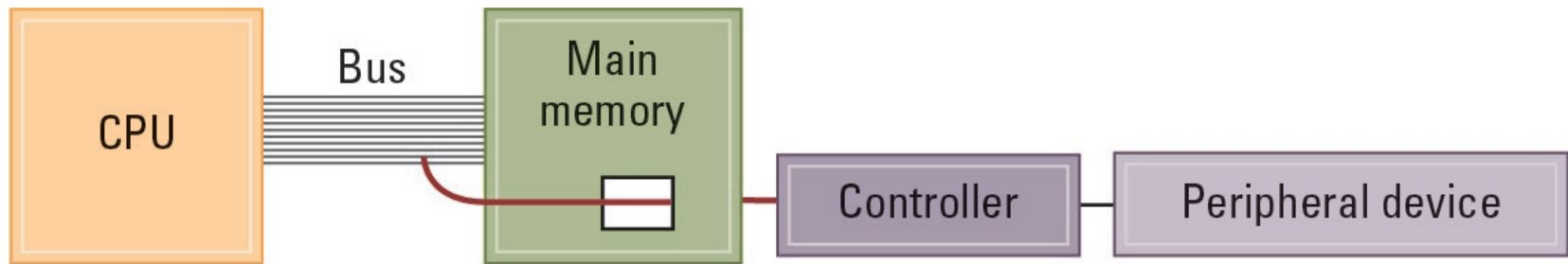
**Main memory 64 kB.**

A15, A14, A13 =  
1,0,0 →

1111	1111	1111	1111b	FFFFh
1100	0000	0000	0000b	C000h
1011	1111	1111	1111b	BFFFh
1010	0000	0000	0000b	A000h
1001	1111	1111	1111b	9FFFh
1000	0000	0000	0000b	8000h
0111	1111	1111	1111b	7FFFh
0100	0000	0000	0000b	4000h
0011	1111	1111	1111b	3FFFh
0000	0000	0000	0000b	0000h

Prog. User 16 kB ROM/RAM
Res. 8 kB
I/O 8 kB
Data 16 kB RAM
Prog. OS/BIOS 16 kB ROM

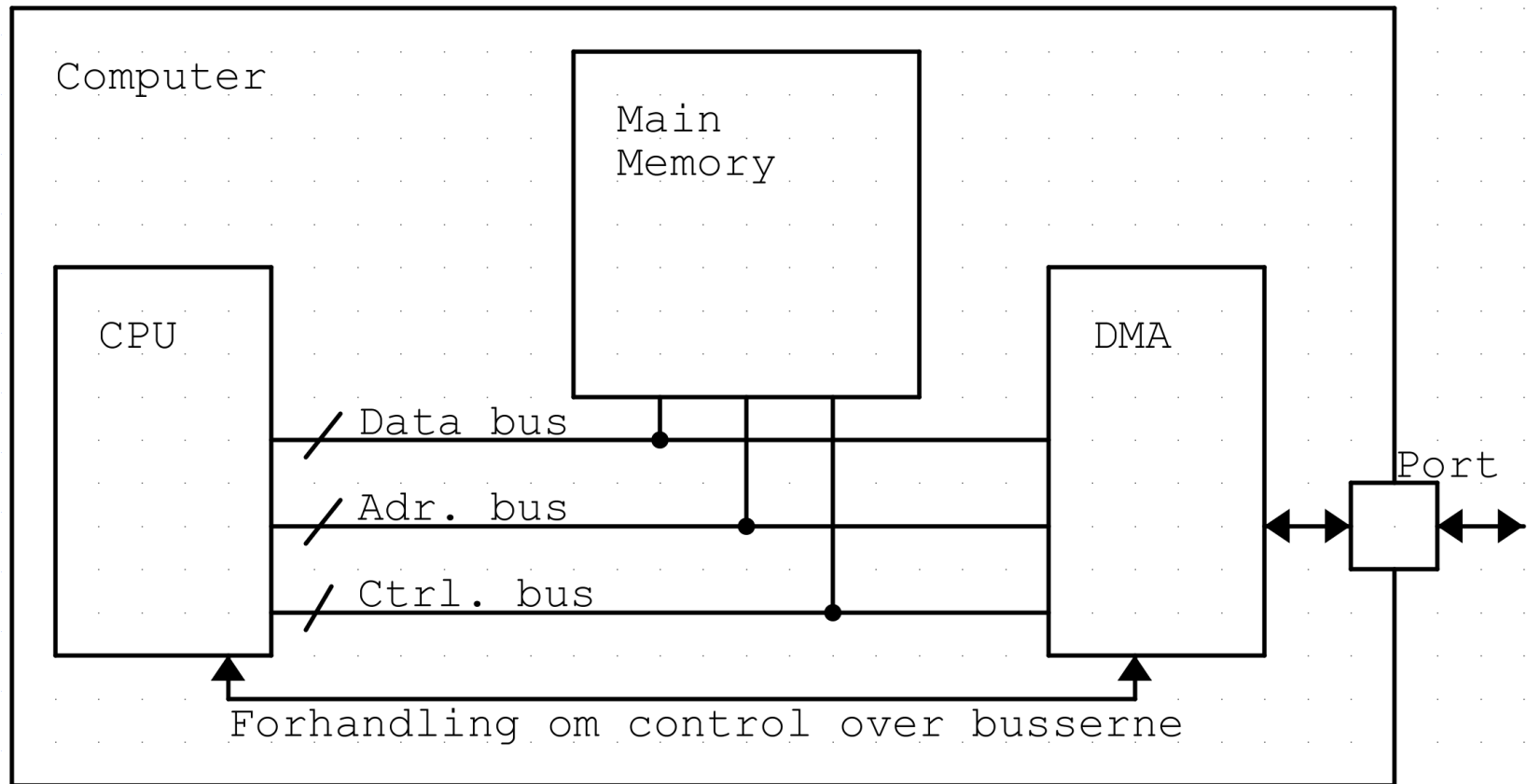
## Figure 2.14 A conceptual representation of memory-mapped I/O



# Communicating with Other Devices (continued)

- **Direct memory access (DMA):** Main memory access by a controller over the bus
  - **Von Neumann Bottleneck:** occurs when the CPU and controllers compete for bus access

# DMA



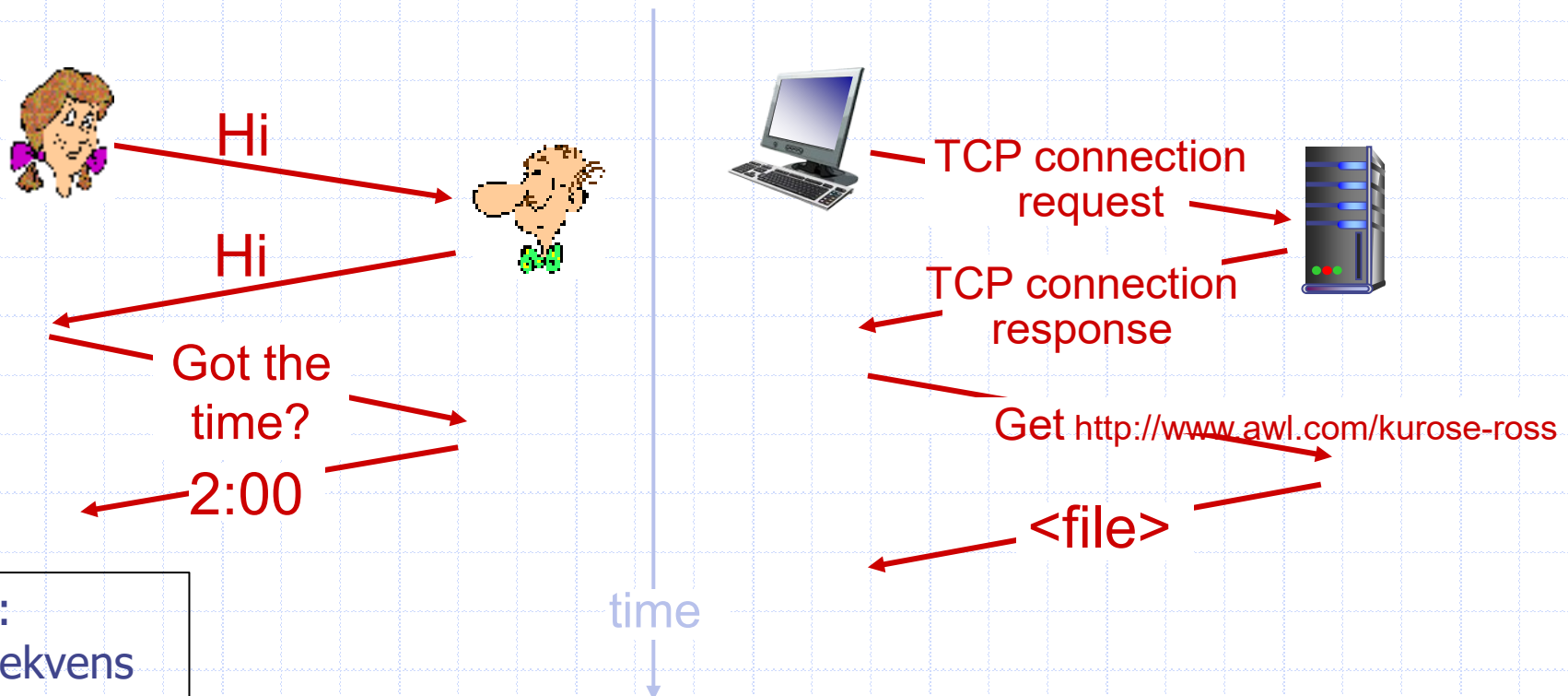
# Handshaking

- **Handshaking** er koordinering af dataoverførsel mellem to eller flere enheder (software eller hardware)
- **Handshaking** er overholdelse af en protocol.
- **Aftal en “Protokol”.**
  - Computerens kommunikation med ydre enheder.
  - Multiprocess systemer
  - Distribuerede systemer
  - Netværk
  - Internet
  - Interface
  - API
  - Standard eller proprietær.



# What's a protocol?

a human protocol and a computer network protocol:



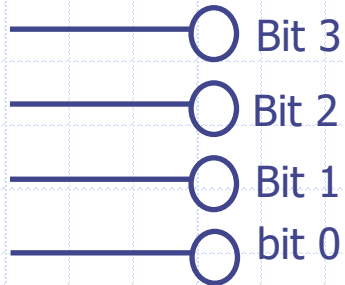
Aftal:

- Sekvens
- Sprog
- Format
- Hastighed
- ...andet

# Communicating with Other Devices (continued)

- **Popular Communication Media**
  - **Parallel Communication:** Several signals transferred at the same time, each on a separate “line” (computer’s internal bus)
  - **Serial Communication:** Signals are transferred one after the other over a single “line” (USB, FireWire)

# Parallel / Serial kommunikation



Bit 3 = 0V = '0'  
Bit 2 = 5V = '1'  
Bit 1 = 5V = '1'  
Bit 0 = 0V = '0'

0 1 1 0

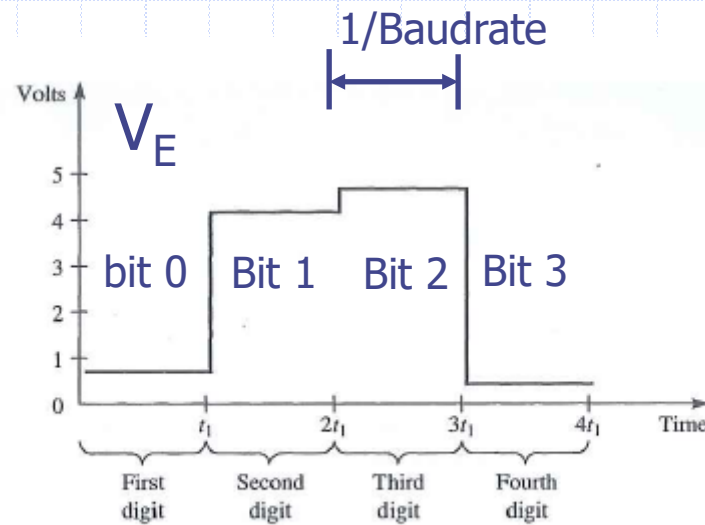
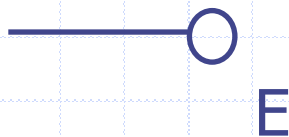


FIGURE 11.2

0 1 1 0

# I/O standarder.

- RS-232 Serial port.
- USB (Universal Serial Bus).
- HDMI
- VGA
- Ethernet
- Analog audio

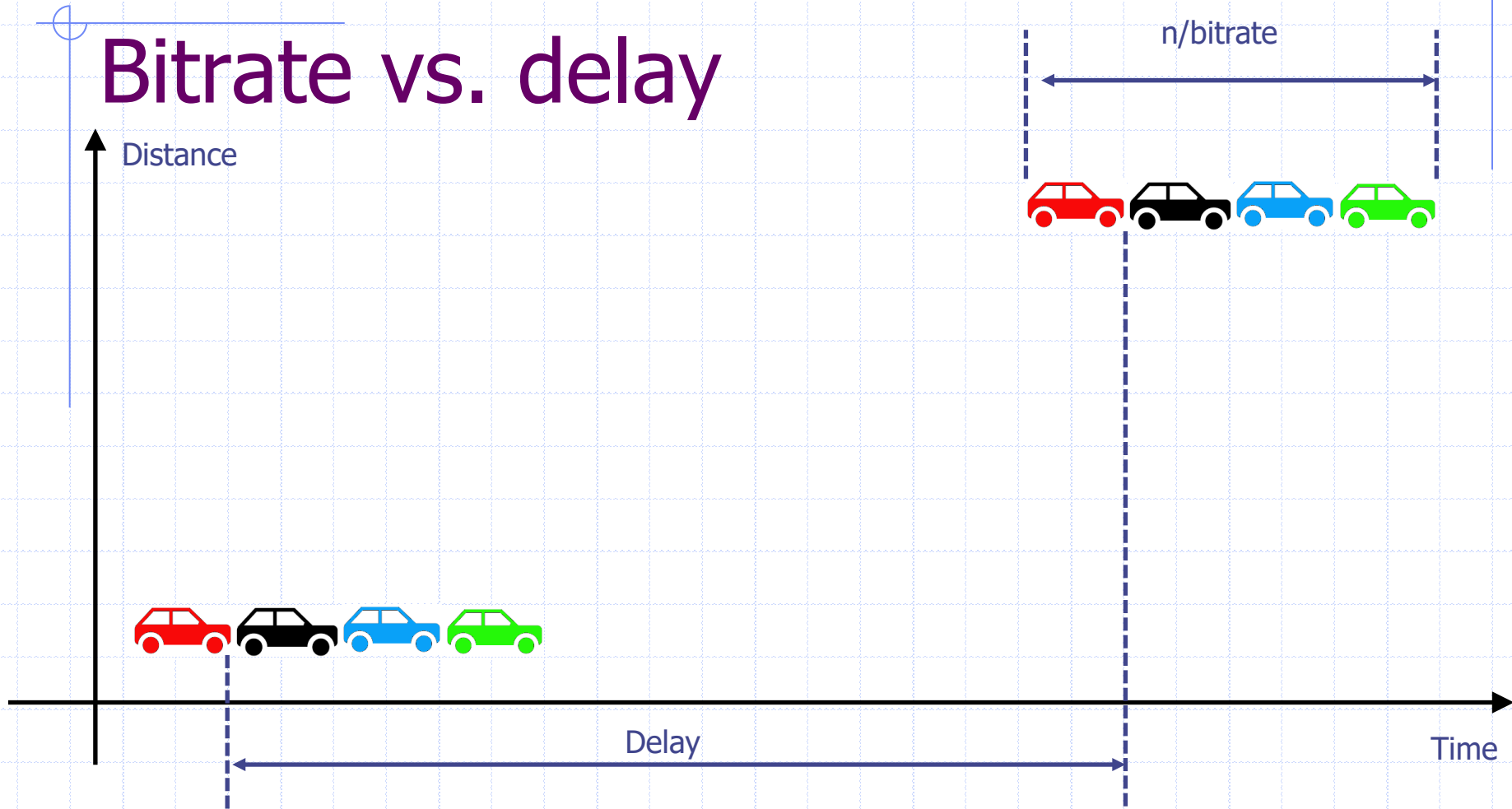
# Data Communication Rates

- Measurement units
  - bps: bits per second
  - Kbps: Kilo-bps (1,000 bps)
  - Mbps: Mega-bps (1,000,000 bps)
  - Gbps: Giga-bps (1,000,000,000 bps)
- Bandwidth: Maximum available rate

# Kommunikations hastighed

- Communication rate [bps][Baud-rate]
- Bandwidth [bps]
- Delay [s]

# Communication rate Bitrate vs. delay



## 2.7 Other Architectures

- Technologies to increase throughput:
  - Pipelining: Overlap steps of the machine cycle
  - Parallel Processing: Use multiple processors simultaneously
    - SISD: Single Instruction, Single Data
      - No parallel processing
    - MIMD: Multiple Instruction, Multiple Data
      - Different programs, different data
    - SIMD: Single Instruction, Multiple Data
      - Same program, different data



Spørgsmål?