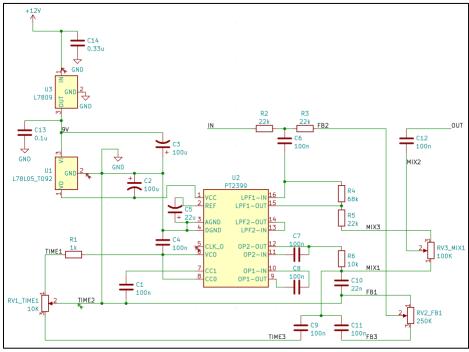
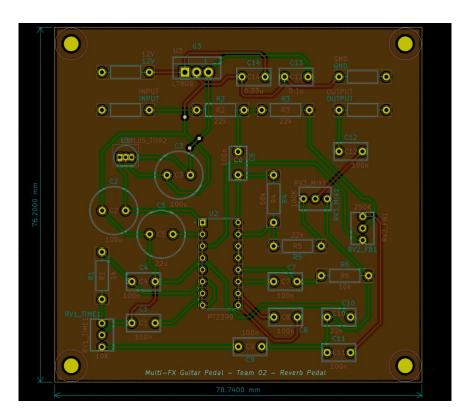
ECE 49022 - Team 02 (Multi-FX Guitar Pedal) - Reverb Pedal - Michael Scirocco

a) Schematic:

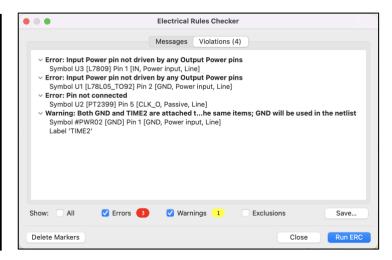


- b) (Please see attached Gerber files)
- c) PCB:



d) Proof of DRC/ERC checks:

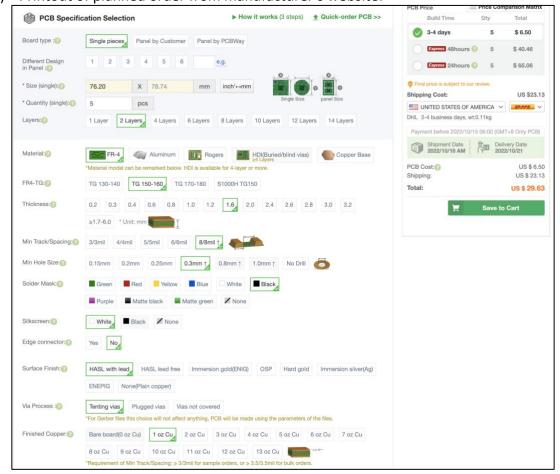




ERC Comments: The first two errors are the schematic not recognizing the +12V symbol as a power source. The third error is for a pin that does not need to be connected.

e) Centroid file: N/A, we are soldering the components in ourselves.

f) Printout of planned order from manufacturer's website:



g)	Complete and final bill of materials (BOM):		
Ο,			
•	Resistors:		
	0	1kOhm – 1x	
	0	10kOhm – 1x	
	0		
	0	68kOhm – 1x	
•	Capacitors:		
	0	100n – 8x	
		100u – 2x	
		22u – 1x	
		22n – 1x	
		0.1u - 1x	
		0.33u – 1x	
•	Potentiometers:		
		10k – 1x	
		250k – 1x	
	0	100k – 1x	
•	Other:		
	0	78L05 (3-Terminal Voltage Regulator) – 1x	
	0	L7809 (Linear Voltage Regulator) – 1x	
	0	PT2399 (CMOS Echo/Delay Processor) – 1x	
h)	Responses to rubric questions:		
		What manufacturer do you plan to use:	PCBWay
		What is the lead + shipping time:	8 business days
		What quantity do you intend to order:	5
		Manufacturer specifications	
		Maximum number of layers:	10
		Surface finish:	HASL with lead
		Copper weight:	1 oz Cu
		Minimum solder mask clearance:	0 mm
		Minimum drill hole size:	0.2 mm
		Minimum trace width:	0.1mm/4mil
		Minimum trace spacing:	0.1mm/4mil
		Minimum via to trace spacing:	0.15 mm
		Blind / Buried vias: both	
		Vac. / Na	

Have you configured your tools DRC based on these specifications:

<u>Yes</u> / No

Does your PCB require a stencil to populate:

Yes / No

If so, do you have a plan to order one:

Yes / No

All components fit on PCB printed 1:1 scale:

<u>Yes</u> / No

Checked for and corrected issues from appendix 1 checklist:

<u>Yes</u> / No

Your team name and number is clearly visible in the silkscreen:

<u>Yes</u> / No