1. Description

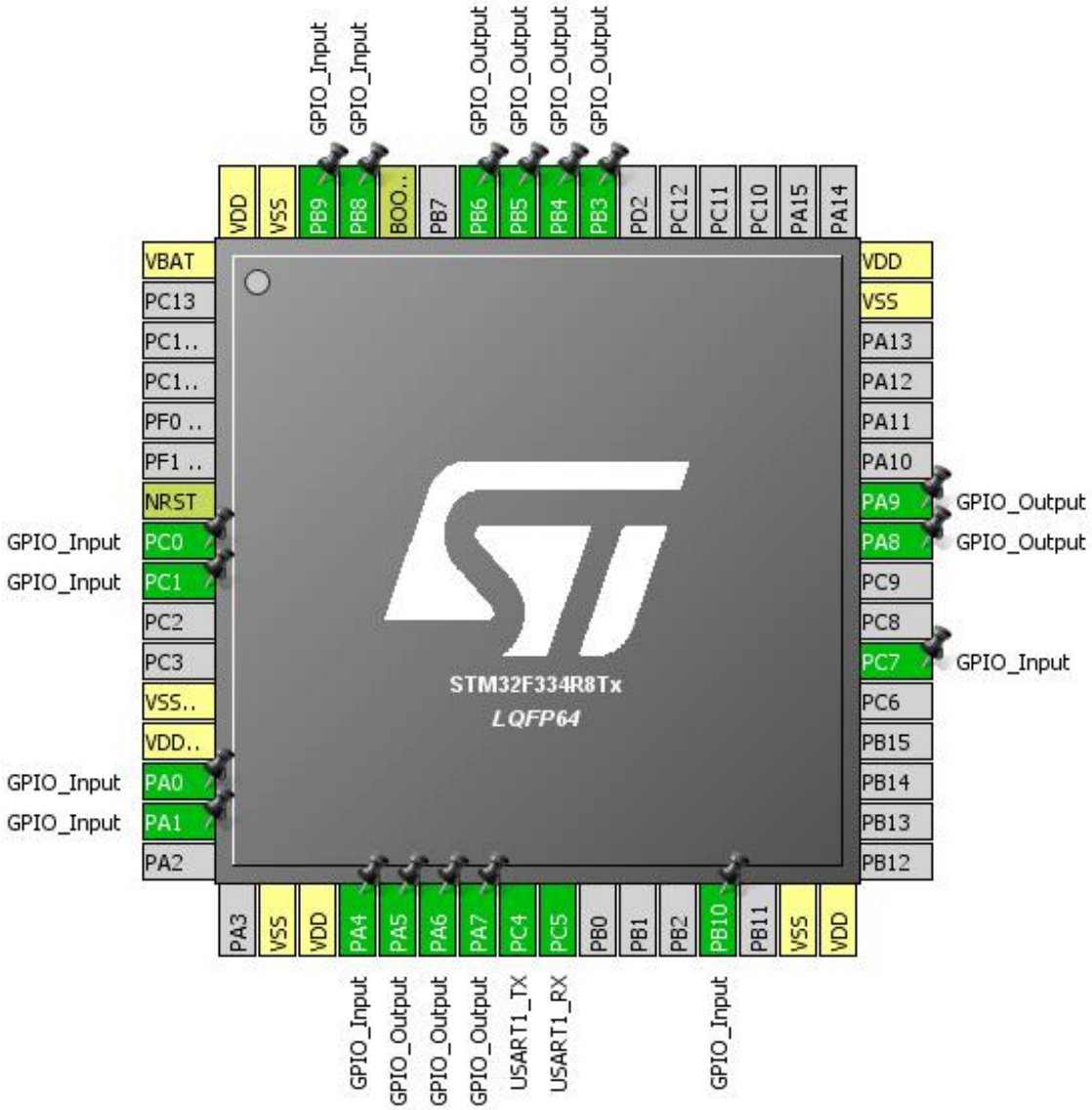
1.1. Project

Project Name	edesign_latest
Board Name	edesign_latest
Generated with:	STM32CubeMX 4.24.0
Date	03/02/2018

1.2. MCU

MCU Series	STM32F3
MCU Line	STM32F334
MCU name	STM32F334R8Tx
MCU Package	LQFP64
MCU Pin number	64

2. Pinout Configuration

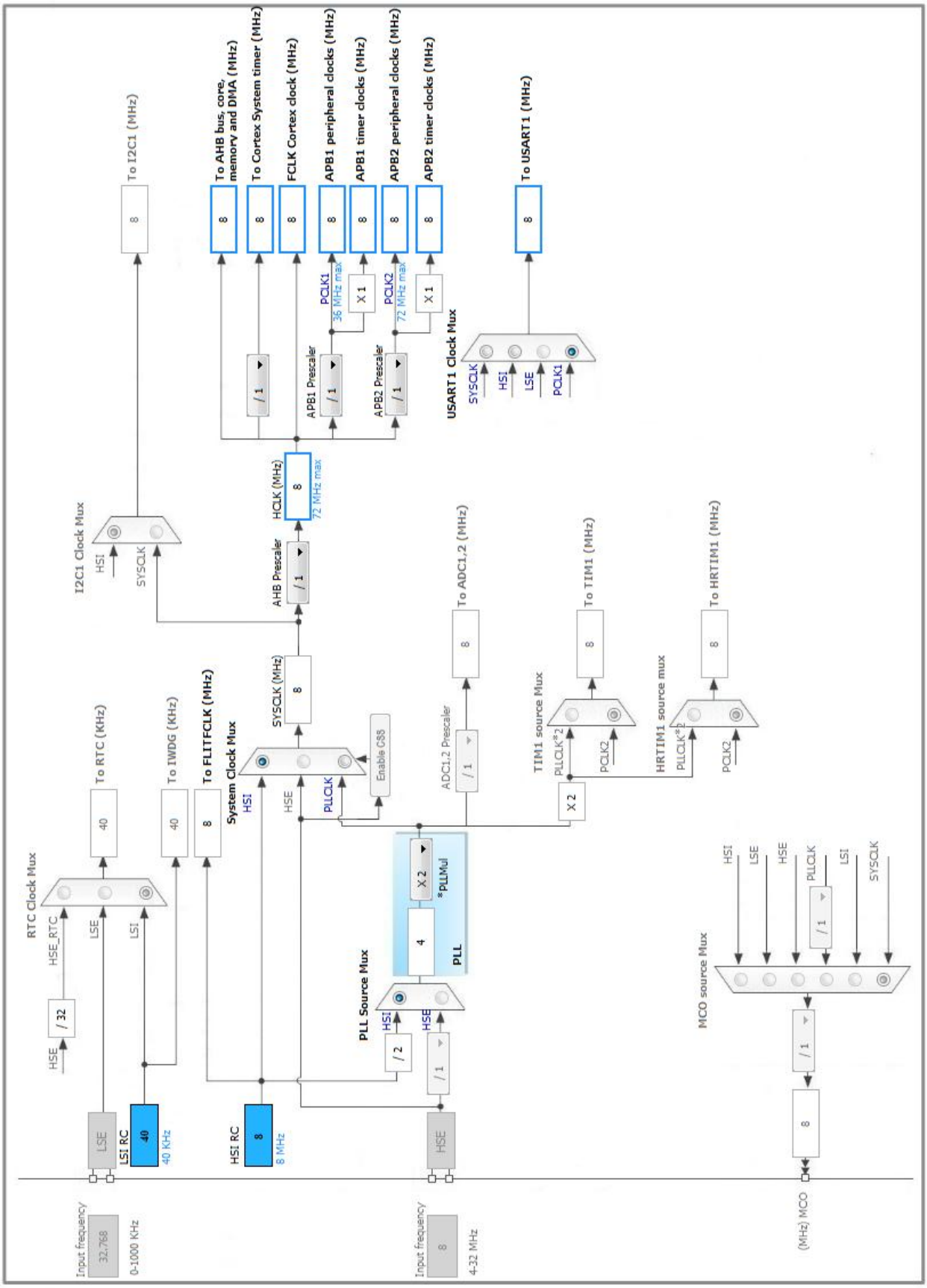


3. Pins Configuration

Pin Number LQFP64	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
7	NRST	Reset		
8	PC0 *	I/O	GPIO_Input	
9	PC1 *	I/O	GPIO_Input	
12	VSSA/VREF-	Power		
13	VDDA/VREF+	Power		
14	PA0 *	I/O	GPIO_Input	
15	PA1 *	I/O	GPIO_Input	
18	VSS	Power		
19	VDD	Power		
20	PA4 *	I/O	GPIO_Input	
21	PA5 *	I/O	GPIO_Output	
22	PA6 *	I/O	GPIO_Output	
23	PA7 *	I/O	GPIO_Output	
24	PC4	I/O	USART1_TX	
25	PC5	I/O	USART1_RX	
29	PB10 *	I/O	GPIO_Input	
31	VSS	Power		
32	VDD	Power		
38	PC7 *	I/O	GPIO_Input	
41	PA8 *	I/O	GPIO_Output	
42	PA9 *	I/O	GPIO_Output	
47	VSS	Power		
48	VDD	Power		
55	PB3 *	I/O	GPIO_Output	
56	PB4 *	I/O	GPIO_Output	
57	PB5 *	I/O	GPIO_Output	
58	PB6 *	I/O	GPIO_Output	
60	BOOT0	Boot		
61	PB8 *	I/O	GPIO_Input	
62	PB9 *	I/O	GPIO_Input	
63	VSS	Power		
64	VDD	Power		

* The pin is affected with an I/O function

4. Clock Tree Configuration



5. IPs and Middleware Configuration

5.1. SYS

Timebase Source: SysTick

5.2. USART1

Mode: Asynchronous

5.2.1. Parameter Settings:

Basic Parameters:

Baud Rate	**115200 ***
Word Length	**8 Bits (including Parity) ***
Parity	None
Stop Bits	1

Advanced Parameters:

Data Direction	Receive and Transmit
Over Sampling	16 Samples
Single Sample	Disable

Advanced Features:

Auto Baudrate	Disable
TX Pin Active Level Inversion	Disable
RX Pin Active Level Inversion	Disable
Data Inversion	Disable
TX and RX Pins Swapping	Disable
Overrun	Enable
DMA on RX Error	Enable
MSB First	Disable

*** User modified value**

6. System Configuration

6.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
USART1	PC4	USART1_TX	Alternate Function Push Pull	No pull up pull down	**High ***	
	PC5	USART1_RX	Alternate Function Push Pull	No pull up pull down	**High ***	
GPIO	PC0	GPIO_Input	Input mode	No pull up pull down	n/a	
	PC1	GPIO_Input	Input mode	No pull up pull down	n/a	
	PA0	GPIO_Input	Input mode	No pull up pull down	n/a	
	PA1	GPIO_Input	Input mode	No pull up pull down	n/a	
	PA4	GPIO_Input	Input mode	No pull up pull down	n/a	
	PA5	GPIO_Output	Output Push Pull	No pull up pull down	Low	
	PA6	GPIO_Output	Output Push Pull	No pull up pull down	Low	
	PA7	GPIO_Output	Output Push Pull	No pull up pull down	Low	
	PB10	GPIO_Input	Input mode	No pull up pull down	n/a	
	PC7	GPIO_Input	Input mode	No pull up pull down	n/a	
	PA8	GPIO_Output	Output Push Pull	No pull up pull down	Low	
	PA9	GPIO_Output	Output Push Pull	No pull up pull down	Low	
	PB3	GPIO_Output	Output Push Pull	No pull up pull down	Low	
	PB4	GPIO_Output	Output Push Pull	No pull up pull down	Low	
	PB5	GPIO_Output	Output Push Pull	No pull up pull down	Low	
	PB6	GPIO_Output	Output Push Pull	No pull up pull down	Low	
	PB8	GPIO_Input	Input mode	No pull up pull down	n/a	
	PB9	GPIO_Input	Input mode	No pull up pull down	n/a	

6.2. DMA configuration

nothing configured in DMA service

6.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Pre-fetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
USART1 global interrupt / USART1 wake-up interrupt through EXT line 25	true	0	0
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
Floating point unit interrupt	unused		

*** User modified value**

7. Power Consumption Calculator report

7.1. Microcontroller Selection

Series	STM32F3
Line	STM32F334
MCU	STM32F334R8Tx
Datasheet	025409_Rev6

7.2. Parameter Selection

Temperature	25
Vdd	3.6

8. Software Project

8.1. Project Settings

Name	Value
Project Name	edesign_latest
Project Folder	H:\E-Design2018\edesign_latest
Toolchain / IDE	TrueSTUDIO
Firmware Package Name and Version	STM32Cube FW_F3 V1.9.0

8.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No

9. Software Pack Report