COMP1521 24T2 — MIPS Data

https://www.cse.unsw.edu.au/~cs1521/24T2,

The Memory Subsystem

- memory subsystem typically provides capability to load or store bytes (not bits)
 - 1 byte == 8 bits (on general purpose modern machines)
- each byte has unique address, think of:
 - · memory as implementing a gigantic array of bytes
 - and the address is the array index
- $\boldsymbol{\cdot}$ typically, a small (1,2,4,8,...) group of bytes can be loaded/stored in a single operation
- general purpose computers typically have complex cache systems to improve memory performance
 - \cdot if we have time we'll look at cache systems a little, late in this course

Virtual Memory - Quick Summary

- we'll come back to virtual memory if anyt time left in week 10
- operating systems on general purpose computers typically provide virtual memory
- · virtual memory make it look to every running program that it has entire address space
 - hugely convenient for multi-process systems
- \cdot disconnects addresses running programs (processes) use from actual RAM address.
- $\boldsymbol{\cdot}$ operating system translates (virtual) address a process uses to an physical (actual) RAM address.
- translation needs to be really fast needs to be largely implemented in hardware (silicon)
- · virtual memory can be several times larger than actual RAM size
- · multiple processes can be in RAM, allowing fast switching
- part of processes can be load into RAM on demand.
- provides a mechanism to share memory betwen processes.

Address Size

- most modern general purpose computers use 64-bit addresses
 - · CSE servers use 64-bit addresses
- some (older) general purpose computers use 32-bit addresses
- many special purpose (embedded) CPUs use 32-bit addresses
 - but some use 64-bit addresses
 - some use 16-bit addresses
- on the MIPS32 machine implemented by mipsy, all addresses are 32-bit so in COMP1521 assembler we'll be using 32-bit addresses
- there are 64-bit MIPS CPUs

Accessing Memory on the MIPS

- · addresses are 32 bits
- only load/store instructions access memory on the MIPS
- 1 byte (8-bit) loaded/stored with lb/sb
- 2 bytes (16-bit) called a half-word, loaded/stored with lh/sh
- 4 bytes (32-bits) called a word, loaded/stored with lw/sw
- memory address used for load/store instructions is sum of a specified register and a 16-bit constant (often 0) which is part of the instruction
- for **sb** & **sh** operations low (least significant) bits of source register are used.
- lb/lh assume byte/halfword contains a 8-bit/16-bit signed integer
 - high 24/16-bits of destination register set to 1 if 8-bit/16-bit integer negative
- unsigned equivalents **lbu** & **lhu** assume integer is **unsigned**
 - high 24/16-bits of destination register always set to 0
- · signed and unsigned integer representations covered later in course

MIPS Load/Store Instructions

assembly	meaning	bit pattern
$\overline{ \ln r_t, \mathrm{I}(r_s) }$	$r_t = \operatorname{mem}[r_s + \mathbf{I}]$	100000ssssstttttIIIIIIIIIIIII
$\ln r_t, \mathrm{I}(r_s)$	$r_t = mem[r_s \text{+} \mathbf{I}] \mid$	100001ssssstttttIIIIIIIIIIIIII
	$\mathit{mem}[r_s\text{+I+1}] <<~8$	
${\rm lw}\; r_t \text{, I}(r_s)$	$r_t = mem[r_s \text{+I}] \mid$	100011ssssstttttIIIIIIIIIIIIII
	$mem[r_s\text{+I+1}]<<8\mid$	
	$\mathit{mem}[r_s\text{+I+2}]<<~16~ $	
	$mem[r_s\text{+I+3}]<<\text{ 24}$	
${\rm sb}\; r_t {\rm ,}\; {\rm I}(r_s)$	$\operatorname{mem}[r_s + \mathbf{I}] = r_t \; \delta \; \; \operatorname{Oxff}$	101000ssssstttttIIIIIIIIIIIIII
$sh\; r_t, \mathtt{I}(r_s)$	$\operatorname{mem}[r_s ext{+I}] = r_t \; \delta \; \; \operatorname{Oxff}$	101001ssssstttttIIIIIIIIIIIIII
	$mem[r_s\text{+I+1}] = r_t >> 8 \$ \texttt{0xff}$	
sw r_t , $\mathbf{I}(r_s)$	$mem[r_s\text{+I}] = r_t \; \delta \; \; 0xff$	101011ssssstttttIIIIIIIIIIIIII
	$\mathit{mem}[r_s\text{+I+1}] = r_t >> \text{8 & 0xff}$	
	$\mathit{mem}[r_s\text{+I+2}] = r_t >> \text{ 16 & 0xff}$	
	$\mathit{mem}[r_s\text{+I+3}] = r_t >> \text{ 24 \& 0xff}$	

Code example: storing and loading a value (no labels)

```
# we normally use directives and labels
main:
    li
         $t0.42
    sb
         $t0. 0x10000000 # store 42 in byte at address 0x10000000
    lb
        $a0. 0x10000000 # load $a0 from same address
    li
         $v0, 1
                          # print $a0 which will nows contain 42
    svscall
         $a0, '\n'
    li
    li
         $v0. 11
    syscall
    li
         $v0, 0
    ir
         $ra
```

source code for load_store_no_label.s

Assembler Directives

mipsy has directives to initialise memory, and to associate labels with addresses.

```
.text
    .data
    .space 18
                  # int8 t a[18];
    .align 2
    .word 42
    .word 1,3,5
    .half 2.4.6
b:
    .bvte 7:5
f:
    .float 3.14
    .asciiz "abc" # char s[4] {'a'.'b'.'c'.'\0'}:
    .ascii "abc" # char t[3] {'a'.'b'.'c'}:
```

simple example of load & storing a byte # we normally use directives and labels main: li \$t0. 42 sb \$t0. answer # store 42 in byte at address labelled answer lb \$a0, answer # load \$a0 from same address li \$v0, 1 svscall \$a0. '\n' # print '\n' li li \$v0. 11 svscall li \$v0, 0 jr \$ra .data answer:

Code example: storing and loading a value with a label

li \$t0, 42 la \$t1, answer \$t0, 0(\$t1) # store 42 in byte at address labelled answer sb \$a0. 0(\$t1) # load \$a0 from same address li \$v0, 1 syscall li \$a0. '\n' # print '\n' li \$v0. 11 syscall li \$v0. 0 jr \$ra .data answer: .space 1 10 / 75

Code example: storing and loading a value with address in register

simple example of storing & loading a byte

main:

Setting A Register to An Address

· Note the la (load address) instruction is normally used to set a register to a labelled memory address.

```
la $t8, start
```

- · mipsy converts labels to addresses (numbers) before a program is run,
 - no real difference between la and li instructions
- \cdot For example, if ${f vec}$ is the label for memory address ${f 0x10000100}$ then these two instructions are equivalent:

```
la $t7, vec
li $t7, 0x10000100
```

- In both cases the constant is encoded as part of the instruction(s).
- Neither la or li access memory!
 They are very different to lw etc

Specifying Addresses: Some mipsy short-cuts

• mipsy allows the constant which is part of load & store instructions can be omitted in the common case it is 0.

```
sb $t0, 0($t1) # store $t0 in byte at address in $t1
sb $t0, ($t1) # same
```

• For convenience, MIPSY allows addresses to be specified in a few other ways and will generate appropriate real MIPS instructions

```
sb $t0, x  # store $t0 in byte at address labelled x
sb $t1, x+15  # store $t1 15 bytes past address labelled x
sb $t2, x($t3) # store $t2 $t3 bytes past address labelled x
```

- These are effectively pseudo-instructions.
- · You can use these short cuts but won't help you much
- · Most assemblers have similar short cuts for convenience

MIPSY Memory Layout

Region	Address	Notes
.text	0x00400000	instructions only; read-only; cannot expand
.data	0x10000000.	data objects; read/write; can be expanded
.stack	0x7fffffef	this address and below; read/write
.ktext	0x80000000.	kernel code; read-only; only accessible in kernel mode
.kdata	0x90000000	kernel data; only accessible in kernel mode

Data Structures and MIPS

C data structures and their MIPS representations:

- · char ... as byte in memory, or register
- int ... as 4 bytes in memory, or register
- double ... as 8 bytes in memory, or \$f? register
- arrays ... sequence of bytes in memory, elements accessed by index (calculated on MIPS)
- \cdot structs ... sequence of bytes in memory, accessed by fields (constant offsets on MIPS)

A char, int or double

- · can be stored in register if local variable and no pointer to it
- otherwise stored on stack if local variable
- stored in data segment if global variable

Global Variables

```
Labels and \it directives used to allocate space for global variables in the \tt.data segment.
```

```
.data
.word
.bvte
.byte
       0:4
.asciiz "hello"
.align 2
.space 4
```

```
#include <stdio.h>
int global_counter = 0;
int main(void) {
    // Increment the global counter.
    // The following is the same as global_counter = global_counter + 1 (generally)
    global_counter++;
    printf("%d", global_counter);
    putchar('\n');
}
```

Incrementing a Global Variable: MIPS

```
lw $t1, global counter
   addi
           $t1, $t1, 1
   sw $t1, global counter # global counter = global counter + 1;
   li $v0. 1 # syscall 1: print int
   la $t0. global counter #
   lw $a0. ($t0)
   svscall
   li $v0.11
   li $a0. '\n'
   li $v0, 0
   ir $ra
    .data
global_counter:
    .word
```

```
c
int main(void) {
   int x, y, z;
   x = 17;
   y = 25;
   z = x + y;
///
```

MIPS

```
main:
    # x in $t0
    # y in $t1
    # z in $t2
    li $t0, 17
    li $t1, 25
    add $t2, $t1, $t0
# ...
```

int main(void) { x = 17: y = 25;

add variables in memory (uninitialized)

int x, y, z;

MIPS (.data)

.data

.space 4 **y:**

.space 4 z:

```
MIPS (.text)
main:
```

li \$t0, 17 la \$t1, x \$t0. (\$t1) # x = 17;li \$t0, 25 la \$t1, y **\$t0, (\$t1)** # y = 25; la \$t0, x lw \$t1, (\$t0) la \$t0, y lw \$t2, (\$t0) add \$t3, \$t1, \$t2 la \$t0, z sw \$t3, 0(\$t0) # z = x + y;

li \$v0.1 # syscall 1: print int

.space 4

```
int x=17;
int y=25
int main(void) {
MIPS .data
    .data
     .word
             17
٧:
     .word
             25
     .space
```

MIPS

```
main:
    la $t0, x
    lw $t1, ($t0)
    la $t0, y
    lw $t2, ($t0)
    add $t3, $t1, $t2
    la $t0, z
    sw $t3, 0($t0) # z = x + y;
source code for add memory initialized.
```

int main(void) { x = 17: y = 25;

add variables in memory (uninitialized)

int x, y, z;

MIPS (.data)

.data

.space 4 **y:**

.space 4 z:

.space 4

MIPS (.text) main:

li \$t0, 17 la \$t1, x

\$t0. (\$t1) # x = 17;li \$t0, 25

la \$t1, y **\$t0, (\$t1)** # y = 25;

la \$t0, x lw \$t1, (\$t0) la \$t0, y

la \$t0, z

lw \$t2, (\$t0)

add \$t3, \$t1, \$t2

sw \$t3, 0(\$t0) # z = x + y;

li \$v0.1 # syscall 1: print int

```
int x=17;
int y=25
int main(void) {
MIPS .data
    .data
     .word
             17
٧:
     .word
             25
     .space
```

MIPS

```
main:
    la $t0, x
    lw $t1, ($t0)
    la $t0, y
    lw $t2, ($t0)
    add $t3, $t1, $t2
    la $t0, z
    sw $t3, 0($t0) # z = x + y;
```

```
add variables in memory (array)
                                   MIPS .text
        int x[] = \{17, 25, 0\};
                                  main:
         int main(void) {
                                      la $t0, x
            x[2] = x[0] + x[1];
                                      lw $t1.0($t0)
                                      lw $t2, 4($t0)
                                      add $t3, $t1, $t2 \# x[2] = x[0] + x[1];
                                      sw $t3, 8($t0)
                                      li $v0, 1 # syscall 1: print int
                                      lw $a0.8($t0)#
                                      syscall
                                                     # printf("%d". x[2]):
                                      li $v0, 11 # syscall 11: print char
                                      li $a0, '\n'
                                      syscall
                                      li $v0, 0
                                      ir $ra  # return 0:
                                       .data
```

```
double array[10];
for (int i = 0; i < 10; i++) {
    printf("&array[%d]=%p\n", i, &array[i]);
printf("\nExample computation for address of array element\n");
uintptr t a = (uintptr t)&array[0];
printf("\deltaarray[0] + 7 * sizeof (double) = 0x\%lx\n", a + 7 * sizeof (double));
printf("&array[0] + 7 * %lx
                                          = 0x%lx\n", sizeof (double), a + 7 * sizeof (double)
printf("0x%1x + 7 * %1x = 0x%1x1x, a, sizeof (double), a + 7 * sizeof (double)
printf("&array[7]
                                        = %p\n", &array[7]);
```

source code for array_element_address

• this code uses types covered later in the course

Address of C 1-d Array Elements - Code

Address of C 1-d Array Elements - Output \$ dcc array_element_address.c -o array_element_address \$./array_element_address \$ array[0]=0x7fffdd841d00 \$ array[1]=0x7fffdd841d08 \$ array[2]=0x7fffdd841d10 \$ array[3]=0x7fffdd841d18 \$ array[4]=0x7fffdd841d20 \$ array[5]=0x7fffdd841d28

Example computation for address of array element σ

&array[6]=0x7fffdd841d30
&array[7]=0x7fffdd841d38
&array[8]=0x7fffdd841d40
&array[9]=0x7fffdd841d48

 $\delta arrav[0] + 7 * 8$

&array[7]

0x7fffdd841d00 + 7 * 8

= 0x7fffdd841d38

```
C
int x[10];
int main(void) {
    // sizeof x[0] == 4
    x[3] = 17;
}
```

MIPS

```
main:
    li
         $t0.3
    mul $t0, $t0, 4
    la
         $t1, x
         $t2, $t1, $t0
    add
    li
         $t3, 17
         $t3, 0($t2)
    SW
.data
    .space 40
```

```
tinclude <stdint.h>
int16_t x[30];
int main(void) {
    // sizeof x[0] == 2
    x[13] = 23;
}
```

MIPS

```
main:
    li
         $t0. 13
    mul $t0, $t0, 2
    la
         $t1, x
         $t2, $t1, $t0
    add
    li
         $t3, 23
         $t3, 0($t2)
.data
    .space 60
```

```
int main(void) {
    int i = 0:
   while (i < 5) {
        printf("%d\n", numbers[i]);
        i++:
    return 0;
```

Simplified C

```
int main(void) {
    int i = 0:
loop:
    if (i >= 5) goto end;
        printf("%d", numbers[i]);
        printf("%c", '\n');
        i++:
    goto loop;
end:
    return 0;
```

```
Printing Array: MIPS
# print array of ints
main:
    li
        $t0, 0
loop:
    bge $t0.5.end
    la
         $t1, numbers
        $t2, $t0, 4
    mul
    add
         $t3, $t2, $t1
        $a0, 0($t3) # printf("%d", j);
    lw
    li
       $v0, 1
    svscall
    li $a0, '\n'
    li
       $v0, 11
    syscall
    addi $t0, $t0, 1
         loop
                                                                                  29 / 75
```

Printing Array: MIPS (continued)

Changing an Array: C

```
int i;
i = 0;
while (i < 5) {
    numbers[i] *= 42;
    i++;
}</pre>
```

source code for change_array.

Changing an Array MIPS

```
main:
    li
         $t0, 0
loop1:
    bge
         $t0, 5, end1
   mul
         $t1, $t0, 4
    la
         $t2. numbers
                       # calculate &numbers[i]
         $t1, $t1, $t2
    add
    lw
         $t3, ($t1)
         $t3, $t3, 42
   mul
         $t3, ($t1)
    SW
    addi $t0, $t0, 1
         loop1
end1:
```

Reading into an Array: C

```
int i = 0;
while (i < 10) {
    printf("Enter a number: ");
    scanf("%d", &numbers[i]);
    i++;
}</pre>
```

Reading into an Array: MIPS

```
li
        $t0.0
loop0:
   bge
       $t0. 10. end0 # while (i < 10) {
   la
        $a0, string0 # printf("Enter a number: ");
   li
       $v0. 4
   syscall
   li $v0, 5
   svscall
   mul $t1, $t0, 4
                         calculate &numbers[i]
   la $t2, numbers
   add $t3, $t1, $t2
   sw $v0, ($t3)
   addi $t0, $t0, 1 # i++;
   b
        loop0
end0:
```

Printing in reverse order: C

```
printf("Reverse order:\n");
count = 9;
while (count >= 0) {
    printf("%d\n", numbers[count]);
    count--;
}
```

Printing in reverse order: C

```
la $a0, string1
   li $v0, 4
   syscall
   li $t0.9
next:
   blt $t0, 0, end1  # while (count >= 0) {
   mul $t1, $t0, 4
       $t2, numbers
   la
   add $t1. $t1. $t2
       $a0. ($t1)
                          load numbers[count] into $a0
   lw
   li $v0, 1
   li $a0, '\n'
   li $v0.11
   addi $t0, $t0, -1 # count--;
   b
end1:
```

```
int array[X][Y];
printf("sizeof array[2][3] = %lu\n", sizeof array[2][3]);
printf("sizeof array[1] = %lu\n", sizeof array[1]);
printf("sizeof array = %lu\n", sizeof array);
printf("&arrav=%p\n". &arrav):
for (int x = 0; x < X; x++) {
    printf("&array[%d]=%p\n". x. &array[x]):
    for (int v = 0: v < Y: v++) {
        printf("\deltaarray[%d][%d]=%p\n", x, y, \deltaarray[x][y]);
```

source code for 2d_array_element_address.c

this code uses types covered later in the course

Address of 2-d C Array Elements - Output

```
$ dcc 2d array element address.c -o 2d array element address
$ ./2d array element address
sizeof array[2][3] = 4
sizeof array[1] = 16
sizeof array = 48
&array=0x7ffd93bb16c0
&array[0]=0x7ffd93bb16c0
&array[0][0]=0x7ffd93bb16c0
&array[0][1]=0x7ffd93bb16c4
&array[0][2]=0x7ffd93bb16c8
&array[0][3]=0x7ffd93bb16cc
&array[1]=0x7ffd93bb16d0
&arrav[1][0]=0x7ffd93bb16d0
&array[1][1]=0x7ffd93bb16d4
&arrav[1][2]=0x7ffd93bb16d8
&array[1][3]=0x7ffd93bb16dc
&array[2]=0x7ffd93bb16e0
&array[2][0]=0x7ffd93bb16e0
&array[2][1]=0x7ffd93bb16e4
&arrav[2][2]=0x7ffd93bb16e8
&array[2][3]=0x7ffd93bb16ec
```

Computing sum of 2-d Array : C

Assume we have a 2d-array:

```
int32_t matrix[6][5];
We can sum its value like this in C
```

```
int row, col, sum = 0;
// row-by-row
for (row = 0; row < 6; row++) {
    // col-by-col within row
    for (col = 0; col < 5; row++) {
        sum += matrix[row][col];
    }
}</pre>
```

MIPS directives for an equivalent 2d-array

```
.data
matrix: .space 120 # 6 * 5 == 30 array elements each 4 bytes
```

li \$t0, 0 li \$t1. 0

Computing sum of 2-d Array: MIPS

loop1: bge

loop2: bge

end2:

end1:

li

la

mul mul

add

add

lw

\$t1, 6, end1 \$t2, 0

\$t2, 5, end2

\$t3, matrix

\$t6, \$t3, \$t4

\$t7. \$t6. \$t5 \$t5, 0(\$t7)

add \$t0. \$t0. \$t5 addi \$t2, \$t2, 1 loop2 addi \$t1, \$t1, 1

loop1

\$t4, \$t1, 20 \$t5, \$t2, 4

```
Simplified C
int main(void) {
                                                int main(void) {
    int i = 0:
                                                     int i = 0:
    while (i < 3) {
                                                loop1:
        int j = 0:
                                                     if (i >= 3) goto end1:
        while (j < 5) {
                                                         int j = 0;
             printf("%d", numbers[i][j]);
                                                     loop2:
             printf("%c". ' ');
                                                         if (i >= 5) goto end2:
             j++;
                                                             printf("%d", numbers[i][j]);
                                                             printf("%c". ' '):
        printf("%c", '\n');
                                                             j++;
                                                         goto loop2;
                                                     end2:
    return 0;
                                                         printf("%c". '\n'):
                                                         i++:
source code for print2d c
                                                     goto loop1:
                                                end1.
                                                                                            41 / 75
```

Printing 2-d Array: MIPS

```
# print a 2d array
main:
       $t0, 0
loop1:
       $t0, 3, end1 # if (i >= 3) goto end1;
   bge
        $t1, 0
loop2:
   bge $t1, 5, end2 # if (j >= 5) goto end2;
        $t2, numbers #
   mul
        $t3, $t0, 20
       $t4, $t3, $t2
   mul $t5, $t1, 4
   add
       $t6, $t5, $t4
        $a0, 0($t6)
   lw
   li $v0, 1
   syscall
```

Printing 2-d Array: MIPS (continued)

li \$a0, ' ' # printf("%c", ' ');

```
li $v0, 11
   addi $t1, $t1, 1 # j++;
       loop2  # goto loop2;
   b
end2:
   li $a0, '\n' # printf("%c", '\n');
   li $v0, 11
   syscall
   addi $t0, $t0, 1 # i++
       loop1
end1:
      $v0.0
       $ra
.data
numbers:
    .word 3, 9, 27, 81, 243, 4, 16, 64, 256, 1024, 5, 25, 125, 625, 3125
```

Printing a Flag: C #include <stdio.h> #define N ROWS 6

};

```
// Print a 2D array of characters.
#define N_COLS 12
char flag[N_ROWS][N_COLS] = {
 int main(void) {
  for (int row = 0: row < N ROWS: row++) {
   for (int col = 0; col < N COLS; col++) {
     printf("%c", flag[row][col]);
```

Printing a Flag: simplified C

```
row_loop__init:
    int row = 0;
row_loop_cond:
    if (row >= N ROWS) goto row loop end;
row_loop__body:
col_loop_init:
    int col = 0:
col_loop__cond:
    if (col >= N COLS) goto col loop end;
col loop body:
   printf("%c", flag[row][col]);
col loop step:
   goto col_loop__cond;
col loop end:
   printf("\n"):
row loop step:
   goto row_loop__cond;
row loop end:
```

Printing a Flag: MIPS

```
N ROWS = 6
N COLS = 12
main:
main row loop init:
    li $t0.0
main row loop cond:
   bge $t0, N ROWS, main row loop end # if (row >= N ROWS) goto main row loop end;
main row loop body:
main col loop init:
   li $t1.0
main col loop cond:
   bge $t1, N_COLS, main_col_loop_end # if (col >= N_COLS) goto main_col_loop_end;
main col loop body:
   li $v0.11
```

Printing a Flag: MIPS

```
mul $t2, $t0, N_COLS
   add $t2, $t2, $t1
   lb $a0, flag($t2)
main col loop step:
   addi $t1, $t1, 1
   j main col loop cond
main col loop end:
   li $v0. 11
   li $a0, '\n'
main row loop step:
   addi $t0, $t0, 1
   i main row loop cond
main__row_loop_end:
         $v0, 0
flag:
```

Alignment

- \cdot C standard requires simple types of size N bytes to be stored only at addresses which are divisible by N
 - if int is 4 bytes, must be stored at address divisible by 4
 - if 'double is 8 bytes, must be stored at address divisible by 8
- · compound types (arrays, structs) must be aligned so their components are aligned
- MIPS requires this alignment
- on other architectures aligned access faster

Example C with unaligned accesses

```
char bytes[32];
int *i = (int *)&bytes[1];
// illegal store - not aligned on a 4-byte boundary
*i = 42;
printf("%d\n", *i);
```

Example MIPS with unaligned accesses

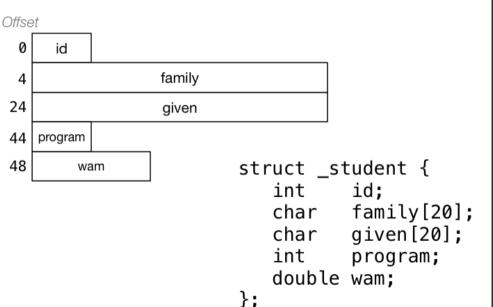
```
.data
    # data will be aligned on a 4-byte boundary
    # most likely on at least a 128-byte boundary
    .align 2
    .space 1
v1: .space 1
v2: .space 4
v3: .space 2
v4: .space 4
    .space 1
    .align 2 # ensure e is on a 4 (2**2) byte boundary
v5: .space 4
    .space 1
v6: .word 0 # word directive aligns on 4 byte boundary
```

Example MIPS with unaligned accesses

```
li
     $t0. 1
sb
     $t0, v1
              # will succeed because no alignment needed
     $t0. v1
              # will fail because v1 is not 2-byte aligned
     $t0. v1
SW
     $t0. v2
              # will succeeed because v2 is 2-byte aligned
     $t0. v2
SW
     $t0. v3
     $t0, v3
SW
sh
     $t0. v4
     $t0, v4
SW
     $t0. v5
              # will succeeed because v5 is 4-byte aligned
SW
     $t0. v6
SW
li
     $v0. 0
     $ra
```

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Structs in MIPS



```
C struct definitions effectively define a new type.
```

```
// new type called "struct student"
struct student {...};

// new type called student_t
typedef struct student student t:
```

Instances of structures can be created by allocating space:

.space 56

```
stu:
    .space 4 # student_t *stu;
```

Accessing structure components is by offset, not name

```
li $t0 5012345
la
   $t1. stu1
   $t0, 0($t1)
li
  $t0. 3778
sw $t0. 44($t1)
                     # stu1.program = 3778:
   $t2. stu2
la
li
   $t0.3707
   $t0, 44($t2)
li
   $t0, 5034567
   $t0, 0($t2)
SW
```

Student Details: C

```
struct details {
    uint16 t postcode;
    uint8 t wam;
    uint32 t zid;
struct details student:
int main(void) {
    student.postcode = 2052;
    student.wam = 95;
    student.zid = 5123456;
    printf("%d", student.zid);
    putchar(' ');
    printf("%d". student.wam):
    putchar(' ');
    printf("%d", student.postcode);
    putchar('\n');
    return 0;
```


OFFSET_WAM
OFFSET ZID

OFFSET_POSTCODE

= 0

Student Details: MIPS

= 4 # unused padding byte before zid field to ensure it is on a 4-byte

Student Details: MIPS

```
### Save values into struct ###
la $t0, student # student.postcode = 2052;
addi $t1, $t0, OFFSET POSTCODE
li
    $t2. 2052
sh $t2. ($t1)
la $t0, student # student.wam = 95;
addi $t1, $t0, OFFSET WAM
li
    $t2.95
  $t2, ($t1)
sb
la $t0. student
addi $t1, $t0, OFFSET ZID
li
    $t2, 5123456
    $t2, ($t1)
SW
```

source code for student.s

Student Details: MIPS

```
### Load values from struct ###
   la $t0. student
   addi $t1, $t0, OFFSET_ZID
   lw $a0, ($t1)
   li $v0.11
   addi $t1, $t0, OFFSET_WAM
   lbu $a0. ($t1)
   li $v0, 1
   li $v0, 11
   addi $t1, $t0, OFFSET_POSTCODE
   lhu $a0. ($t1)
   li $v0, 1
   li $a0. '\n'
   li $v0.11
student:
    .space 8
```

More complex student info: C

```
#include <stdio.h>
struct student {
    int zid;
    char first[20];
    char last[20];
    int program;
    char alias[10];
struct student abiram = {
    .zid = 5308310.
    .program = 3778.
struct student xavier = {
    .last = "Cooney",
    .alias = "xavc"
```

More complex student info: C

```
int main(void) {
    struct student *selection = &abiram:
   printf("zID: z%d\n", selection->zid);
   printf("First name: %s\n", selection->first);
   printf("Last name: %s\n", selection->last);
   printf("Program: %d\n", selection->program);
   printf("Alias: %s\n", selection->alias);
   // as well as the overall struct?
   printf("sizeof(zid) = %zu\n". sizeof(selection->zid));
   printf("sizeof(first) = %zu\n". sizeof(selection->first));
   printf("sizeof(last) = %zu\n". sizeof(selection->last));
   printf("sizeof(program) = %zu\n". sizeof(selection->program));
   printf("sizeof(alias) = %zu\n", sizeof(selection->alias));
   printf("sizeof(struct student) = %zu\n", sizeof(struct student));
   // We can see that two extra padding bytes were added to the end
   // to a word boundary.
   return 0:
```

More complex student info: MIPS

```
# A demo of accessing fields of structs in MIPS.
# Offsets for fields in `struct student`
STUDENT OFFSET ZID = 0
STUDENT OFFSET FIRST = 4
STUDENT OFFSET LAST = 20 + STUDENT OFFSET FIRST
STUDENT OFFSET PROGRAM = 20 + STUDENT OFFSET LAST
STUDENT OFFSET ALIAS = 4 + STUDENT OFFSET PROGRAM
SIZEOF STRUCT STUDENT = 10 + STUDENT OFFSET ALIAS + 2
    .text
```

main:

source code for struct.s

More complex student info: MIPS

```
li $v0. 1
li $v0. 4
```

Array of Structs: C

```
#include <stdio.h>
#define MAX POLYGON 6
struct point {
    int x:
    int v:
};
struct polygon {
    int
                   degree;
    struct point vertices[MAX POLYGON]; // C also allows variable size array here
};
void print last vertex(struct polygon *p);
struct polygon triangle = {3, {{0,0}, {3,0}, {0,4}}};
```

Array of Structs: C

```
int main(void) {
    print_last_vertex(&triangle); // prints 0,4
    return 0;
}
```

source code for struct_array.

Array of Structs: MIPS

```
OFFSET_POINT_X = 0
OFFSET POINT Y = 4
SIZEOF_POINT
                 = 8
OFFSET_POLYGON_DEGREE
                        = 0
OFFSET_POLYGON_VERTICES
SIZEOF POLYGON
                        = 52
main:
```

Array of Structs: MIPS

```
$a0, triangle
   jal print last vertex
        $v0, 0
   pop $ra
print_last_vertex:
   lw $t2, OFFSET_POLYGON_DEGREE($a0) # int n = p->degree - 1;
   addi $t0, $t2, -1
    addi $t3, $a0, OFFSET_POLYGON_VERTICES # calculate &(p->vertices[n])
   mul $t4. $t0. SIZEOF POINT
   add $t1, $t3, $t4
   lw $a0, OFFSET_POINT_X($t1)
   li $v0, 1
    li $a0. '.'
   li $v0, 11
    lw $a0, OFFSET_POINT_Y($t1)
   li $v0.1
   li $v0, 11
```

Array of Structs: MIPS

```
.data
# struct polygon triangle = {3, {{0,0}, {3,0}, {0,4}}};
triangle:
    .word 3
    .word 0,0, 3,0, 0,4, 0,0, 0,0, 0,0
```

source code for struct_array.

MIPS int i; **\$t0, answer** # p = &answer; la int *p; t1, (t0) # i = *p;lw p = &answer: move \$a0, \$t1 # printf("%d\n", i); i = *p;li \$v0.1 syscall printf("%d\n", i); li \$a0, '\n' # printf("%c", '\n'); *p = 27: li \$v0.11 syscall printf("%d\n". answer): li \$t2. 27 # *p = 27; \$t2, (\$t0) # SW lw \$a0, answer # printf("%d\n", answer); li \$v0.1 syscall li \$a0, '\n' # printf("%c", '\n'); <u>li</u> \$v0, 11 syscall

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Implementing Pointers in MIPS

```
#include <stdio.h>
#define MAX POLYGON 6
struct point {
    int x;
    int y;
};
struct polygon {
    int
                    degree;
    struct point vertices[MAX POLYGON]; // C also allows variable size array here
};
void print last vertex(struct polygon *p);
struct polygon triangle = \{3, \{\{0,0\}, \{3,0\}, \{0,4\}\}\}\};
```

Example - Accessing Struct within Array within Struct (main)

```
int main(void) {
    print_last_vertex(&triangle); // prints 0,4
    return 0;
main:
    push
           $ra
         $a0, triangle
    la
    jal
         print last vertex
    li
         $v0. 0
         $ra
    pop
         $ra
```

source code for struct_array

```
printf("%d", p->vertices[p->degree - 1].x);
    putchar(',');
    printf("%d", p->vertices[p->degree - 1].y);
    putchar('\n');
void print last vertex(struct polygon *p) {
    int n = p - > degree - 1:
    struct point *last = &(p->vertices[n]):
    printf("%d", last->x);
    putchar(',');
    printf("%d", last->v);
    putchar('\n'):
```

Example - Accessing Struct within Array within Struct (C)

void print last vertex(struct polygon *p) {

Example - Accessing Struct within Array within Struct (MIPS)

```
print last vertex:
   # $a0: p
   # $t2..$t5: temporaries
   lw $t2, OFFSET_POLYGON_DEGREE($a0) # int n = p->degree - 1;
   addi $t0. $t2. -1
   addi $t3, $a0, OFFSET POLYGON VERTICES # calculate &(p->vertices[n])
   mul $t4, $t0, SIZEOF_POINT
       $t1, $t3, $t4
   add
        $a0. OFFSET POINT X($t1)
        $v0. 1
   svscall
       $a0. '.'
        $v0. 11
       $a0. OFFSET POINT Y($t1)
        $v0. 1
       $a0, '\n'
   li $v0. 11
   svscall
    jr $ra
```

```
int main(void) {
    int *p = &numbers[0];
    int *q = &numbers[4]:
   while (p \le q) {
        printf("%d\n", *p);
        p++:
    return 0;
```

Simplified C

```
int main(void) {
    int *p = &numbers[0]:
    int *q = \delta numbers[4]:
loop:
    if (p > q) goto end;
        int j = *p:
        printf("%d", j);
        printf("%c". '\n'):
        p++:
    goto loop;
end:
    return 0;
```

Printing Array with Pointers: MIPS

```
main:
   la
        $t0, numbers
   la
       $t0, numbers
   addi $t1, $t0, 16
loop:
   bgt $t0, $t1, end
       $a0, 0($t0)
    lw
   li
       $v0, 1
   svscall
   li $a0, '\n'
   li
       $v0. 11
   syscall
   addi $t0, $t0, 4
   b
        loop
end:
```

Printing Array with Pointers: MIPS - faster

```
main:
   la $t0, numbers
   addi $t1, $t0, 16  # int *q = &numbers[4];
loop:
       $a0, ($t0)
   lw
   li
      $v0. 1
   syscall
   li $a0, '\n'
   li $v0.11
   syscall
   addi $t0, $t0, 4 # p++
   ble $t0. $t1. loop # if (p <= g) goto loop:
```