Chip\_2 <u>×10<sup>-3</sup></u> frame\_2 Fraction of events / 20 ADC bins **Entries** 115174 0.7 -737.7 Mean Std Dev 494.1 0.6 0.5 0.4 0.3 0.2 0.1 -2200-2000-1800-1600-1400-1200-1000 -800 -600 -400 -200Amplitude [ADC bins]