**HEPSYCODE Prompt**

**Context:** We have developed an Electronic Design Automation tool for designing embedded systems called HEPSYCODE. The tool is built upon Eclipse Ecore metamodels and utilizes Sirius features. The metamodel is designed to model algorithms, functionalities, and applications of embedded systems as a process network.

**Input Demonstration:** This is the HEPSYCODE metamodel:

<HEPSYCODE METAMODEL>

This is an example model based on HEPSYCODE metamodel:

<HEPSYCODE EXAMPLE MODEL>

**Output Demonstration:** This is a XES trace file representing the modeling step done when we created the example model based on HEPSYCODE metamodel:

<XES EXAMPLE TRACE>

**Task:** Generate the XES trace file from the following HEPSYCODE model, where the model represents another embedded application:

<HEPSYCODE MODEL>