



LFSR: generic N for output size

inputs: CLK
en
rst
bit

output: [N-1:0] data

Seven seg already made

input: [3:0] hex #

CLK
output: [7:0] HEX#

RNG: generic N for size

inputs: CLK
en (switch)
rst (to seed)

output: [N-1:0] RN