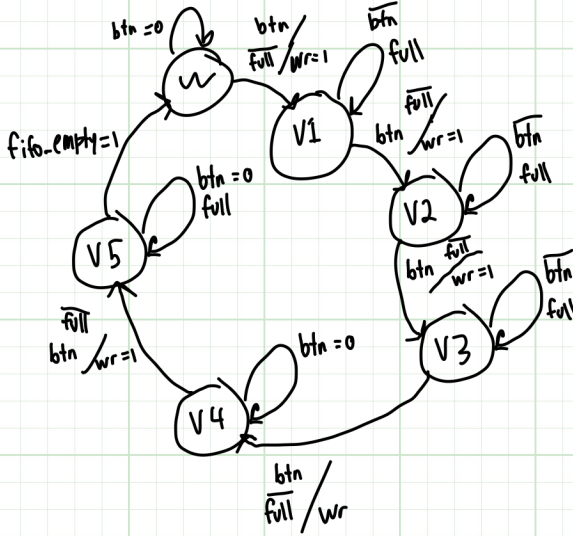


Signal paths

Write FSM

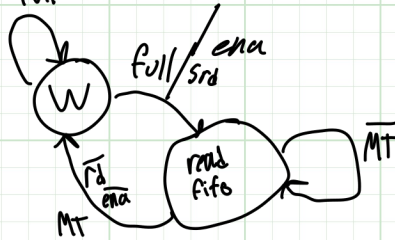
btn = "store" = "Go"



inputs: CLK
btn
Full
MT
outputs: wr

Read FSM

MT + $\overline{\text{Full}}$



inputs: MT
Full
outputs: rd
en

Fifo accumulator (signals no clk)

