

A schematic diagram of a race track system. A large light gray rectangle represents the 'Race Track'. On its right side, a vertical section is labeled 'Detection Loop'. Below the track, a dark gray rectangle is labeled 'Decoder and Transmitter'. A line connects the 'Decoder and Transmitter' to a small dark gray square, which is connected to the bottom of the 'Detection Loop'.

**Race Track**

**Detection  
Loop**

**Decoder and Transmitter**