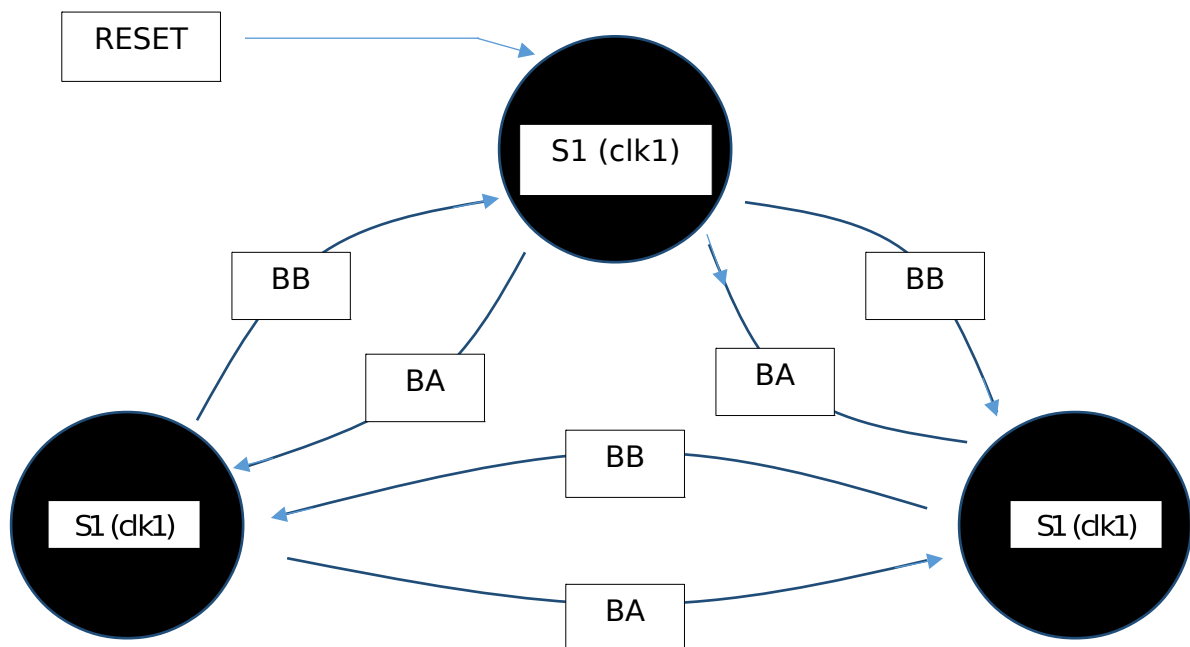


Embedded Logic Design  
Assignment - LED Worm  
Group08

1. The Finite State Machine giving SSLED frequency as output:

The following machine implements a clockwise OR anti-clockwise rotation of clocks i.e., BA (Button A) achieves clockwise rotation in set {clk1, clk2, clk3} and BB (Button B) achieves anti-clockwise rotation in set {clk1, clk2, clk3}.

Total States required = 3



Input	Present State	Next State		Output	
		X = BA	X = BB	X = BA	X = BB
Reset	S1	S2	S3	Clk2	Clk3
BA	S2	S3	S1	Clk3	Clk1
BB	S3	S1	S2	Clk1	Clk2