| Date: 07/01/2019 | 30MHz to 1GHz VNA | | | Drawn By: Josh Johnson |
|---------------------|-------------------|---|--|--|
| | LO | Filter Bank | Source Leveling | Coupling |
| | | 125 MHz LPF 250 MHz LPF 500 MHz LPF 1000 MHz LPF | Variable Attenuator Amplifier Level Control Log Power Detector | Forward Reverse Coupler Resistive Split |
| MCU | Gaiı | n/Phase Detector | | D U T |
| | 7 - V | MAG RF A (REF) | Reference | |
| ADC | | AD8302 PHASE RF B (DUT) | Return Signal Through Signal | |