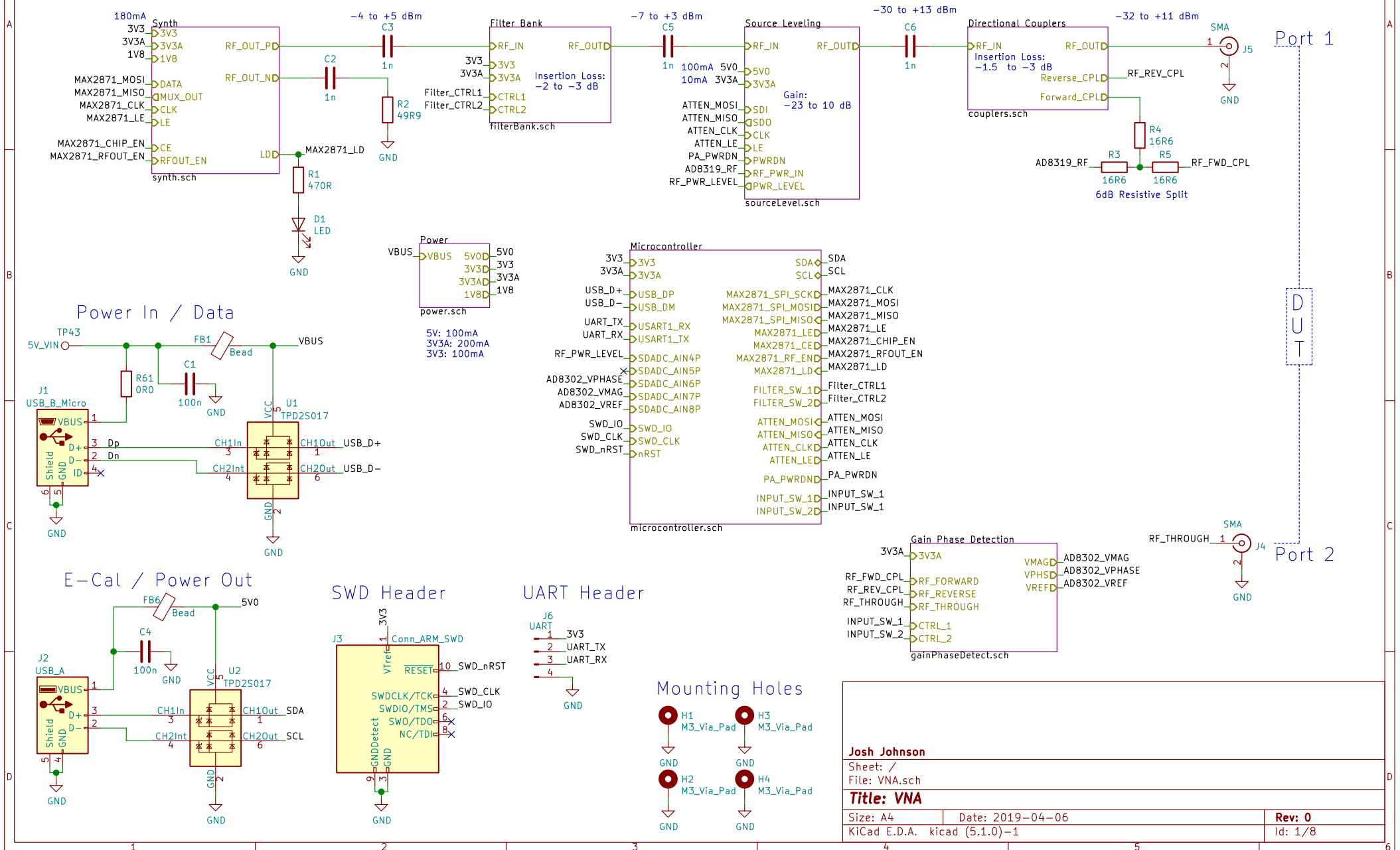


Josh's VNA



Josh Johnson

Sheet: /

File: VNA.sch

Title: VNA

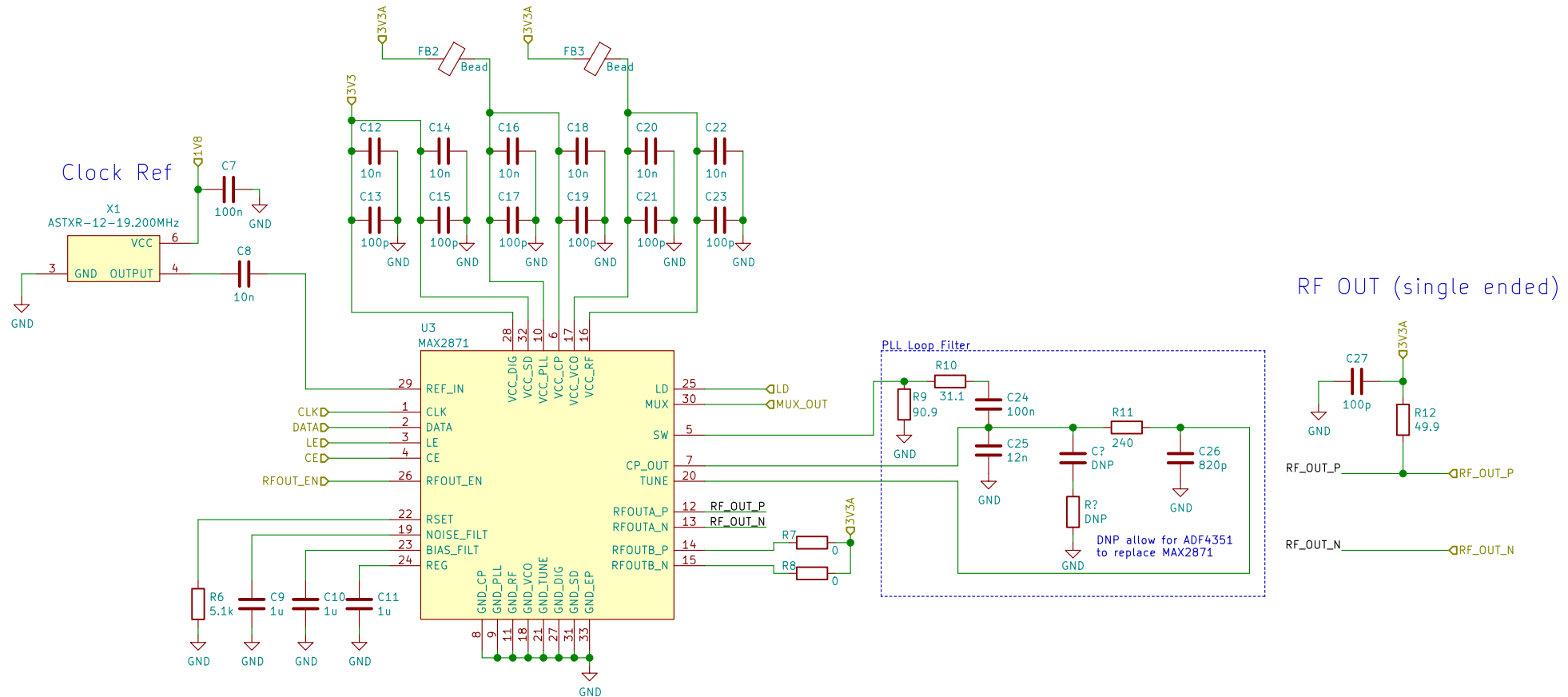
Size: A4 Date: 2019-04-06

KiCad E.D.A. kicad (5.1.0)-1

Rev: 0

Id: 1/8

MAX2871: 23.5 – 6000 MHz PLL



Josh Johnson

Sheet: /Synth/
File: synth.sch

Title: Synth

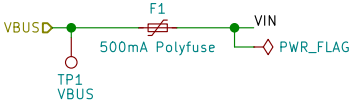
Size: A4 Date: 2019-04-06

KiCad E.D.A. kicad (5.1.0)-1

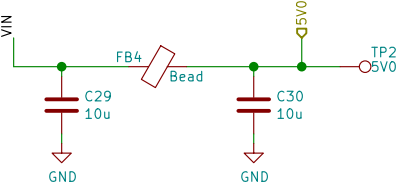
Rev: 0

Id: 2/8

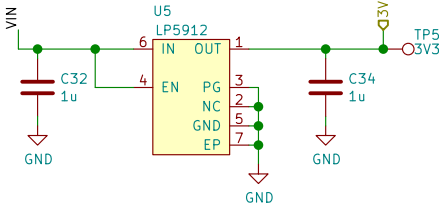
Input Protection



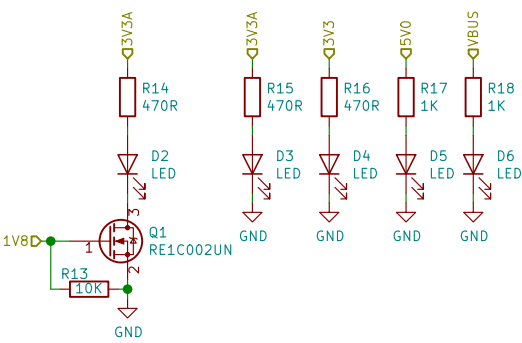
5V (Filtered)



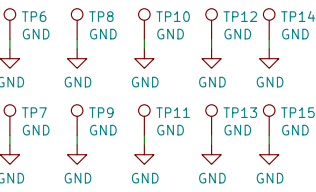
3V3 LDO



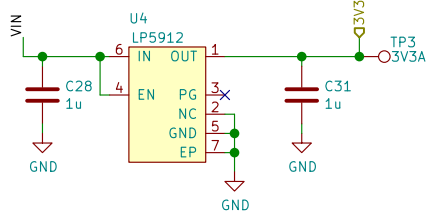
Voltage Rail LEDs



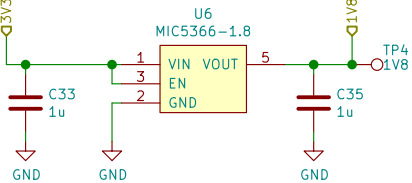
Ground Test Points



3V3A LDO



1V8 LDO



Josh Johnson

Sheet: /Power/
File: power.sch

Title: Power

Size: A4 Date: 2019-04-06

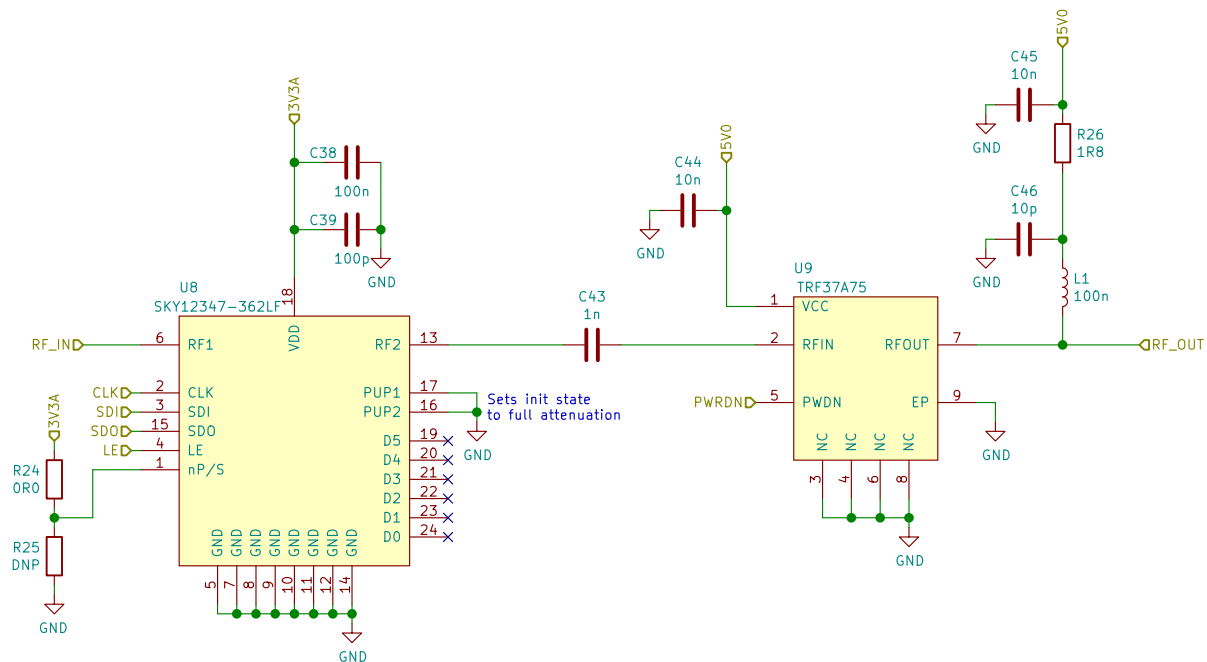
KiCad E.D.A. kicad (5.1.0)-1

Rev: 0

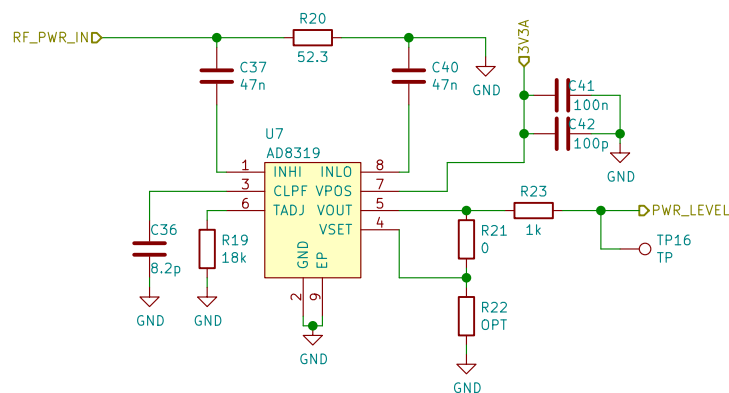
Id: 3/8

Programmable Attenuator

Amplifier
(Gain = 12dB P1dB = +18dBm IP3 = +32.5dBm)



Power Detector



Josh Johnson

Sheet: /Source Leveling/

File: sourceLevel.sch

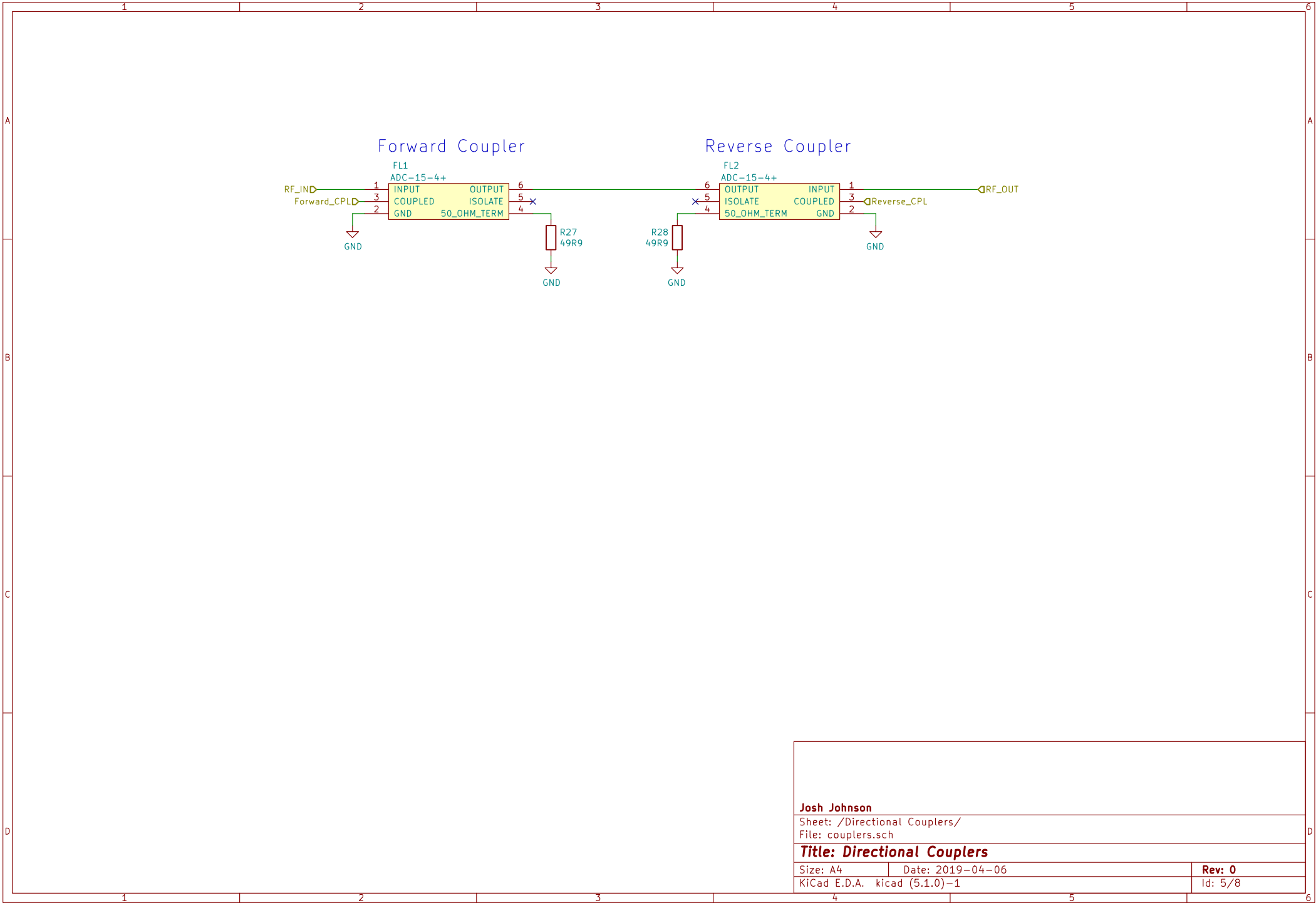
Title: Source Leveling

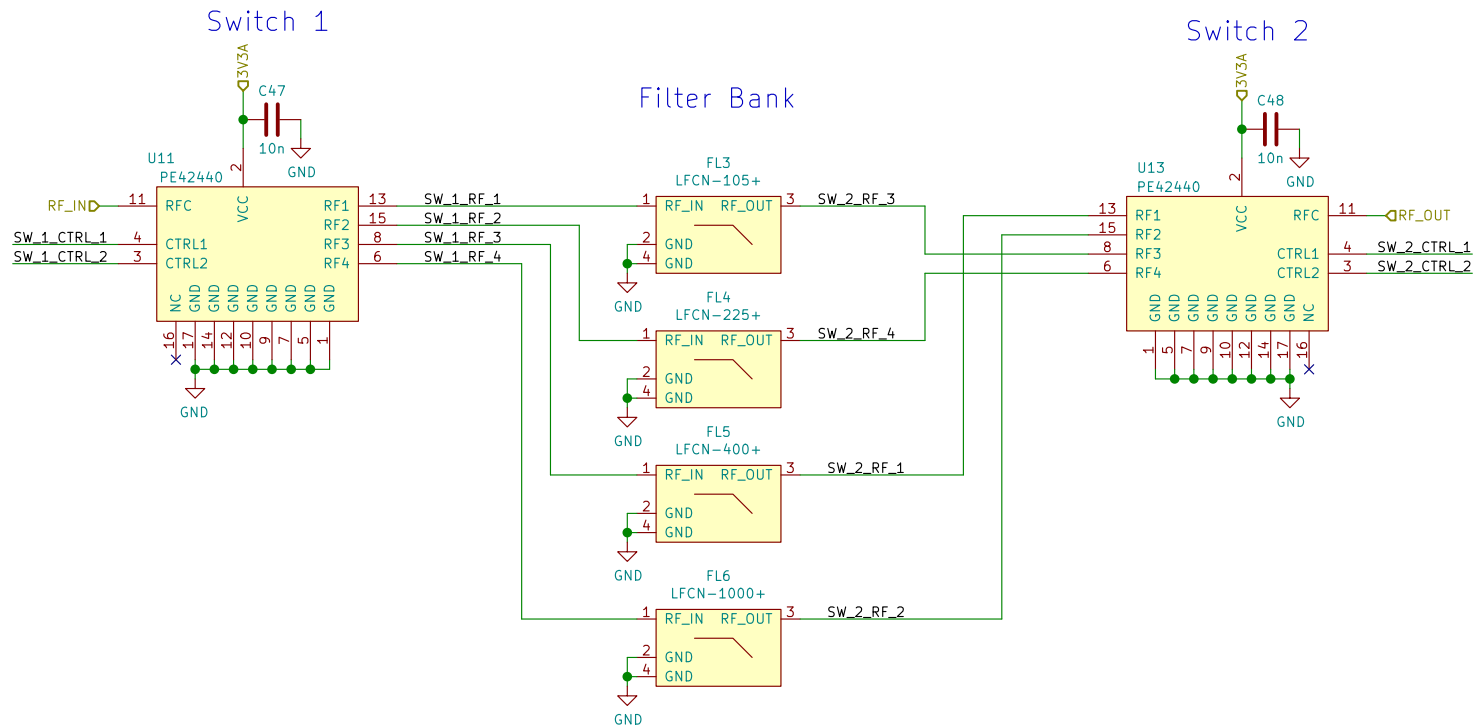
Size: A4 Date: 2019-04-06

KiCad E.D.A. kicad (5.1.0)-1

Rev: 0

Id: 4/8

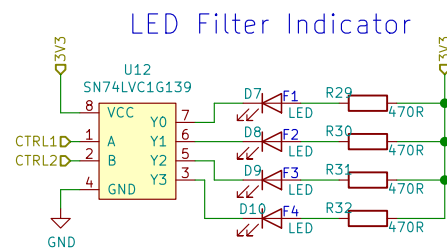
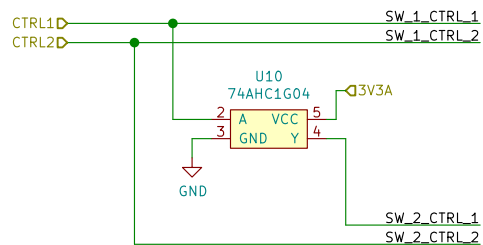




Switch Configuration

74AHC1G04 is a not gate, used to allow the below truth tables to be correct with only two control lines following the values in the first table

SW_1	CTRL_1	CTRL_2	SW_2	CTRL_1	CTRL_2
1	0	0	3	1	0
2	0	1	4	1	1
3	1	0	1	0	0
4	1	1	2	0	1



Josh Johnson

Sheet: /Filter Bank/
File: filterBank.sch

Title: Filter Bank

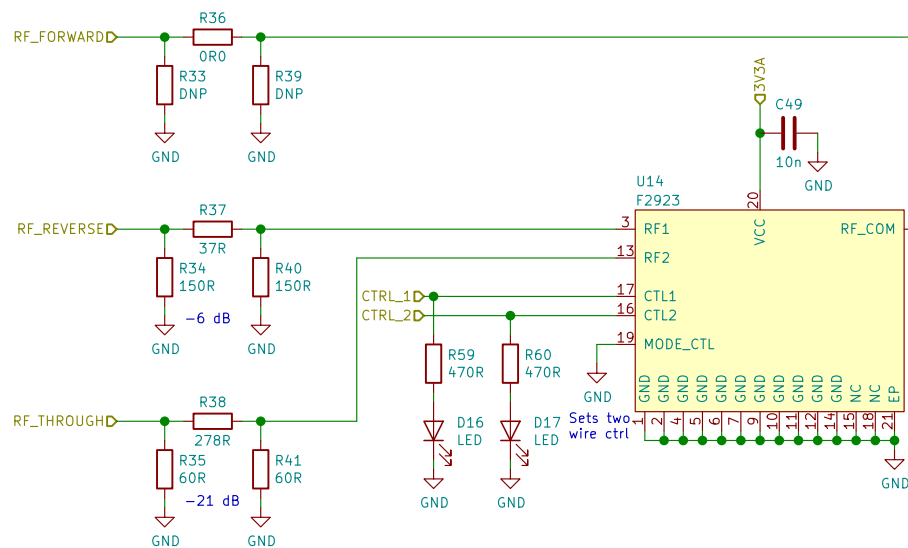
Size: A4 Date: 2019-04-06

KiCad E.D.A. kicad (5.1.0)-1

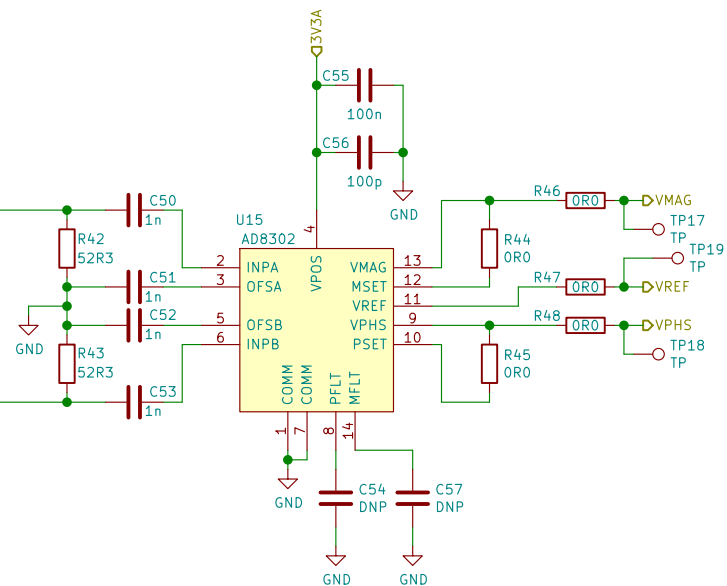
Rev: 0

Id: 6/8

Path Select



AD8302 Gain Phase Detector



Josh Johnson

Sheet: /Gain Phase Detection/
File: gainPhaseDetect.sch

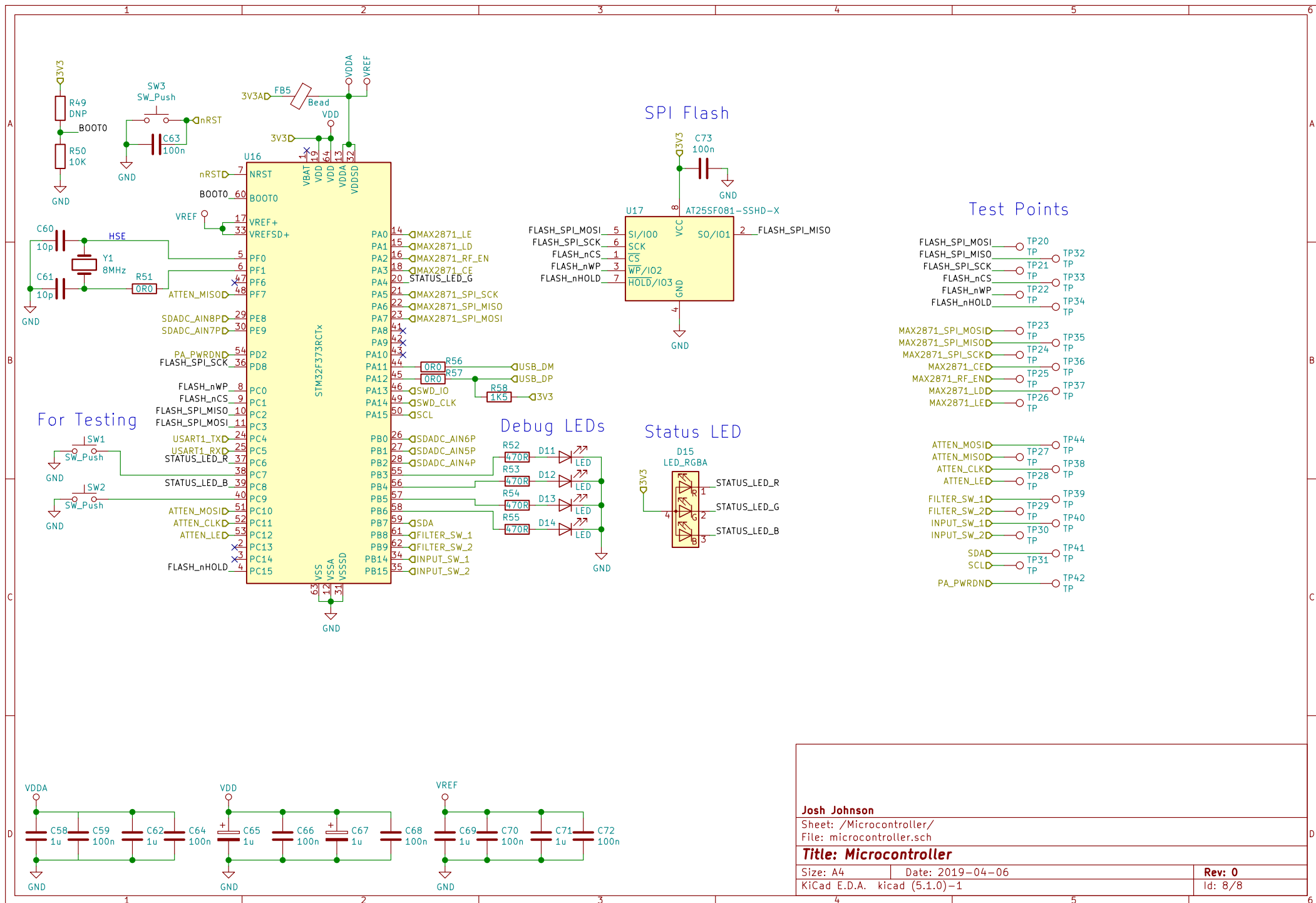
Title: Gain Phase Detector

Size: A4 Date: 2019-04-06

KiCad E.D.A. kicad (5.1.0)-1

Rev: 0

Id: 7/8



Josh Johnson

Sheet: /Microcontroller/
File: microcontroller.sch

Title: Microcontroller

Size: A4 Date: 2019-04-06

KiCad E.D.A. kicad (5.1.0)-1

Rev: 0

Id: 8/8