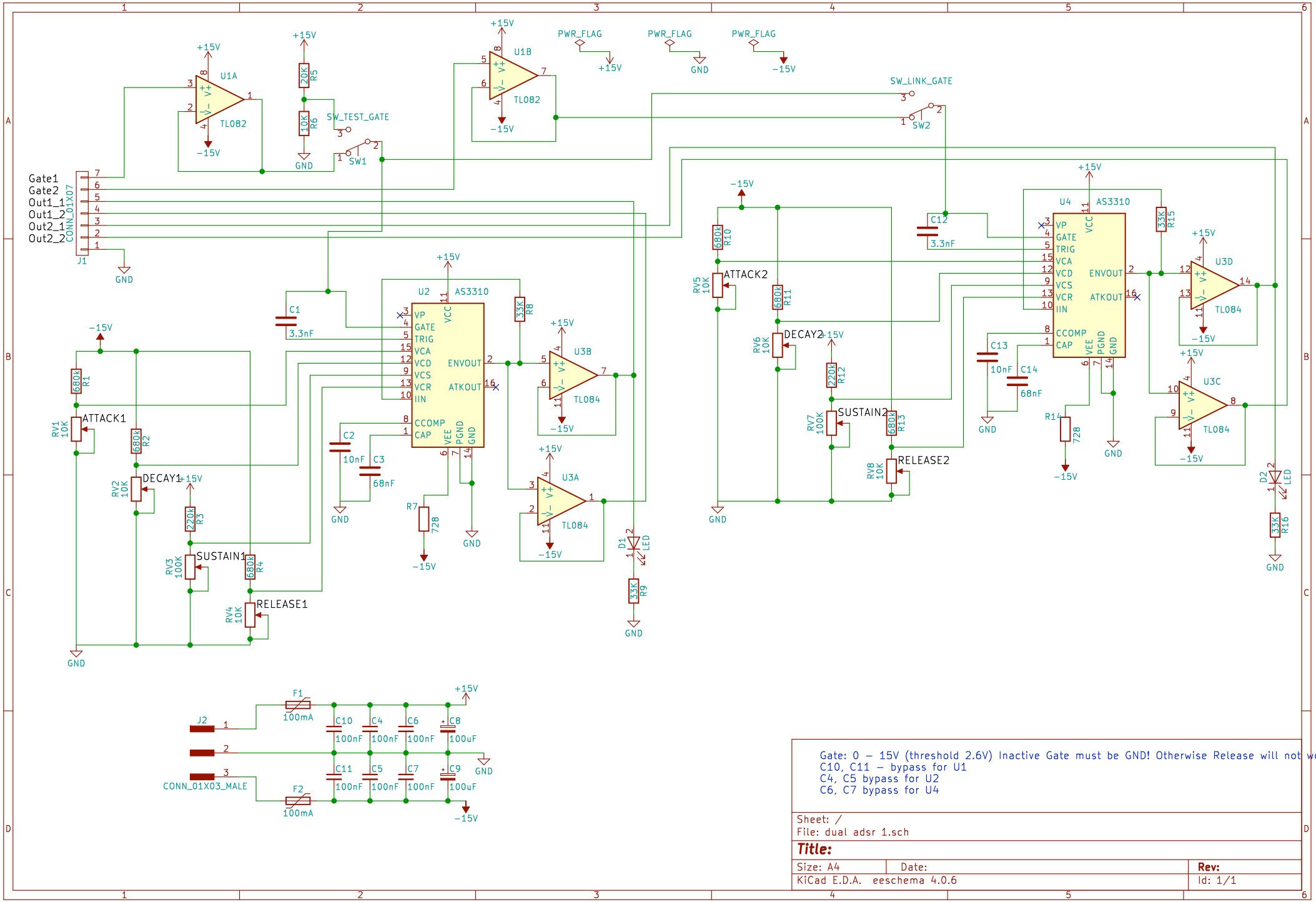
Gate1
Gate2
Out1_1
Out1_2
Out2_1
Out2_2
CONN_01X07
J1
U1A
TL082
U1B
TL082
SW_TEST_GATE
SW1
SW_LINK_GATE
SW2
PWR_FLAG
U2
AS3310
U3A
U3B
U3C
U3D
TL084
U4
AS3310
ATTACK1
DECAY1
SUSTAIN1
RELEASE1
ATTACK2
DECAY2
SUSTAIN2
RELEASE2
LED
J2
CONN_01X03_MALE
F1
F2
100mA
Gate: 0 – 15V (threshold 2.6V) Inactive Gate must be GND! Otherwise Release will not work.
C10, C11 – bypass for U1
C4, C5 bypass for U2
C6, C7 bypass for U4
Sheet: /
File: dual adsr 1.sch
Title:
Size: A4
Date:
Rev:
KiCad E.D.A. eeschema 4.0.6
Id: 1/1