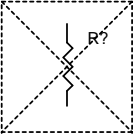


5
NOTES: UNLESS OTHERWISE SPECIFIED.

1. Components marked with a dashed and crossed box will not be installed during assembly.



2. Please refer to the 10106052 - Powerpack board (dual battery) Bill of Material (BOM) which provides all necessary component information for the final assembly.

3. Bypass capacitors shall be distributed evenly amongst power pins of the IC power and GND pin.

4. Always observe layout guidelines and documentation from the component manufacturers, beside the indications and comments marked on schematics.

5. J1 to J8 conn - Through hole components, place on Top side of the PCB

6. RL1, FL1, J9 & J11 conn - Through hole components, place on Top right side of the PCB

7. J12 conn - SMD component, place on Bottom left side of the PCB

8. Power tracks - marked with thick line on schematic, should be able to withstand minimum 3.3A continuous current route as wide as possible, minimum of 0.6mm.

9. Components names to be marked in silkscreen and also PCB board name, number and revision.

TABLE OF CONTENTS

SHEET	DESCRIPTION
1	Title Page
2	Block Diagram
3	Power supply & Switching
4	Microcontroller

2		REVISIONS		1	
REV	DESCRIPTION			DATE	APPROVED
B	RELEASE PER ECO-R249715			SEE AGILE	SEE AGILE




ORCAD CAPTURE DRAWING -
ALL CHANGES MUST BE DONE IN ORCAD SYSTEM

Revision Changes

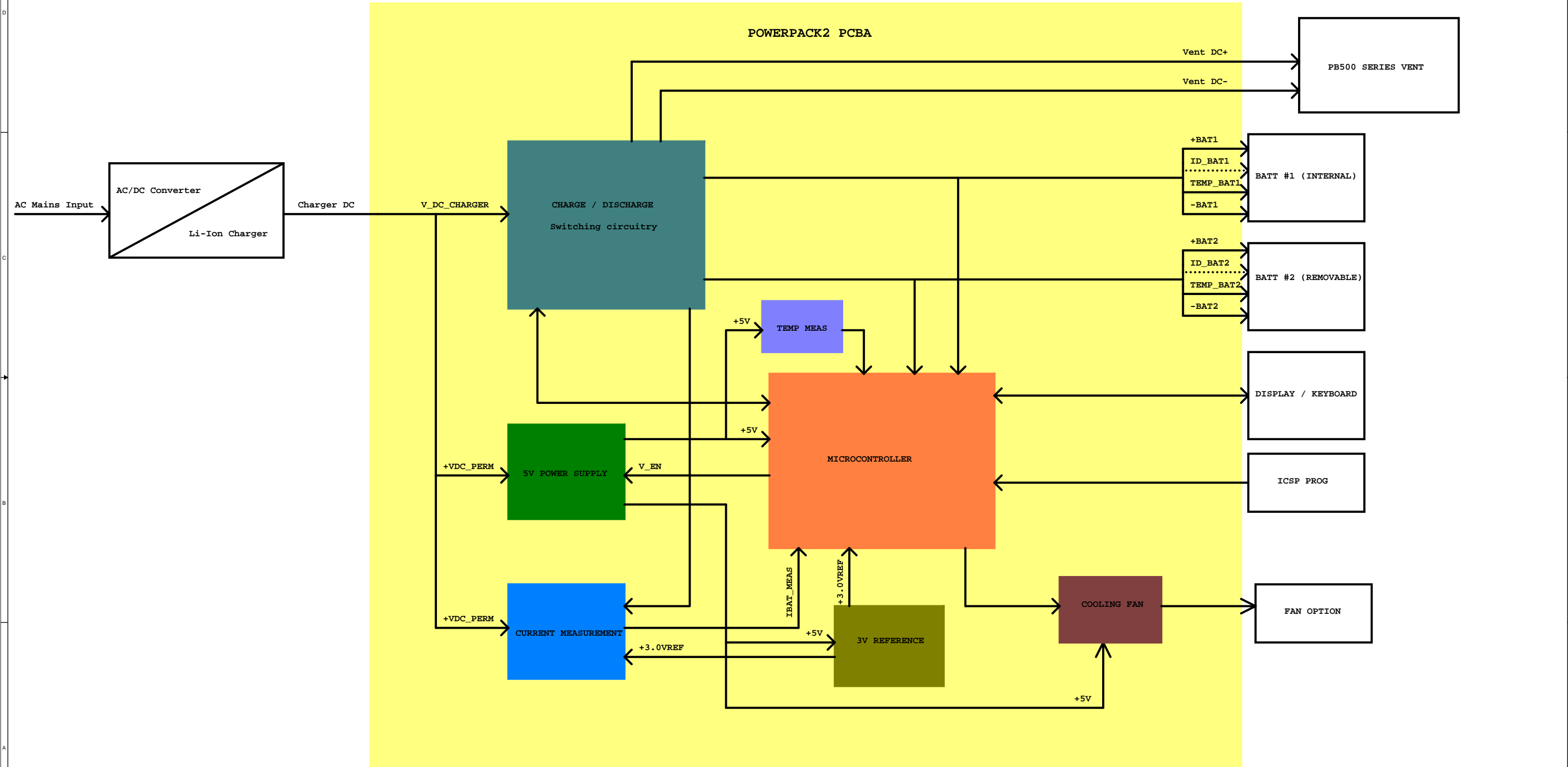
REV	DESCRIPTION
X01	Initial release
X02	D16 added, U1 part number updated to TPS7A1650Q1, C13,C16,C18,C20 to be mounted by default on PCBA.
X03	Removed D16, replaced with T14, T15, R52 to R56. Capacitors C13, C15, C16, C17, C18, C19, C20, C21 removed. R5, R15 value changed to 18k. R41 value changed to 4.7k. R48 value changed to 62k. U1 changed to NCV87725D7S50R4G. Removed RS-232 interface (U5, C26, C27, C28, C29, C31, J14). Deleted TP1 to TP10. R57, R58 added. R4, R14, R50, R51, R57, R58, D15, T12, T13 marked as not installed.
A	Production release. Changed R26 to 68 Ohm & R32 to 2.2k.
B	Changed R32 to 470 Ohm.

Project: **Powerpack2 (dual battery)**


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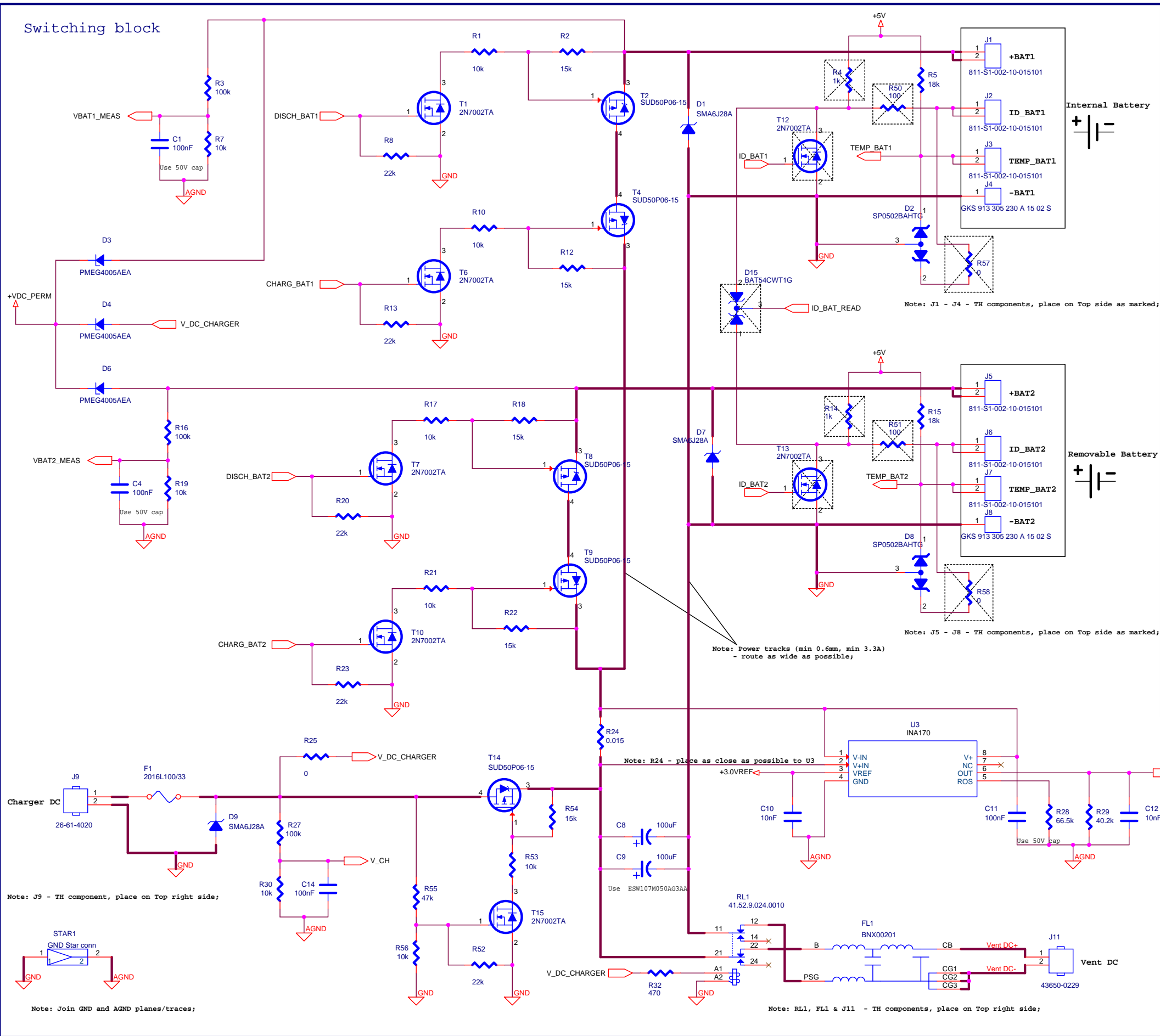
APPROVALS		DATE		 COVIDIEN Carlsbad, CA 92008			
DRAWN:		14-May-2013		SCHEMATIC DIAGRAM POWERPACK2 BOARD, PB500			
ENGINEER:		14-May-2013					
CHECKED	SEE AGILE	SEE AGILE					
APPROVED	SEE AGILE	SEE AGILE					
RELEASED:	SEE AGILE	SEE AGILE		SIZE	MODEL	DWG NO.	REV:
				B	PB500	10105965	B
				SCALE: NONE		Template 10093694 Rev A	SHEET 1 of 4

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
SEE SHEET 1.			

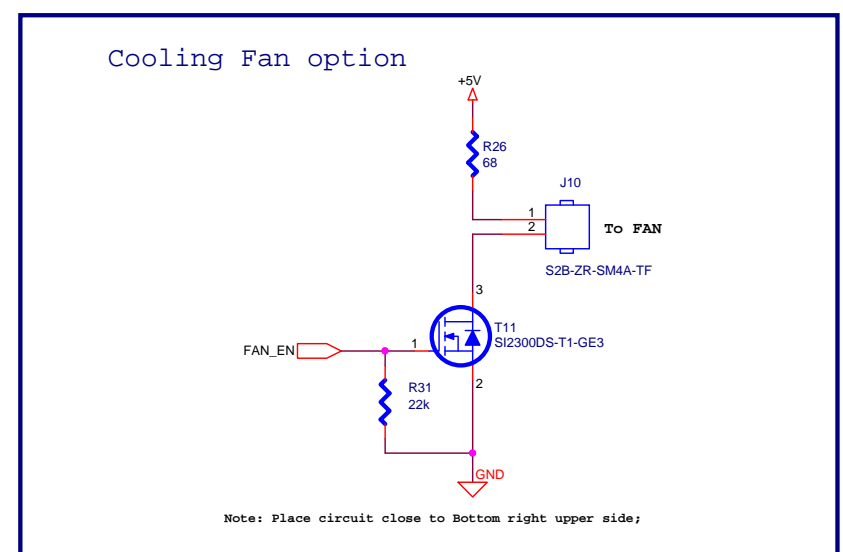
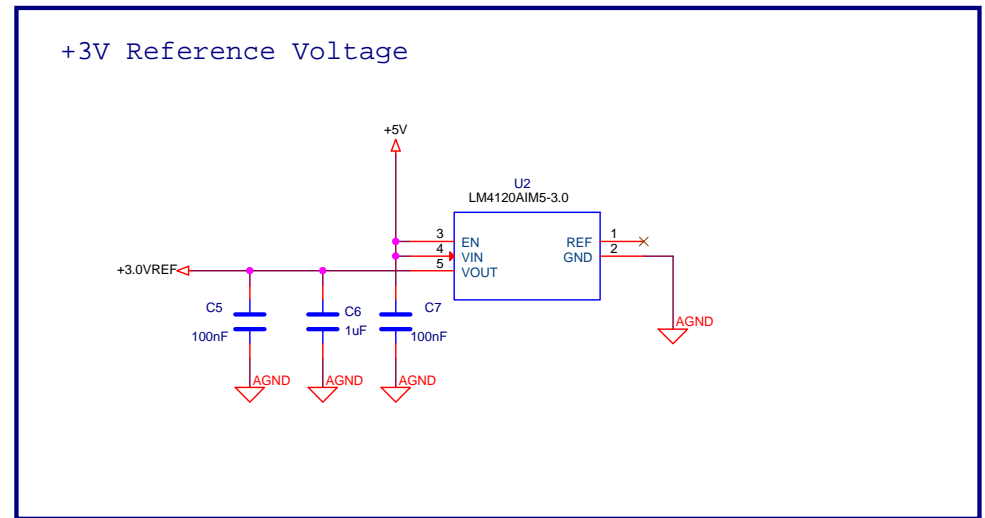
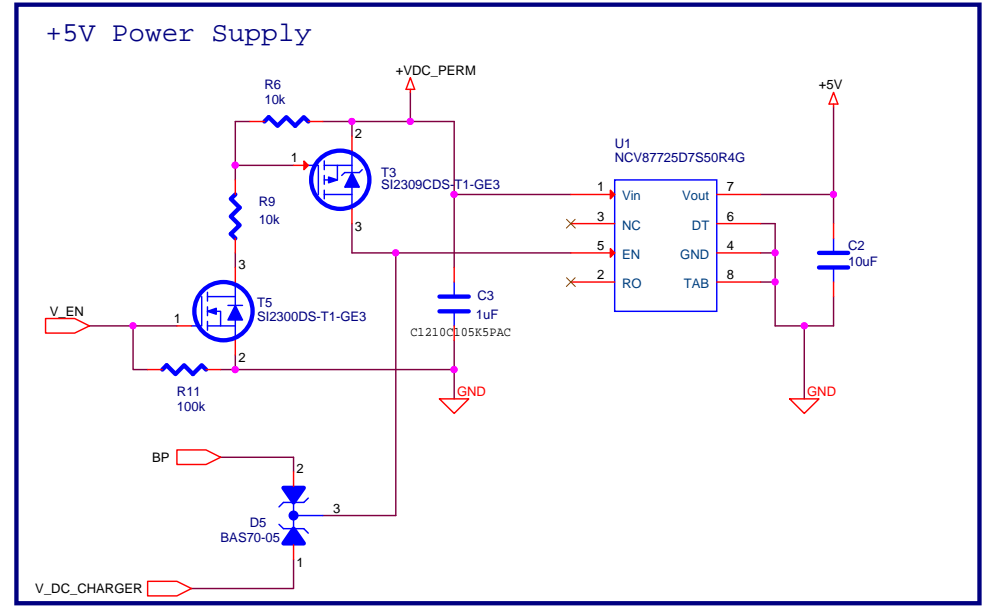


Function: *Block Diagram*

	SIZE	MODEL	DWG NO.	REV:
	B	PB500	10105965	B
SCALE: NONE		Template 10093694 Rev A	SHEET 2 of 4	
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REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
SEE SHEET 1.			

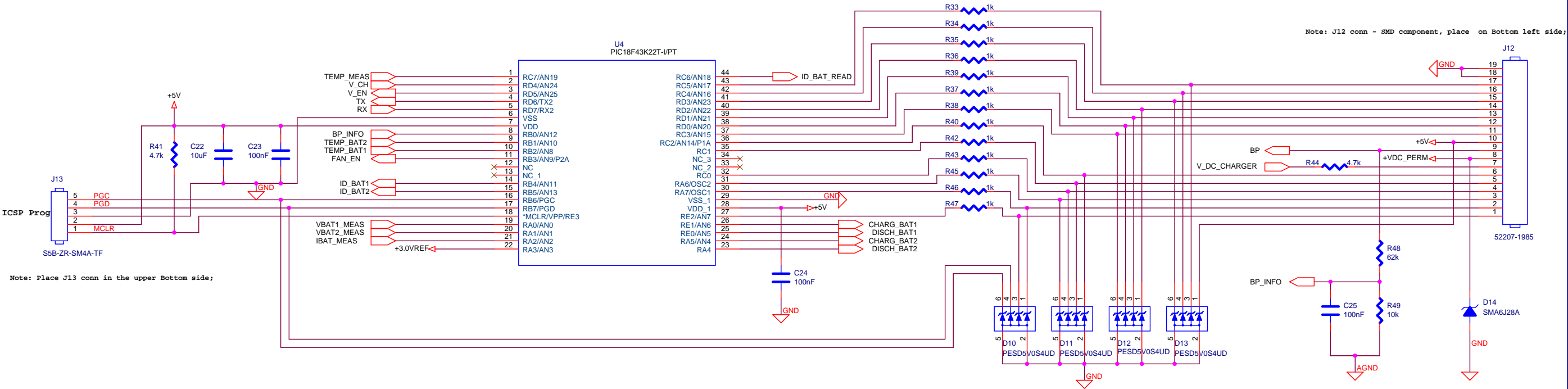


Function: Power Supply & Switching

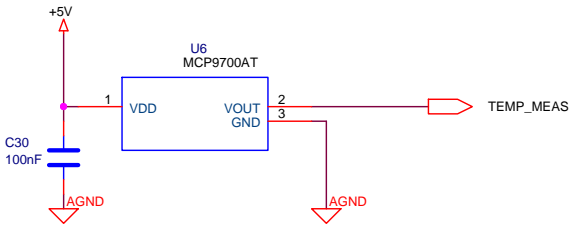
	SIZE	MODEL	DWG NO.	REV:
	B	PB500	10105965	B
SCALE: NONE		Template 10093694 Rev A	SHEET 3 of 4	
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REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
SEE SHEET 1.			

Microcontroller block



Ambient temperature measurement - option



Note: U6 - TH component, place on Top right side;

Function: *Microcontroller*

	SIZE	MODEL	DWG NO.	REV:
	B	PB500	10105965	B
SCALE: NONE		Template 10093694 Rev A	SHEET 4 of 4	

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