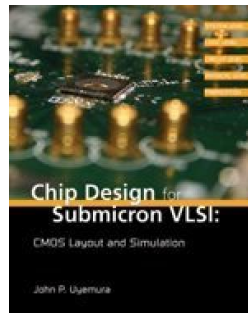


Chip Design for Submicron VLSI: CMOS Layout and Simulation



Book Review

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