Judy Hanwen Shen

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Education

Massachusetts Institute of Technology **Cambridge, MA**
Masters of Media Arts and Sciences *2017–Current*
MIT Media Lab: Scalable Cooperation Group

University of Toronto **Toronto, Canada**
Bachelor of Applied Science in Engineering Science *2012–2017*
High Honors 3.9/4.0 CGPA

Computer Engineering Coursework: Machine Learning, Neural Networks, Natural Language Processing, Algorithms and Data Structures, Signals Processing, Operating Systems, Computer Security, Computer Architecture

Honours and Awards

Deans's Honour List Fall 2012 – Spring 2017
Recognizing students with honors standing in a given semester

C. William Daniel Leadership Award October 2015
Awarded for academic standing, leadership qualities and community involvement

AJB Software Kick-Start Award October 2014
Awarded for academic merit and entrepreneurial spirit

Peter Sands Award in Engineering Science November 2014
Awarded for qualities of character, leadership and commitment to profession of Engineering

Class of 5T0 Engineering Leadership Award October 2013
Awarded for high academic performance who exhibit leadership potential and volunteerism

Center for International Experience Research Grant June 2013
Research grant for summer research project at National University of Singapore

Faculty of Applied Science and Engineering Award October 2012
Awarded for academic achievements

Avie Bennett Award September 2012
Awarded for exceptional academic achievement

Research Experience

- **Detecting Anxious Behavior on Reddit**
  Supervisor: Frank Rudzicz *May 2016–May 2017*

  I am currently using data mining and natural language tools to study communication patterns of Reddit users with anxiety. This includes three sub objectives of 1) to identify the difference between anxious Reddit posts and regular Reddit posts in linguistic expression, 2) to evaluate a user's level of anxiety based on natural language patterns and 3) to study how fluctuating levels of anxiety affect patterns of expression over time. Currently using: NLTK, SciPy, NumPy, Matplotlib, Gensim

- **Recreating great discoveries and inventions in physics and engineering**
  Supervisor: Anjam Khursheed *May 2013–August 2013*

  I developed hands-on demos/experiments that can assist the teaching of physics/engineering principles in schools, polytechnics and universities in Singapore. Specifically, I designed an oscillating cylinder compressed-air engine, a pendulum-weight mechanical clock and a vacuum pump and chamber

Publications

Judy Hanwen Shen & Frank Rudcicz, "Detecting Anxiety Through Reddit."
Proceedings of the Workshop on Computational Linguistics and Clinical Psychology *August 2017*
Association of Computational Linguistics 2017 (Accepted/Pre-Publication)

Teaching Experience

Teaching Assistant
University of Toronto Department of Computer Science *Spring 2017*
Digital Systems and Computer Organization CSC258

Employment Experience

- **Technical Program Manager Intern** **Microsoft**
  Seattle, USA *June 2017 – Current*

  Working on event sourcing database architecture features on CosmosDB noSQL database team in Azure Data Platform Group.

- **Program Manager Intern** **Microsoft**
  Seattle, USA *May 2015–August 2015, May 2016–August 2016*

  - Conceptualized, designed and shipped full implementation of suggested groups interface and algorithm in Outlook Groups feature. Worked closely with engineering, UI design, product marketing and user research teams to deliver full feature to production.

  - Delivered prototype for integrating AI answer bot into the Outlook conversation experience powered by Bing NLP algorithms. Developed vision with partner teams for overall bot integration strategy across all Outlook experiences.

- **Embedded Software Engineer Intern** **Verity Studios**
  Zurich, Switzerland *August 2015 – May 2016*

  Designed, implemented and tested various drivers for autonomous quadcopter system including CRC, LED, Can Bus, EEPROM, IMUs and Bootloaders. Analyzed performance of embedded programs on TI microprocessor using advanced trace modules. Soldered customized components to test various microprocessor integrated PCB units. Automated testing of embedded memory peripherals using relay components.

- **Software Engineer Intern** **Intel**
  San Jose USA *May 2014 – August 2014*

  Worked on High Speed Serial Interface transceiver team to design algorithms and tools to support FPGA development. Developed tool to automate flow of generating maps that connect physical circuit elements to software elements using python and eclipse. Wrote full documentation for tool and TCL scripts to simplify the operation of the tools.