**BIRLA INSTITUTE OF TECHNOLOGY AND SCIENCE, PILANI**
**DEPARTMENT OF COMPUTER SCIENCE AND INFORMATION SYSTEMS**
**Compiler Construction (CS F363)**
**II Semester 2022-23**
**Compiler Project (Stage-2 Submission)**
**Coding Details**
**(April 12, 2023)**

**Group number _____50_____(Write your group number here)**

*Instruction: Write the details precisely and neatly. Places where you do not have anything to mention, please write NA for Not Applicable.*

1. IDs and Names of team members

   ID:____ 2019B3A70489P_____ Name:_____Harsh Pandey _____

   ID:_____2019B3A70470P _____ Name:_____Aryan Puwar _____

   ID:_____ 2019B3A70562P _____ Name:_____Tejas Deshpande _____

   ID:_____ 2019B2A70898P _____ Name:_____Harshit Gupta _____

   ID:_____ 2019B3A70819P _____ Name:_____Krish Vora _____

2. Mention the names of the Submitted files ( Include Stage-1 and Stage-2 both)

   1 _grammar.txt 7___structs.h_____ 13____t1.txt_____ 19_____t7.txt_____
   2_____lexer.c_____ 8_lexer.h_____ 14_____t2.txt_____ 20____t8.txt_____
   3____driver.c_____ 9___ast.c___ 15__t3.txt_____ 21_____t9.txt____
   4__makefile_____ 10_dfa.pdf_____ 16_____t4.txt_____ 22_____t10.txt_____
   5__parser.c_____ 11__firstfollow.pdf_____ 17___t5.txt_____ 23_____c1.txt_____
   6__parser.h_____ 12_cocoastgrp50.pdf_____ 18___t6.txt_____ 24_____c2.txt_____
   25 c3.txt 26 c4.txt 27 c5.txt 28 c6.txt 29 c7.txt 30 c8.txt 31 c9.txt 32 c10.txt 33 c11.txt

3. Total number of submitted files: ____33____ (All files should be in **ONE** folder named exactly as Group number)
4. Have you mentioned names and IDs of all team members at the top of each file (and commented well)? (Yes/ no) ____Yes _____ [Note: Files without names will not be evaluated]
5. Have you compressed the folder as specified in the submission guidelines? (yes/no)_____Yes _____

6. **Status of Code development**: Mention 'Yes' if you have developed the code for the given module, else mention 'No'.
   a. Lexer (Yes/No): _____Yes_____
   b. Parser (Yes/No):_____Yes _____
   c. Abstract Syntax tree (Yes/No):____Yes_____
   d. Symbol Table (Yes/ No):_____Yes_____
   e. Type checking Module (Yes/No):____No_____
   f. Semantic Analysis Module (Yes/ no):_____No _____(reached LEVEL _NA_ as per the details uploaded)
   g. Code Generator (Yes/No):_____No_____

7. **Execution Status**:
   a. Code generator produces code.asm (Yes/ No):NA
   b. code.asm produces correct output using NASM for testcases (C#.txt, #:1-11): NA

c. Semantic Analyzer produces semantic errors appropriately (Yes/No):____NA_____

d. Static Type Checker reports type mismatch errors appropriately (Yes/ No):___Not properly functioning_____

e. Dynamic type checking works for arrays and reports errors on executing code.asm (yes/no): __No_____

f. Symbol Table is constructed (yes/no)_Yes____and printed appropriately (Yes /No):___Yes_____

g. AST is constructed (yes/ no) __Yes_____and printed (yes/no) __Yes____

h. Name the test cases out of 21 as uploaded on the course website for which you get the segmentation fault (t#.txt ; # 1-10 and c@.txt ; @:1-11):_____

8. **Data Structures** (Describe in maximum 2 lines and avoid giving C definition of it)
   a. AST node structure So we are basically storing the value of each node and an attribute name as syn list and syn address. So basically some ast nodes have syn list and others have address.

   b. Symbol Table structure: It is basically similar to linked list of a structure that has all the attributes that are required for a symbol table row.

   c. array type expression structure: It is incorporated in the symbol table only. We have used a bool attribute as isArray is that is true than it will have all the fields required for the array type.

   d. Input parameters type structure: It is also incorporated in the symbol table data structure only.

   e. Output parameters type structure: It is also incorporated in the symbol table data structure only.

   f. Structure for maintaining the three address code(if created) :_____

      _____

9. **Semantic Checks:** Mention your scheme NEATLY for testing the following major checks (in not more than 5-10 words)[ Hint: You can use simple phrases such as 'symbol table entry empty', 'symbol table entry already found populated', 'traversal of linked list of parameters and respective types' etc.]
   a. Variable not Declared :symbol table entry empty

   b. Multiple declarations: symbol table entry already found populated

   c. Number and type of input and output parameters:symbol table entry already found populated

   d. assignment of value to the output parameter in a function symbol table entry already found populated

   e. function call semantics:symbol table entry already found populated

   f. static type checking :

   g. return semantics:_____

   h. Recursion :_____

   i. module overloading:_____

   j. 'switch' semantics :_____

   k. 'for' and 'while' loop semantics: _____

   l. handling offsets for nested scopes:_____

   m. handling offsets for formal parameters:_____

   n. handling shadowing due to a local variable declaration over input parameters:_____

      _____

   o. array semantics and type checking of array type variables: _____

_____

p. Scope of variables and their visibility :_____

q. computation of nesting depth:_____

10. Code Generation:
    a. NASM version as specified earlier used (Yes/no):____No_____
    b. Used 32-bit or 64-bit representation:_____NA_____
    c. For your implementation: 1 memory word = _____NA_____(in bytes)
    d. Mention the names of major registers used by your code generator:
        - For base address of an activation record: _____
        - for stack pointer:_____
        - others (specify):_____
    e. Mention the physical sizes of the integer, real and boolean data as used in your code generation module
       size(integer): _____(in words/ locations), _____(in bytes)
       size(real): _____(in words/ locations), _____(in bytes)
       size(booelan): _____(in words/ locations), _____(in bytes)

    f. How did you implement functions calls?(write 3-5 lines describing your model of implementation)
       _____

       _____

       _____

    g. Specify the following:
        - Caller's responsibilities:_____
        - Callee's responsibilities:_____

    h. How did you maintain return addresses? (write 3-5 lines): _____

       _____

       _____

       _____

    i. How have you maintained parameter passing? How were the statically computed offsets of the parameters used by the callee? _____

    j. How is a dynamic array parameter receiving its ranges from the caller? _____

    k. What have you included in the activation record size computation? (local variables, parameters, both):
       _____

    l. register allocation (your manually selected heuristic) :_____
       _____

    m. Which primitive data types have you handled in your code generation module?(Integer, real and boolean):_____

    n. Where are you placing the temporaries in the activation record of a function? _____
       _____

11. **Compilation Details**:
    a. Makefile works (yes/No):__Yes_____
    b. Code Compiles (Yes/ No):__Yes_____

c. Mention the .c files that do not compile:_____

d. Any specific function that does not compile:_____

e. Ensured the compatibility of your code with the specified versions [GCC, UBUNTU, NASM] (yes/no)_____Yes__

12. Execution time for compiling the test cases [lexical, syntax and semantic analyses including symbol table creation, type checking and code generation] :
    i. t1.txt (in ticks) 2086 and (in seconds) 0.002086
    ii. t2.txt (in ticks) 3231 and (in seconds) 0.003231
    iii. t3.txt (in ticks) 7674 and (in seconds) 0.007674
    iv. t4.txt (in ticks) 6656 and (in seconds) 0.006656
    v. t5.txt (in ticks) 8798 and (in seconds) 0.008798
    vi. t6.txt (in ticks) and (in seconds)
    vii. t7.txt (in ticks) 4332 and (in seconds) 0.004332
    viii. t8.txt (in ticks) 8763 and (in seconds) 0.008763
    ix. t9.txt (in ticks) and (in seconds)
    x. t10.txt (in ticks) 4387 and (in seconds) 0.004387

13. **Driver Details**: Does it take care of the **TEN** options specified earlier?(yes/no):__Not all_____
14. Specify the language features your compiler is not able to handle (in maximum one line)
    Code generation part is not handled_____
15. Are you availing the lifeline (Yes/No): ____No_____
16. Write exact command you expect to be used for executing the code.asm using NASM simulator [We will use these directly while evaluating your NASM created code]
    NA
    _____
17. **Strength of your code**(Strike off where not applicable): (a) correctness (b) completeness (c) robustness (d) Well documented (e) readable (f) strong data structure (f) Good programming style (indentation, avoidance of goto stmts etc) (g) modular (h) space and time efficient
18. Any other point you wish to mention: _____

    _____

    _____

19. Declaration: We, Harsh Pandey, Aryan Puwar, Tejas Deshpande, Harshit Gupta, Krish Vora declare that we have put our genuine efforts in creating the compiler project code and have submitted the code developed only by our group. We have not copied any piece of code from any source. If our code is found plagiarized in any form or degree, we understand that a disciplinary action as per the institute rules will be taken against us and we will accept the penalty as decided by the department of Computer Science and Information Systems, BITS, Pilani.
    [Write your ID and names below]

ID_____2019B3A70489P_____ Name:____Harsh Pandey _____
ID_____2019B3A70470P _____ Name:____Aryan Puwar _____
ID_____2019B3A70562P _____ Name:_____Tejas Deshpande _____
ID_____2019B2A70898P _____ Name:_____Harshit Gupta _____

ID_____2019B3A70819P _____ Name:_____Krish Vora _____

Date: ____12/03/2023___ Group number_____50_____

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Should not exceed 6 pages.