

Harry Leung

Software Engineer

408-768-9633 | hleung.cs@gmail.com | Milpitas, CA

<https://hleung.vercel.app>

EDUCATION

University of California, Irvine

Sep. 2021 – Jun. 2025

Bachelor of Science in Computer Science, In Progress

Relevant Coursework: Neural Networks & Deep Learning, Operating Systems, Database Systems

Artificial Intelligence, Machine Learning, Information Retrieval, Data Structures & Algorithms, Networks

WORK EXPERIENCE

Litepoint

Jun. 2023 – Sep. 2023

Software Engineering Intern, QA Automation

San Jose, CA

- Developed a user-friendly GUI tool to display regression tester values, enhancing data visualization and decision-making.
- Implemented Apache Cassandra to optimize storage and retrieval of regression tester data, maximizing data reliability and reducing query response time by over 96%.
- Designed and created an intuitive and responsive GUI using Tkinter, improving user interaction and reducing the learning curve for the team.
- Integrated Matplotlib for data visualization, enabling the creation of insightful graphs to enhance data analysis.
- Improved team efficiency significantly by deploying the tool, facilitating quicker decision-making and higher-quality regression testing outcomes.

PROJECTS

ICS Web Crawler | Python, Web Crawling, Distributed Systems

- Configured a scalable web crawler leveraging a spacetime cache server for distributed URL fetching on the UC Irvine ICS domain.
- Implemented custom scraper rules to filter, parse, and enqueue valid web URLs.
- Designed flexible architecture with re-definable frontier and worker classes for multi-threaded crawling.
- Enforced configurable politeness delays to comply with server request limits.

Virtual Memory Manager | Operating Systems, Memory Management, LFU Caching, Virtualization

- Built a virtual memory manager simulating two-level paging and dynamic memory allocation.
- Implemented 'mmap', 'free', and 'realloc' to manage physical memory blocks with fragmentation-aware policies.
- Designed and integrated LFU (Least Frequently Used) page replacement logic for page faults and allocation retries when memory is full.
- Handled page faults by loading missing pages from simulated disk and maintaining page table mappings.
- Simulated backing storage with disk-mapped page tables and pages, swapping to and from memory as needed during translation.

AI Art Detector | Python, Deep Learning, PyTorch, Transformers

- Designed deep learning pipelines to distinguish AI-generated images from human-created artwork.
- Built and trained custom CNN and Vision Transformer models for binary image classification.
- Implemented data augmentation, normalization, and experiment tracking using WandB.
- Optimized training routines to compare model performance and achieve robust classification accuracy.

SKILLS

Languages: Python, C++, C, Rust, JavaScript, Typescript, Go, Java, Swift, x86/ARM Assembly, SQL, HTML

Libraries: Keras, NumPy, LLVM, OpenGL, React, Node.js, Redis

Developer Tools: Git, Linux, Docker, Bazel, CMake, Makefile, CI, Valgrind, GDB, Radare2, Tracy, libfuzzer, ASAN, React.js, Next.js, Tailwind CSS, Git, GitHub, MySQL, PostgreSQL, MongoDB

Spoken Languages: English, Cantonese, Mandarin