

Test Your Limits With TRex Traffic Generator

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Abstract

Performance measurement tools are an integral part of network testing. There is no shortage of open source tools for network performance testing in the Linux world. To enumerate a few popular tools in the Linux world: Netperf, iperf, Linux kernel based pktgen. These tools tend to fall into two categories:

- Stateless packet shooting such as the Linux kernel pktgen traffic generator
- Stateful client-server tools such as netperf and iperf.

When very high performance network performance testing is required (quantified as many 10s of Gigabits per second/100MPPS and/or hundreds of thousands of flows) or more advanced functionality (e.g. realistic) is required the Linux classical tools are insufficient. Most organizations will opt for very expensive commercial tools such as Ixia, Spirent. In this paper we will introduce TRex a high performance realistic traffic generator and illustrate sample stateless and stateful use cases that apply to testing Linux networking. We will also discuss its design and tricks that help us achieve such high performance.

Keywords

tcp, performance, scale, realistic traffic generation

Introduction

TRex [1] is an advanced traffic generator, it has the following interesting features:

- It leverages COTS x86/ARM servers and physical NICs (Intel, Mellanox etc) for high scale.
- Can support Linux driver or virtual (e.g. virtio/veth) for low scale with advanced features (PF_PACKET)
- It can serve both stateless and stateful traffic generation. tcp stack for stateful traffic and emulation layer to simulate L7 applications.
- It outperforms all of iperf, netperf and kernel pktgen:
 - It can generate upto 200gbps/100mpps advance complex patterns and millions of real world tcp/udp flows.
 - High connection rate - order of Millions of Connection/s (CPS).
- It is extensible:
 - Can emulate L7 application (on top of TCP/IP), e.g. HTTP/HTTPS/Citrix using a DSL programmable language described in Python
 - Ability to change fields in the L7 application - for example, change HTTP User-Agent field
- Support routing protocols like BGP/OSPF/RIP using BIRD [4]
- Support high scale clients simulation protocols like arp, ipv6-nd, dhcp4, dhcp6, 802.1x, icmp, igmp, mld using a new process written in golang

Although TRex is implemented on top of DPDK, a lot of the issues we had to deal with when writing the tool apply equally to scaling Linux networking; we share our experiences in that regard and hope to inspire some of the techniques to be adopted in Linux.

Software design high level

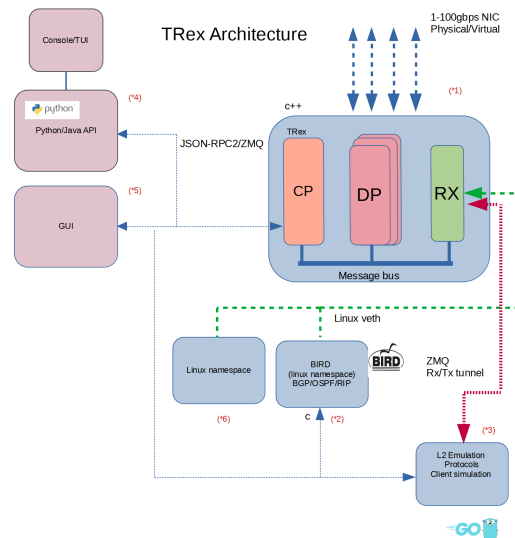


Figure 1: Architecture

Figure 1 presents the main processes. TRex server (*1) is a multi-threaded process, each thread is pinned to a core and

works in event driven fashion using a user-space scheduler with a few levels of hierarchy. There is one control-plane (CP) thread that handles the RPC over ZMQ requests and maintenance tasks. The RX thread is responsible for accurate latency measurement. This thread usually consumes very low CPU utilization. The DP threads generate the traffic using DPDK to transmit/receive traffic via PMD queues. The number of DP threads can be scale up to the number of available Tx/Rx queues. There is almost no memory sharing data structure and no locks to get to best performance. Almost all the information between the threads is exchanged using a messaging bus which is composed of a shared rings (DP→CP, CP→DP, Rx→DP, RX→CP). The system calls to the kernel are kept at minimum(only when required for example with PF_PACKET/AF_XDP driver) (*4) is a Python wrapper to the JSON-RPC2 API over ZMQ to supply easy automation (e.g. load a profile, get statistics etc) on top of Python API there is a Console that can simplify the way to work with the API. (*5) is the GUI written in Java that works directly with the JSON-RPC2 and supports only the stateless mode. (*2) is a BIRD [4] process that works inside a Linux namespace and connects to TRex via a programmable veths. Inside TRex's RX thread there is a Switch that forwards packets to/from the veth related to the Linux namespace. BIRD is used to simulate routing protocols like BGP/OSPF/RIP. (*6) and (*3) is used for simulating clients slow-path protocols like ARP/IPV6-ND/IGMP/MLD/802.1x/DHCP/DHCPv6 while TRex server is for the fast-path high speed TCP/UDP

Dataplane scheduler

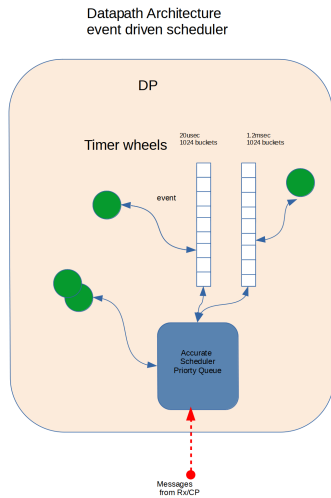


Figure 2: Dataplane Scheduler

Figure 2 presents the schedulers in each DP thread. The priority queue is the low level scheduler that can schedule events at nsec resolution. In addition there are two timer wheels for lower resolution events. The first timer wheel has a resolution of 20usec with 1024 buckets for maximum of 2msec time. The second timer wheel has resolution of 1msec

with 1024 buckets and maximum of 1sec which cover most of the timer duration needed. Each event in the second level is spread each 20usec tick to reduce processing spikes. The DPs transmit/receive messages from the share rings using events. This design achieves linear scale with of performance about 4-20MPPS/core and 200gbps for one COGS server.

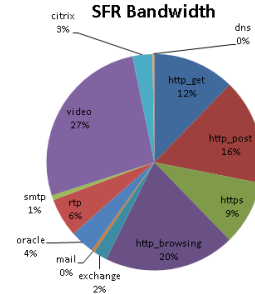


Figure 3: emix

Operation modes

From the functionality point of view TRex has two main operation modes: stateful and stateless.

Stateful is meant for testing L7 services that care about clients/flows/L7 application like DPI/NAT/Firewall, Figure 3 is an example of a mix of traffic that can be generated using this mode.

Stateless is meant for testing Switch/Filters/ACL/QoS services and has no flow/client state context.

Stateless mode

Stateless mode is meant to test networking gear/feature that does not manage any state per flow (instead operating on a per packet basis). This is usually done by injecting customized packet streams to the device under test.

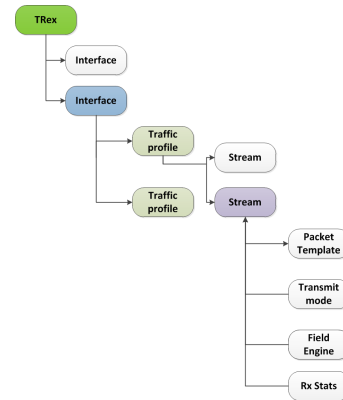


Figure 4: Stateless main objects

Figure 4 shows the model of a profile. Each interface supports one or more traffic profiles in parallel. Each traffic profile supports one or more streams. Each stream includes the following main properties:

- **Packet:** Packet template up to 9 KB
- **Field Engine:** A program that determines which field to change and how to change
- **Mode:** Specifies how to send packets {Continuous, Burst, Multi-burst}
- **Rx Stats:** Which statistics to collect for each stream
- **Rate:** Rate (pps or bps)
- **Action:** Specifies stream to follow when the current stream is complete (valid for Continuous or Burst modes)

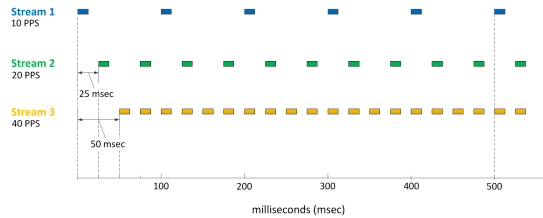


Figure 5: Stateless profile example

Figure 5 shows an example of a profile with three streams. Stream 1 has a rate of 10pps stream 2 has a rate of 20 pps and stream 3 has rate of 40 pps. All are configured for continuous mode.

Listing 1 shows a simplified profile that match Figure 5. The mode is Continuous. It uses Scapy for building the template packet. This profile is converted to json and sent to the TRex server for processing.

Stateless Features

- Large scale - Supports about 10-22 million packets per second (mpps) per core, scalable with the number of cores
- Support for 1, 10, 25, 40, and 100 Gb/sec interfaces with DPDK or PF_PACKET.
- Support for multiple stateless traffic profiles per interface each profile supports multiple streams.
- Programmable Field Engine to change any field inside the packet template i.e.
src_ip=10.0.0.1..10.0.0.255
- Ability to change the packet size
- API, Console, GUI
- Statistics, per interface, per stream
- Latency and jitter per stream
- Multi-user support

Multi stream profile example

Figure 6 shows two streams. Stream 0 is a burst that activate a multi-burst Stream 1 (With 5 burst of 4 packets). Listing 2 shows the Python script to create this profile

```
class STLS1(object):

def __init__(self):
    self.fsize = 64; # the size of the packet

def create_stream(self):

    # Create base packet and pad it to size
    size = self.fsize - 4; # HW will add FCS
    base_pkt = Ether()/
        IP(src="16.0.0.1",dst="48.0.0.1")/
        UDP(dport=12,sport=1025)
    pad = max(0, size - len(base_pkt)) * 'x'

    return STLProfile(
[ STLStream(isg = 1.0, # start in delay in usec
    packet=STLPktBuilder(pkt=base_pkt/pad),
    mode=STLTXCont(pps=10),
    ),

    STLStream(isg = 2.0,
    packet= STLPktBuilder(pkt=base_pkt/pad),
    mode= STLTXCont(pps=20),
    ),

    STLStream(isg = 3.0,
    packet = STLPktBuilder(pkt=base_pkt/pad),
    mode = STLTXCont(pps=40))
]).get_streams()
```

Listing 1: Profile with one continuous UDP stream

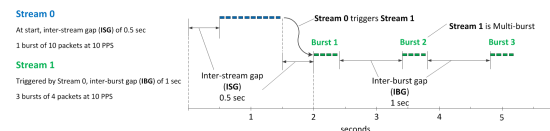


Figure 6: Multi stream profile

Field Engine

The field engine (FE) is a programmable engine that can change any field in the packet and is part of the profile and compiled into bytecode in the TRex server. The challenge was to provide an engine that can change packet fields on a number of cores in parallel but as a black-box it behaves as if it runs on a single core (hardware like). FE works on stateless mode only. Let us provide an example of a syn-attack profile with a simple field engine program and explain it:

Listing 3 shows a FE program that generates a syn-attack using one stream. Each stream object has a context for FE variables. In this example there are two variables, ip_src for the range of the source IPv4 ips and the source_port for the range of the source tcp ports. Those variables are written to the right offset in the packet and the checksum is fixed accordingly (using hardware assist if possible).

```

def create_stream (self):

    # create a base packet and pad it to size
    size = self.fsize - 4 # no FCS
    base_pkt = Ether() /
        IP(src="16.0.0.1",dst="48.0.0.1") /
        UDP(dport=12,sport=1025)
    base_pkt1 = Ether() /
        IP(src="16.0.0.2",dst="48.0.0.1") /
        UDP(dport=12,sport=1025)
    pad = max(0, size - len(base_pkt)) * 'x'

    return STLProfile(
    [ STLStream( isg = 10.0, # start in delay
        name      = 'S0',
        packet = STLPktBuilder(pkt=base_pkt/pad),
        mode = STLTxSingleBurst(pps=10,
                                total_pkts=self.burst_size),
        next = 'S1'), # run s1 after s0

        # stream is disabled. Enabled by S0
        STLStream( self_start = False,
        name      = 'S1',
        packet = STLPktBuilder(pkt=base_pkt1/pad),
        mode = STLTxMultiBurst(pps = 1000,
                                pkts_per_burst = 4,
                                ibg = 1000000.0,
                                count = 5)
        )
    ]).get_streams()

```

Listing 2: Multi profile example

Automation using Python API

Listing 4 shows a simple script to automate TRex. It is self-explanatory.

Stateless Performance

Figure 7 [5] shows the measured performance on a Cisco UCS server with dual socket CPU E5-2667 v3@3.20GHz 8cores/per socket and two Intel XL710 NICS (4 ports total of 4x40gbps)

In Figure 7 IMIX refer to a mix of packets size see [7] with an average packet size of 364 bytes. In case of 64B packet size the maximum L1 utilization is only 52% due to XL710 chip limitation and not CPU/software.

Stateful mode

The stateful model's objective is to simulate realistic L7 applications on top of a TCP/UDP stack at high scale. A fork of a BSD TCP module running in a user space is used for this purpose. The scale could reach millions of flows and 100k clients/servers up to 200gbps for one server. It is important to test stateful features using realistic traffic scenarios because this is the only way to estimate accurate performance metrics and identify bottlenecks in the design.

The high level features are:

- Realistic traffic at high scale (flows, bandwidth, connection per second)

```

class STLS1(object):
    """ attack 48.0.0.1 at port 80 """
    def create_stream (self):

        # TCP SYN
        base_pkt = Ether() /
            IP(dst="48.0.0.1") /
            TCP(dport=80,flags="S")

        # create an empty program (VM)
        vm = STLVM()

        # define two vars
        vm.var(name = "ip_src",
            min_value = "16.0.0.0",
            max_value = "18.0.0.254",
            size = 4,
            op = "random")

        vm.var(name = "src_port",
            min_value = 1025,
            max_value = 65000,
            size = 2,
            op = "random")

        # write src IP and fix checksum
        vm.write(fv_name = "ip_src",
            pkt_offset = "IP.src")

        vm.fix_checksum()

        # write TCP source port
        vm.write(fv_name = "src_port",
            pkt_offset = "TCP.sport")

        # create the packet
        pkt = STLPktBuilder(pkt = base_pkt, vm = vm)

    return STLStream(packet = pkt,
        random_seed = 0x1234,
        mode = STLTxCont())

```

Listing 3: FE syn-attack on 48.0.0.1 server

- Measure latency/jitter/drop in high rate
- Emulate L7 application, e.g. HTTP/HTTPS/Citrix- there is no need to implement the exact application.
- TCP implementation
- Automation Python API
- TCP/UDP/Application statistics (per client side/per template)

Figure 8 presents the main objects in stateful mode Each profile includes:

- **Client pool:** Range of clients with a distribution model (e.g. random,seq). A profile can include a few pools.
- **Server pool:** Range of servers, profile can include a few pools.

```

c = STLClient(username="itay",
              server = "10.0.0.10",
              verbose_level = "error")

try:
    # connect to server
    c.connect()

    # prepare our ports
    c.reset(ports = [0, 1])

    # add both streams to ports
    c.add_streams(s1, ports = [0])

    # clear the stats before injecting
    c.clear_stats()

    c.start(ports = [0, 1],
           mult = "5mpps",
           duration = 10)

    # block until done
    c.wait_on_traffic(ports = [0, 1])

    # check for any warnings
    if c.get_warnings():
        # handle warnings here
    pass

finally:
    c.disconnect()

```

Listing 4: Stateless automation example

- **Template:** A model that describes an application on top of TCP/UDP. Each template could be associated with a different pool of clients/servers. The L7 data that can be extracted from a pcap file and converted to emulation program
 - **CPS:** How many connection per second to generate for this template.
 - **Program:** Emulation instructions to simulate the L7 application e.g. HTTP e.g. writeBuffer/readBuffer
 - **Client/Server pool name** Associate with one of the pools
 - **Statistic pool** The index of the TCP/UDP statistic counters pool. Good for QoS scenarios each template could be associated with different pool of counters

Figure 9 presents the traffic generation model.

In this mode, each core has its own context of TCP/UDP stack with no memory sharing and no locks. It works on top of the scheduler hierarchy shown in Figure 2. The most significant changes to the BSD stack were:

- Each stack has a context per thread. No memory sharing, no locks. GRO/LRO/TSO is supported.
- Tx works in pool mode (it builds the packets only when required) and saves reference to the template data. This saves three orders of magnitude of memory resource.

Packet size	Line Utilization (%)	Total L1 (Gb/s)	Total L2 (Gb/s)	CPU Util (%)	Total MPPS	BW per core (Gb/s) ①	MPPS per core ②	Multiplier
Imix	100.04	80.03	76.03	2.7	25.03	89.74	28.07	100%
1514	100.12	80.1	79.05	1.33	6.53	430.18	35.07	100%
590	99.36	79.49	76.89	3.2	16.29	177.43	36.36	99.5%
128	99.56	79.65	68.89	15.4	67.27	36.94	31.2	99.5%
64	52.8	42.3	32.23	14.1	62.95	21.43	31.89	31.5mpps

Packet size	Line Utilization (%)	Total L1 (Gb/s)	Total L2 (Gb/s)	CPU Util (%)	Total MPPS	BW per core (Gb/s) ①	MPPS per core ②	Multiplier
Imix	100.04	80.03	76.03	12.6	25.03	45.37	14.19	100%
1514	100.12	80.1	79.05	2.6	6.53	220.05	17.94	100%
590	99.36	79.49	76.89	5.6	16.29	101.39	20.78	99.5%
128	99.56	79.65	68.89	33.1	67.27	17.19	14.52	99.5%
64	52.8	42.3	32.23	31.3	63.06	9.65	14.37	31.5mpps

- ① Extrapolated L1 bandwidth per 1 core @ 100% CPU utilization.
- ② Extrapolated amount of MPPS per 1 core @ 100% CPU utilization.

Figure 7: Stateless performance with Intel XL710

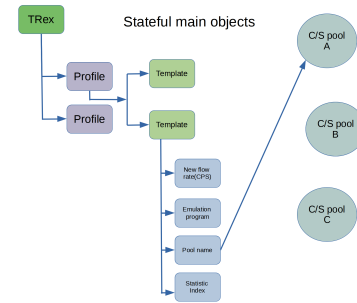


Figure 8: Stateful main objects

Figure 10 shows the stack of the programmable application emulation layer. This module is responsible to simulate applications on top of the TCP/UDP stack. The program is part of a template. Some possible instructions are:

- Start write of buffer
- Continue write
- End Write
- Wait for buffer/timeout
- OnConnect/OnReset/OnClose

Stateful Profile Example

Listing 5 shows a simple profile with one pool of clients (16.0.0.1–16.0.0.254) and one pool of servers (48.0.0.1–48.0.255.254). The pcap file is parsed and the L7 data is converted to instructions on top of the TCP stack.

Emulation layer instructions

Listing 6 shows a simple example of a low level instructions of the emulation layer. In this example the client sends request and waits for the response while the server waits for request and sends a response. Listing 7 and Listing 8 shows the pseudo user-space code that is executed for each flow for Listing 6 emulation program to better understand how it works internally.

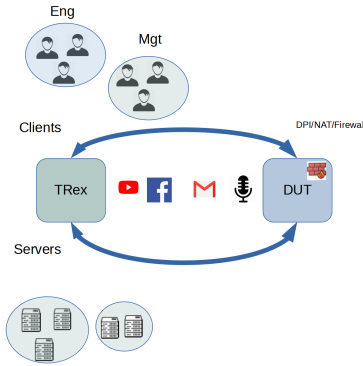


Figure 9: Stateful model

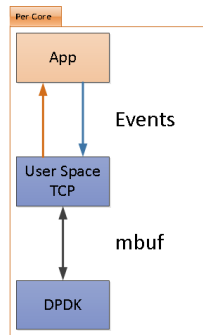


Figure 10: Stateful stack

Automation example

Listing 9 shows an example of a Python script to automate a stateful profile and read the port and the TCP statistics.

Stateful optimization

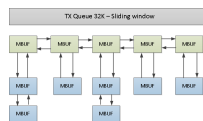


Figure 11: One Flow Tx Ring

Most TCP stacks have an API that allow the user to provide a buffer (write operation). The TCP module stores the buffer until the data is acknowledged by the remote side. With big TCP windows (required with high RTT) and many flows this could create a memory scale issue. Figure 11 shows one TCP flow TX queue and Figure 12 shows the Rx side of the same flow. For 1M active flows with a 64K TX buffer the worst case memory requirement is $1M \cdot 64K \cdot mbufs = 128GB$. The mbuf resource is expensive and needs to be allocated ahead of time (because it is shared with the NIC and translation virtual/physical need to be known). The chosen solution for this problem is to change the API to be a poll API, meaning the TCP Tx queue will just save a reference to the constant

```
from trex.astf.api import *

class Profl():
    def get_profile(self):
        # clients pool range and distribution type
        ip_gen_c = ASTFIPGenDist(
            ip_range=["16.0.0.1", "16.0.0.254"],
            distribution="seq")
        # servers pool range and distribution type
        ip_gen_s = ASTFIPGenDist(
            ip_range=["48.0.0.1", "48.0.255.254"],
            distribution="seq")
        # pool definition for both clients and servers
        ip_gen =
        ASTFIPGen(
            glob=ASTFIPGenGlobal(ip_offset="1.0.0.0"),
            dist_client=ip_gen_c,
            dist_server=ip_gen_s)

        # Parse the pcap file and convert to instructions
        # cps is 1 flows/sec
        return ASTFProfile(default_ip_gen=ip_gen,
            cap_list=[ASTFCapInfo(
                file="../avl/delay_10_http_browsing_0.pcap"
                cps=1)
            ])
    )
```

Listing 5: Stateful profile

```
# client side HTTP program
prog_c = ASTFProgram()
prog_c.send(http_req)
prog_c.recv(len(http_response))

# server side HTTP program
prog_s = ASTFProgram()
prog_s.recv(len(http_req))
prog_s.send(http_response)
```

Listing 6: Emulation layer instructions

traffic and offset. The packets will be assembled with a reference to a constant mbufs only when packets need to be sent (lazy). Now because most of the traffic is almost constant in traffic generation case (per template) and known ahead of time it was possible to implement and save most of the memory. The same idea happens in the Rx side with reassembly¹

Benchmark TRex vs Linux kernel

To evaluate the performance and memory scale of TRex and compare it against standard Linux tools the following was done: Linux `nginx` as a server were compared to TRex for stressing a device under test.

The benchmark setup was designed to take a popular event-driven Linux server application and to test using a TRex client. TRex client requests the pages. Figure 13 shows

¹This will not work for TLS streams

```

c = ASTFClient(server = server)

c.connect()

try:
    c.reset()

    c.load_profile(profile_path)

    c.clear_stats()

    c.start(mult = mult,
            duration = duration,
            nc = True)

    c.wait_on_traffic()

    stats = c.get_stats()

    pprint.pprint(stats)

    if c.get_warnings():
        for w in c.get_warnings():
            print(w)

except TRexError as e:
    print(e)

except AssertionError as e:
    print(e)

finally:
    c.disconnect()

```

Listing 9: Stateful automation example

Benchmark Limitations

The comparison is not perfect, as TRex merely emulates HTTP traffic. It is not a real-world web server or HTTP client. For example, currently the TRex client does not parse the HTTP response for the Length field. TRex simply waits for a specific data size (32KB) over TCP. However the TCP layer is a fully featured TCP (e.g. delay-ack/Retransmission/Reassembly/timers). The benchmark's objective is to compare traffic generation capabilities for stressing network gears and not to replace NGINX server.

Benchmark results

Comparing 1 DP core running TRex to NGINX running on 16 cores with a kernel that can interrupt any NGINX process with IRQ. Figure 15 shows the performance of one DP TRex. m stands for multiplier of the baseline profile. It can scale to about 25Gb/sec of download of HTTP (total of 3MPPS/90KCPS/60k active flows for one core).

Nginx cannot handle more than 20K new flows/sec, due to the kernel TCP software IRQ interrupts processing. The limitation is the kernel and not NGINX's user space process. With more NICs and optimized distribution, the number of flows

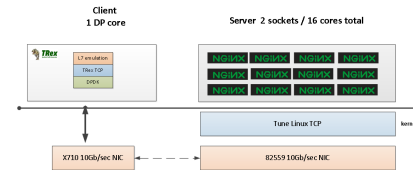


Figure 13: TRex,NGINX server Performance Testing

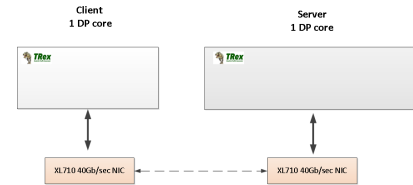


Figure 14: TRex Client Server

could be increased times two, but not more than that. The total number of packets was approximately 600KPPS (Tx+Rx). The number of active flows was 12K.

TRex with one core could scale to about 25Gb/sec, 3MPPS of the same HTTP profile. The main issue with NGINX and Linux setup is the tuning (this might be improved with more sophisticated tuning). It is very hard to let the hardware utilize the full server resource (half of the server was idle in this case and still experienced a lot of drop). TRex is not perfect too, we couldn't reach 100% CPU utilization without a drop (CPU was 84%). To achieve 100gbps with this profile on the server side requires 4 cores for TRex, vs. 20x16 cores for NGINX servers. TRex is faster by a factor of **80**. In this implementation, each flow requires approximately 1K bytes of memory (regardless of Tx/Rx rings because of TRex architecture). In the kernel, with a real-world server, TRex optimization can't be applied and each TCP connection must save memory in Tx/Rx rings. For about 5Gb/sec traffic with this profile, approximately 10GB of memory was required (both NGINX and Kernel). For 100Gb/sec traffic, approximately 200GB is required (extrapolation) With a TRex optimized implementation, approximately 100MB is required. TRex thus provides an improvement by a factor of **2000** in the use of memory resources.

Routing protocol using BIRD

Bird Internet Routing Daemon [4] is a project aimed to develop a fully functional linux dynamic IP routing daemon for BGP/OSPF/RIP and more. It was integrated into TRex to run alongside in order to exploit its features together with Python automation API using Linux network namespace and veth. In this case the Linux TCP stack is used (e.g. BGP) and packets from the veth is moved from/to the physical ports. Figure 17 shows the integration with TRex server. The pyBird server is a daemon that get JSON-RPC2 over ZMQ RPC commands from one side and interact with BIRD daemon using ssh/text protocol. The BIRD automation API expose the daemon capabilities and interact with TRex for the right filters and veth creation. Using this API's one can push 1M of BGP routes in a few seconds to the DUT.

TRex one DP core								
m	CPU (1DP) %%	cps	rps	rx (mb/sec)	pps(tx+rx)	active flows	drop	Memory TCP/MB
1000	0.6	1000	1000	265	34444	600	0	0.3
5000	2.7	5000	5000	1320	172222	3000	0	1.4
10000	5.7	10000	10000	2210	344444	6000	0	2.9
20000	13.5	20000	20000	5410	688889	12000	0	5.7
50000	40.8	50000	50000	13480	1722222	29870	0	14.2
87500	79.0	87500	87500	23670	3013889	55329	0	26.4
90000	86.1	90000	90000	24271	3100000	56217	0.10%	26.8

Figure 15: TRex 1 DP core

Linux 16 cores								
m	cps	rps	rx (mb/sec)	active flows	16xCPU	drop	Kernel memory SLAB (MB)	Total Memory used (free-h) MB
1000	1000	1000	265	600		no		21000
5000	5000	5000	1320	3000		no		22000
10000	10000	10000	2210	6000		no		25000
20000	20000	20000	5410	12000	20%/25% 8x cores IRQ break	yes	800	31000
50000	50000	50000	13480	29870				
87500	87500	87500	23670	55329				
90000	90000	90000	24271	56217				

Figure 16: NGINX 16 cores

Figure 18 show a simple example of configuration with 2 ports. BIRD has 2 veth's in the same subnet of TRex ports. Listing 10 shows a sample profile to push 11 BGP routes 42.42.42.0-10/32 via 1.1.1.3

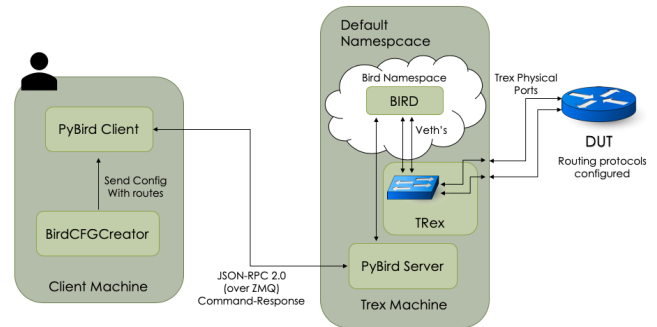


Figure 17: BIRD TRex integration

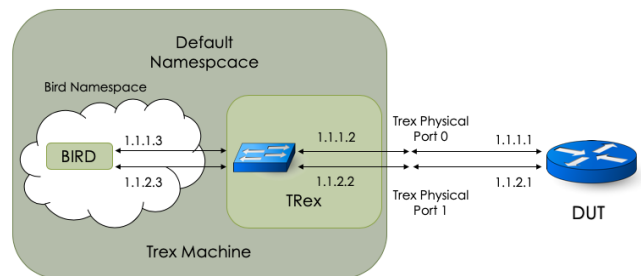


Figure 18: BIRD Simple Example

References

- [1] Cisco systems: "TRex realistic traffic generator", <https://trex-tgn.cisco.com/trex/doc/>
- [2] Cisco systems, "TRex Stateless", https://trex-tgn.cisco.com/trex/doc/trex_stateless.html
- [3] Cisco systems, "TRex Stateful ASTF", https://trex-tgn.cisco.com/trex/doc/trex_astf.html
- [4] BIRD, Faculty of Math and Physics, Charles University Prague <https://bird.network.cz/>
- [5] Cisco systems, "TRex STL benchmark", https://trex-tgn.cisco.com/trex/doc/trex_stateless.html
- [6] NGINX performance <https://www.nginx.com/blog/testing-the-performance-of-nginx/>
- [7] IMIX https://en.wikipedia.org/wiki/Internet_Mix

```
router id 100.100.100.100;

protocol device {
    scan time 1;
}

protocol bgp my_bgp1 {
    # put your local ip and as number here
    local 1.1.1.3 as 65000;
    # put your dut ip and as number here
    neighbor 1.1.1.1 as 65000;
    ipv4 {
        import all;
        export all;
    };
}

# using a second interface
protocol bgp my_bgp2 {
    # same for the second interface
    local 1.1.2.3 as 65000;
    neighbor 1.1.2.1 as 65000;
    ipv4 {
        import all;
        export all;
    };
}

protocol static {
    ipv4 {
        import all;
        export all;
    };

    route 42.42.42.0/32 via 1.1.1.3;
    route 42.42.42.1/32 via 1.1.1.3;
    route 42.42.42.2/32 via 1.1.1.3;
    route 42.42.42.3/32 via 1.1.1.3;
    route 42.42.42.4/32 via 1.1.1.3;
    route 42.42.42.5/32 via 1.1.1.3;
    route 42.42.42.6/32 via 1.1.1.3;
    route 42.42.42.7/32 via 1.1.1.3;
    route 42.42.42.8/32 via 1.1.1.3;
    route 42.42.42.9/32 via 1.1.1.3;
    route 42.42.42.10/32 via 1.1.1.3;
}
}
```

Listing 10: BIRD configuration