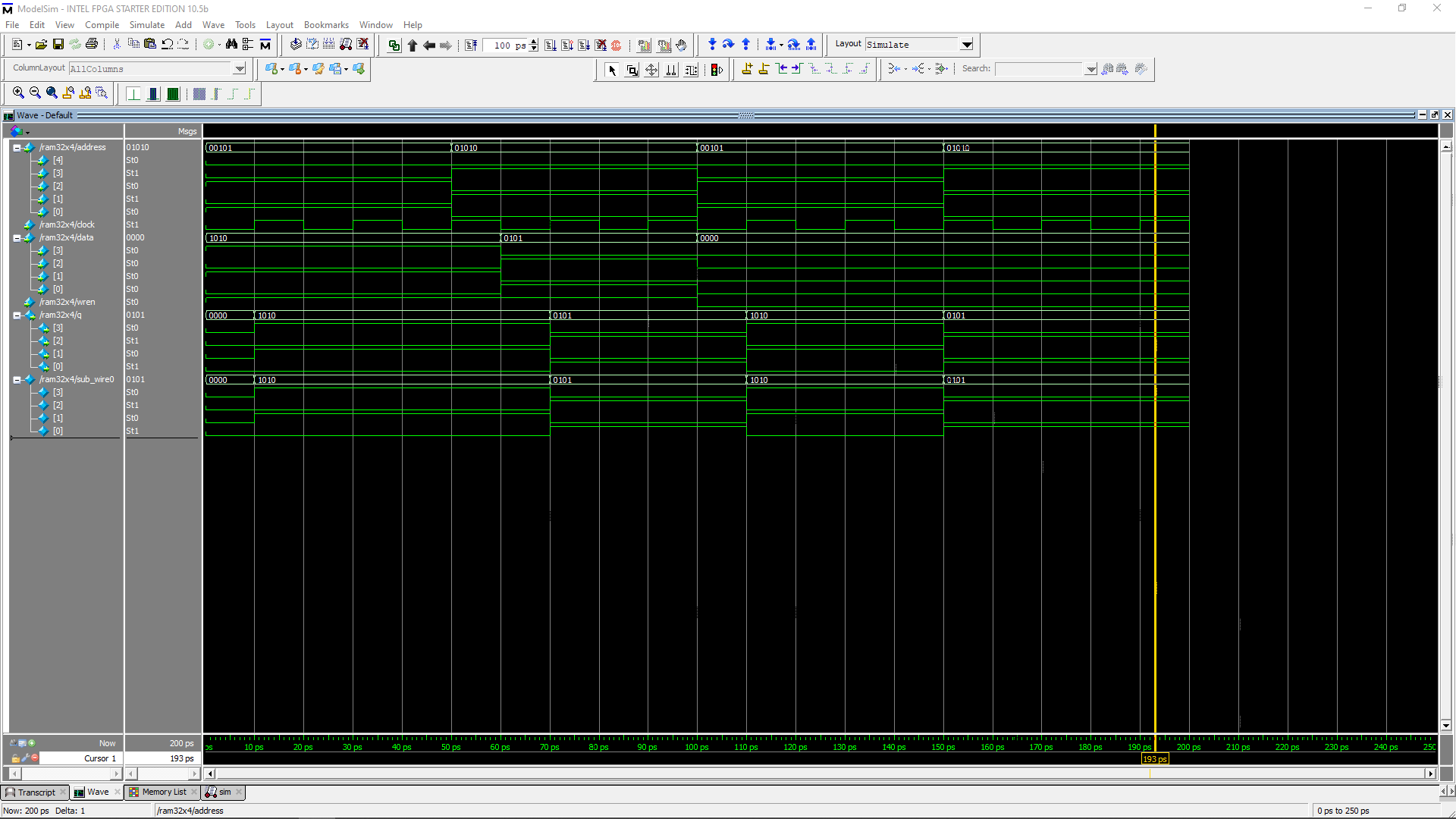
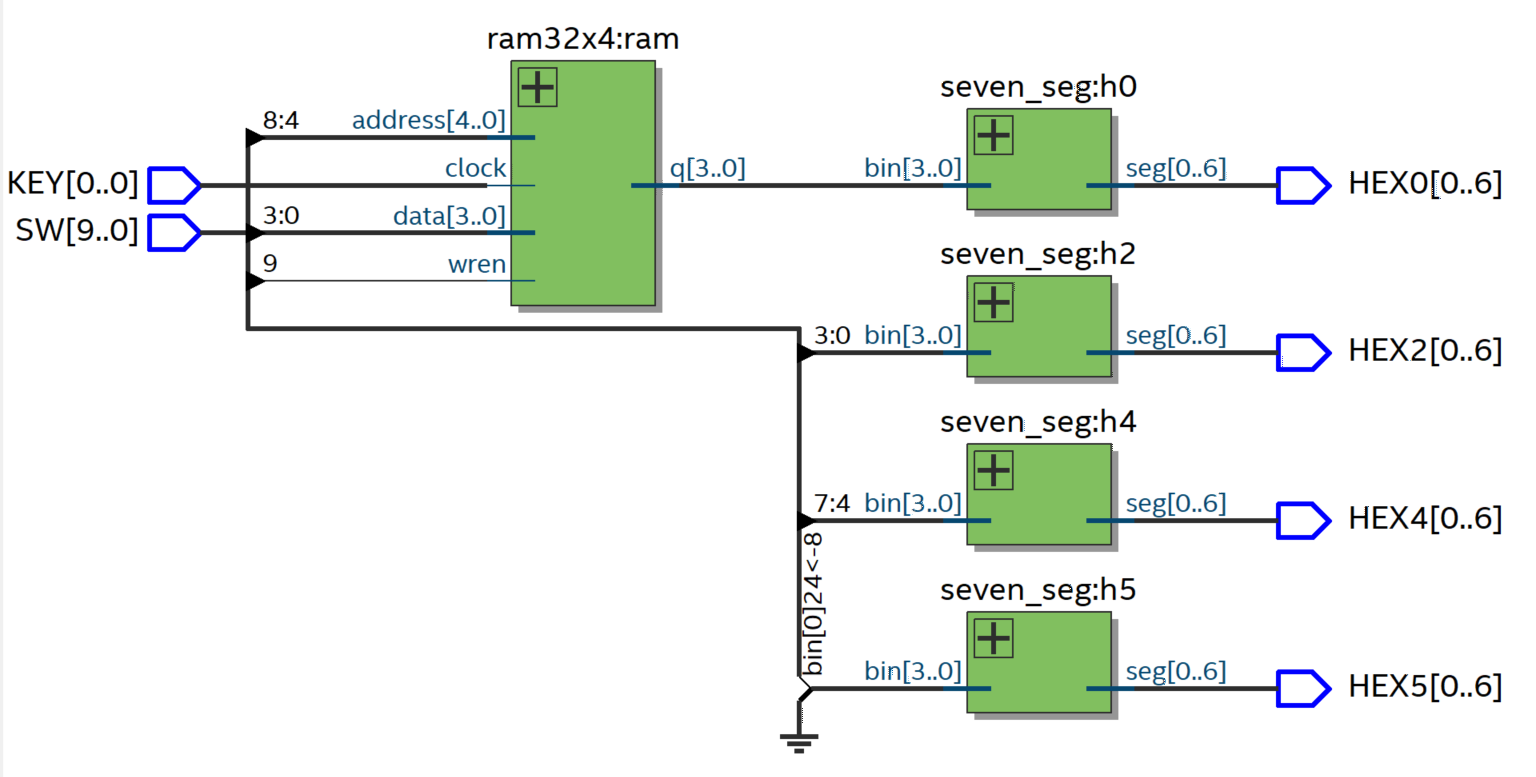
Part I:

9. Simulation for the ram module.

11. 

Schematic for the circuit, with the Keys and switches connected to relevant modules.