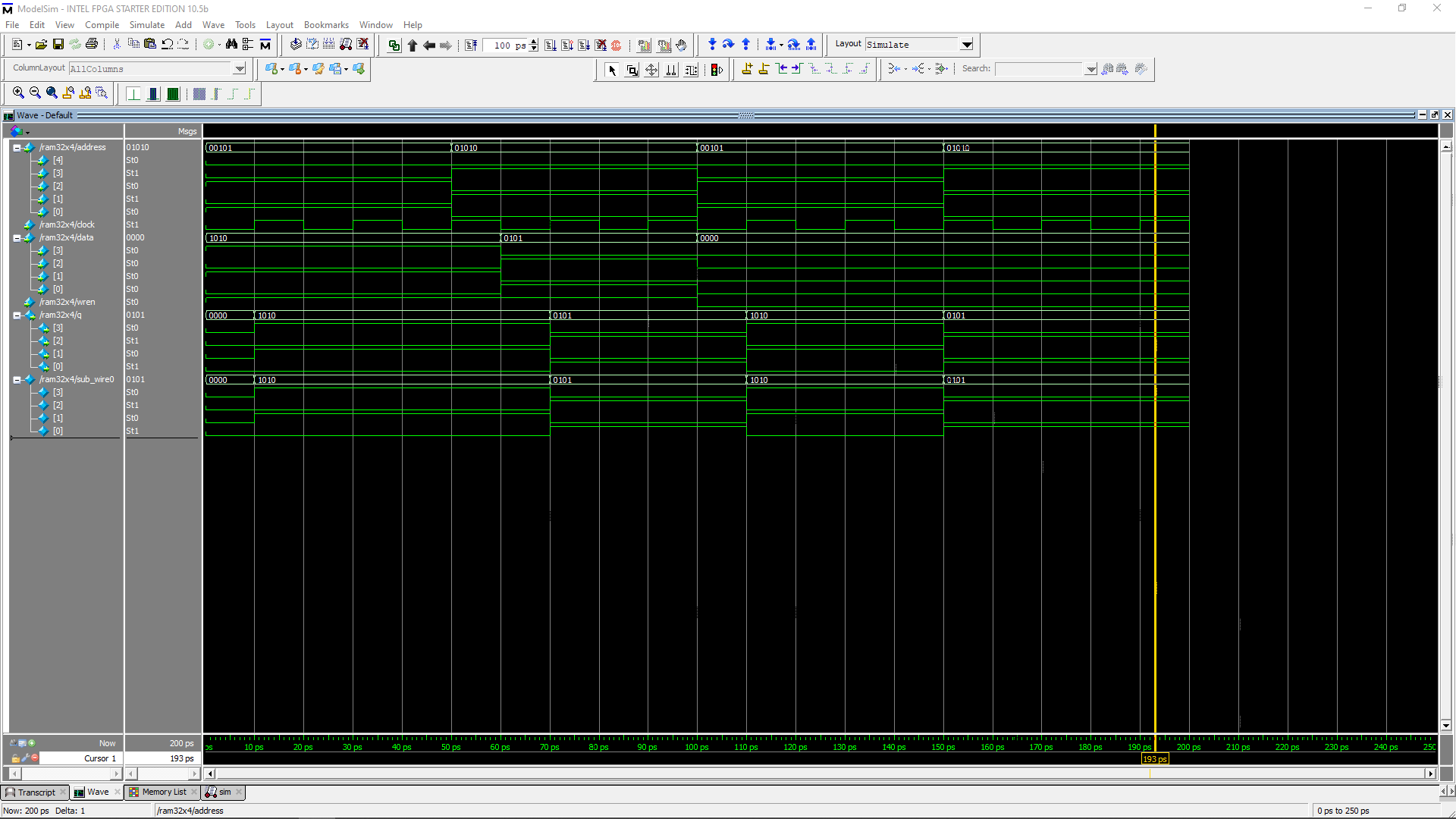
Part I:

9. Simulation for the ram module.