

Special Topics on Basic EECS I Design Technology Co-Optimization

Lecture 23

Sung-Min Hong (smhong@gist.ac.kr)

Semiconductor Device Simulation Laboratory

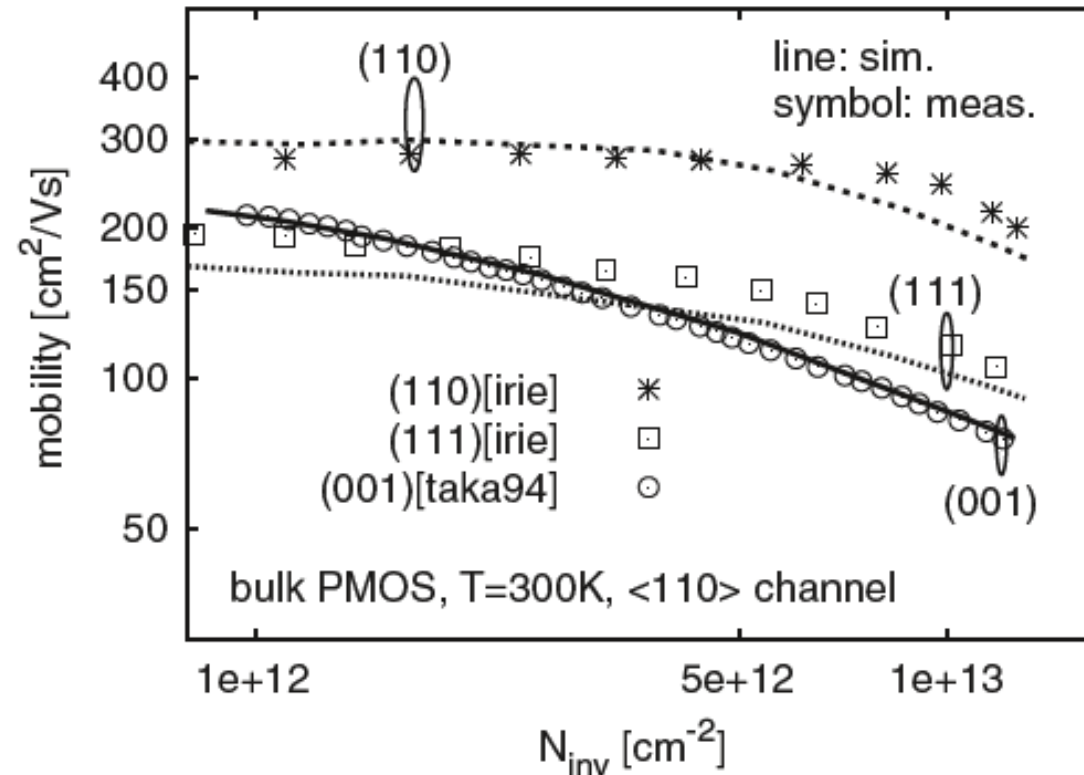
Department of Electrical Engineering and Computer Science

Gwangju Institute of Science and Technology (GIST)

L23

PMOSFET

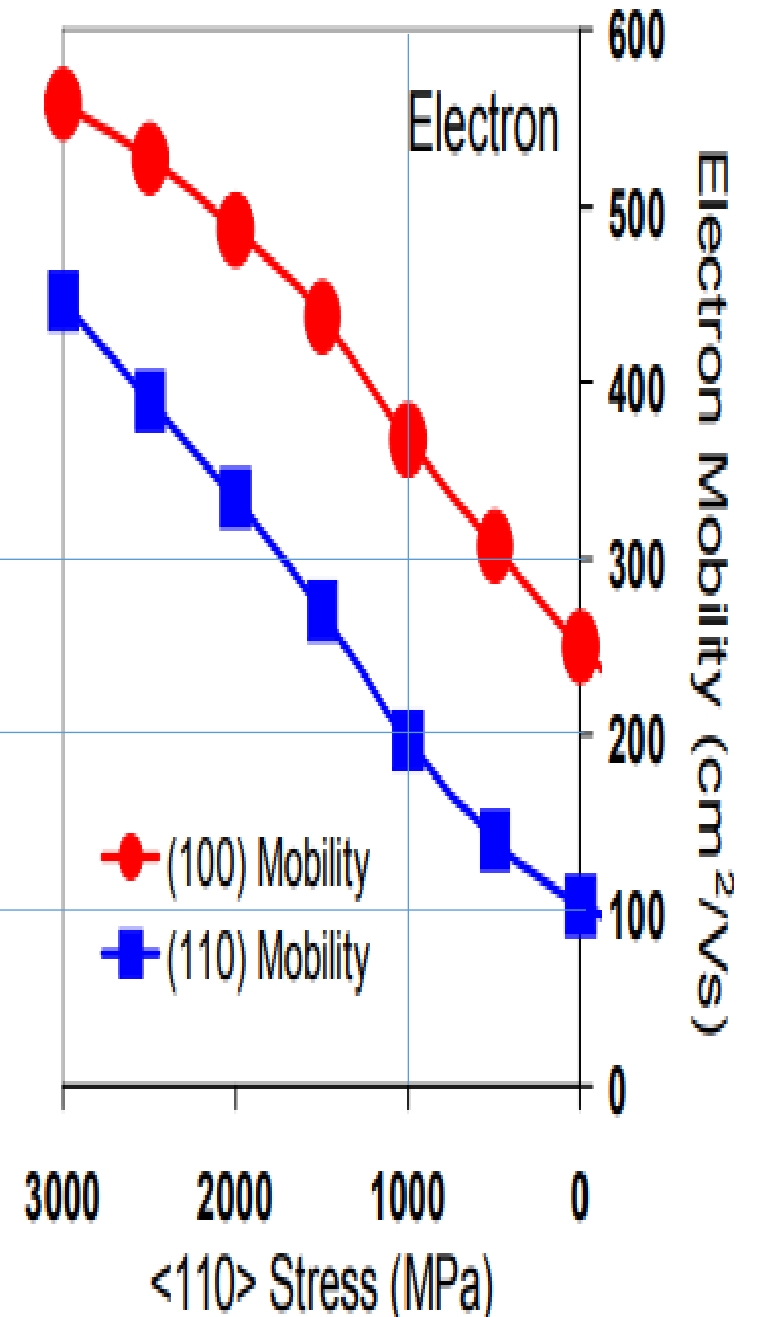
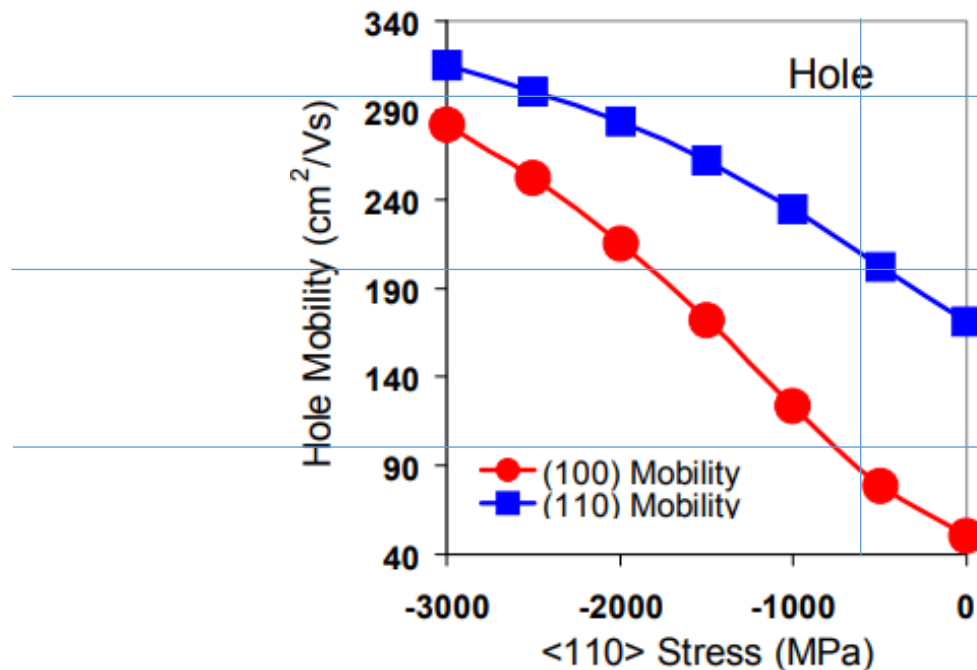
- Hole mobility for different surface orientations
 - The channel direction is $\langle 110 \rangle$.



Low-field mobility (Hong, Pham, and Jungemann)

N/P unbalance issue

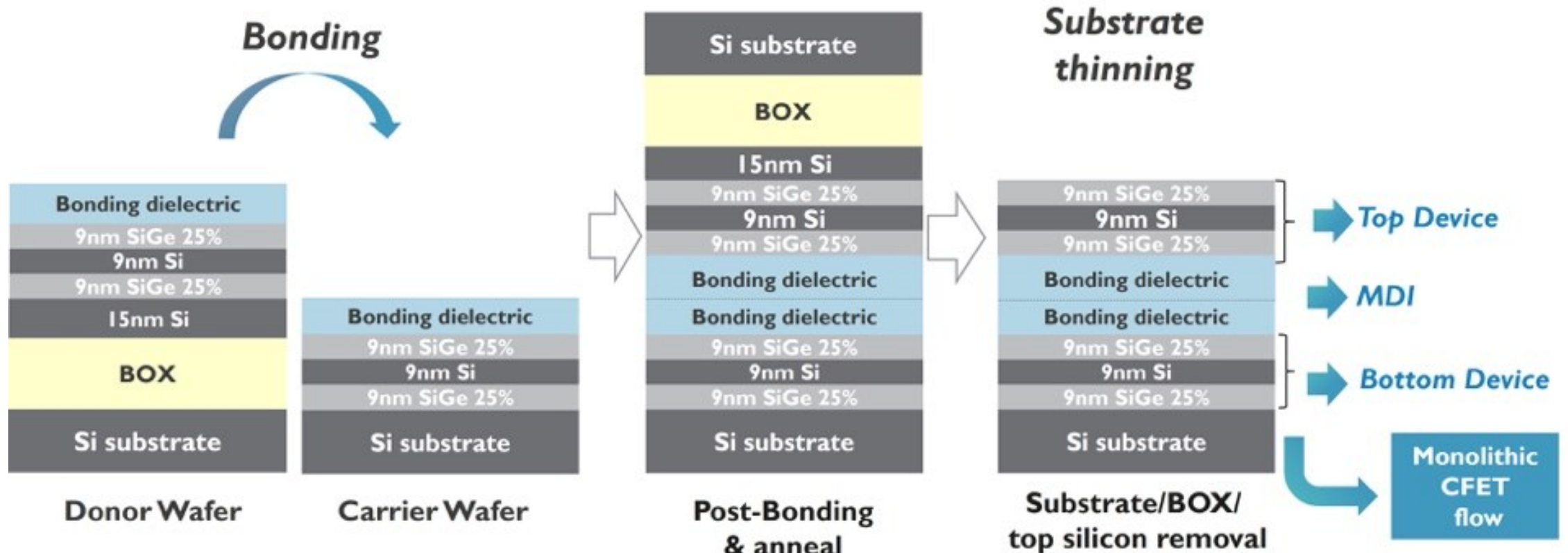
- Blue (FinFET)
 - Red (Nanosheet)



Simulated mobility as a function of stress
(P. Packan et al., Intel)

IMEC's presentation @ IEDM 2025

- 2.2 Hybrid channel monolithic-CFET with Si (110) pMOS & (100) nMOS

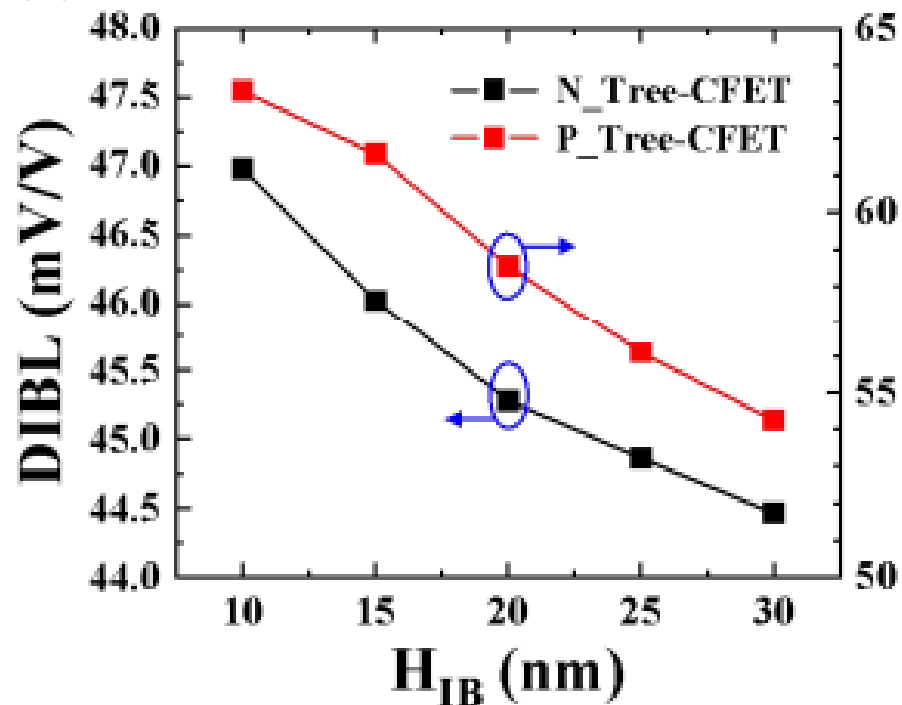


Press release (IMEC)

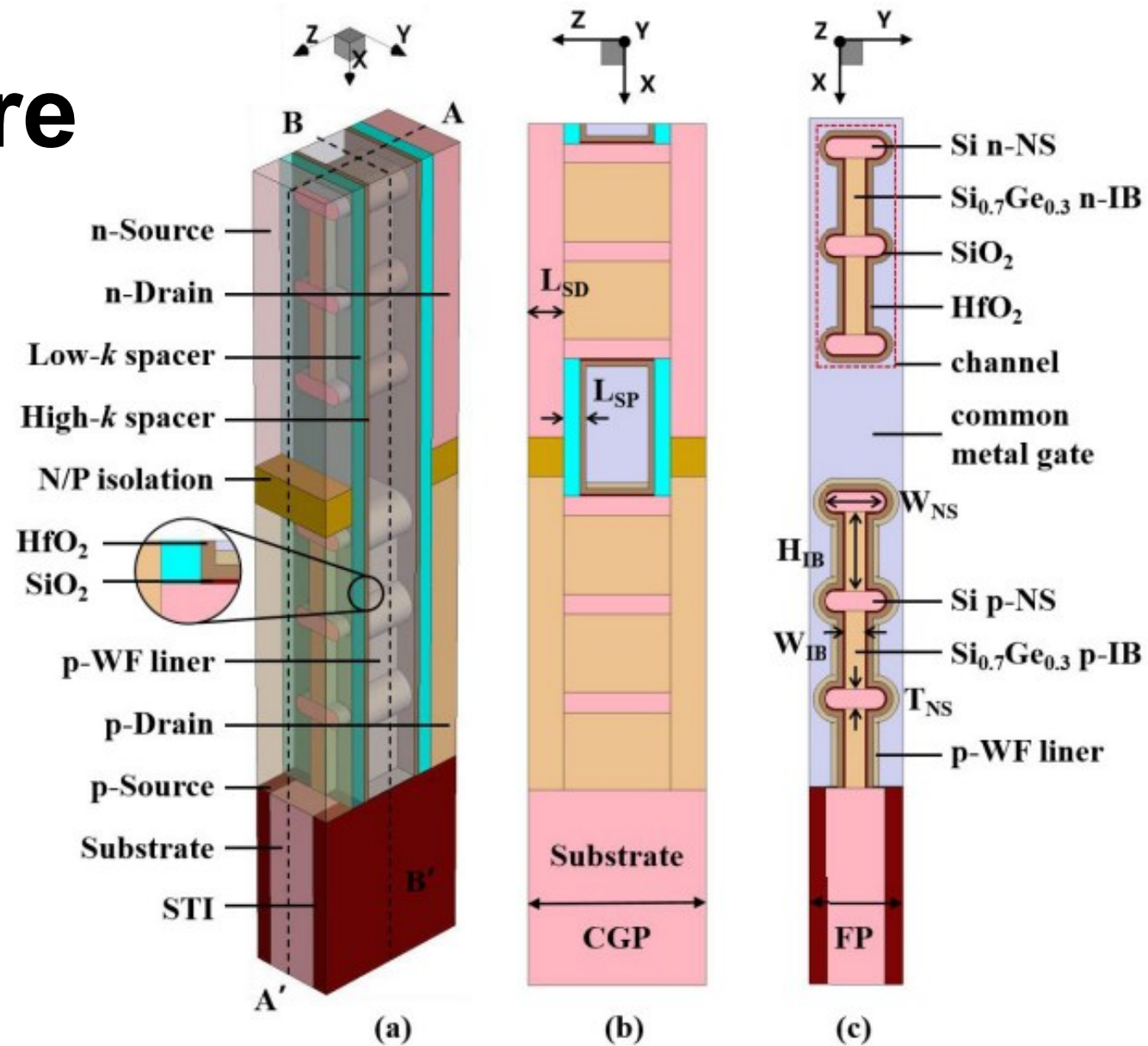
GIST Lecture

Alternative structure

- TreeFET & FishboneFET
 - Nanosheet (15nm X 5 nm)
 - Interbridge



Simulated DIBL



Structure of Tree-CFET (J. Zhao et al., East China Normal University)

Thank you!