|  |  |  |
| --- | --- | --- |
|  | Fmax | Setup Time |
| Slow 1100mV 85C | 457.04 MHz | -1.188 |
| Slow 1100mV 0C | 433.84 MHz | -1.305 |
| Fast 1100mV 85C | N/A | -0.245 |
| Fast 1100mV 0C | N/A | -0.173 |

Largest failing path (also only path shown):

A white background with black text

AI-generated content may be incorrect.

After new clock constraint created

|  |  |  |
| --- | --- | --- |
|  | Fmax | Setup Time |
| Slow 1100mV 85C | 317.26 MHz | 16.848 |
| Slow 1100mV 0C | 313.58 MHz | 16.811 |
| Fast 1100mV 85C | N/A | 18.059 |
| Fast 1100mV 0C | N/A | 18.169 |

Only path shown after new clock constraint created:

