



**Update:** Write the new value with updated timestamp.

**Scan:** Collect the entire array in W.

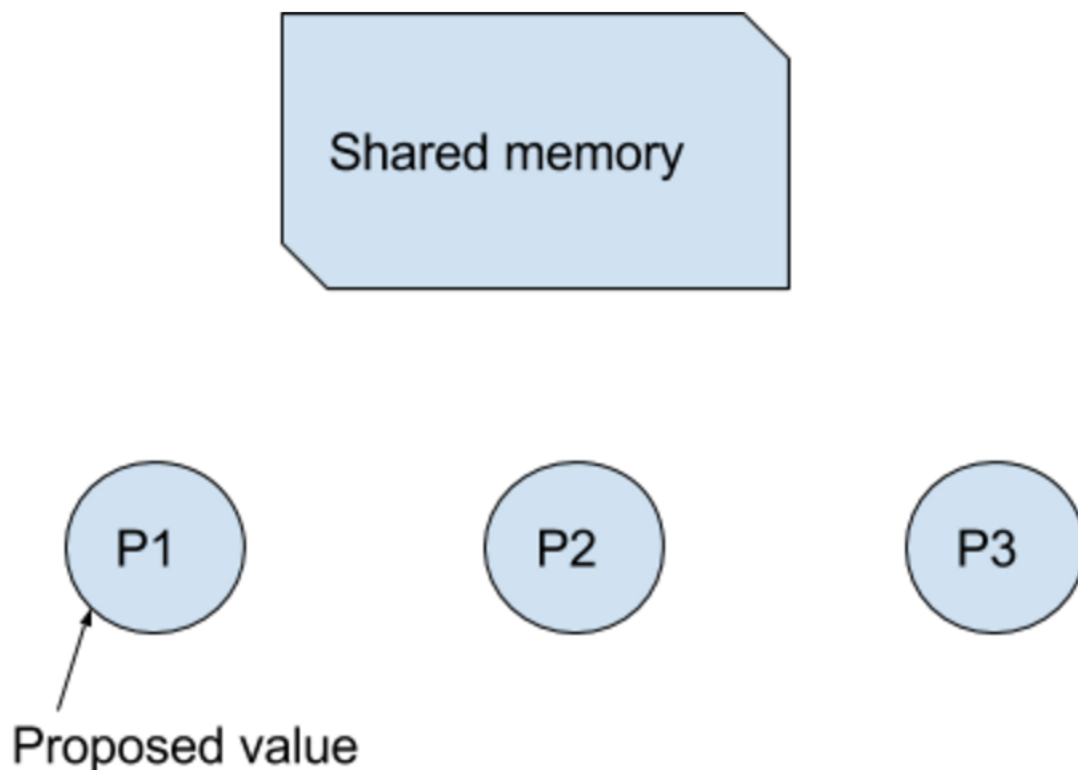
**Loop:** Read the array again to make sure that there is no change in any timestamp.

If there is some change then go to Loop;

## 18.2 Consensus

The **consensus** problem requires a given set of processes to agree on an input value.

We abstract the consensus problem as follows: Each process has a value input to it that it can propose. For simplicity, we will restrict the range of input values to a single bit. The processes are required to run a protocol so that they decide on a common value.



The **requirements** on any object implementing consensus are as follows:

- Agreement: No two correct processes decide on different values.
- Validity: The value decided must be proposed by some process.
- Wait-freedom: Decides in a finite number of steps.

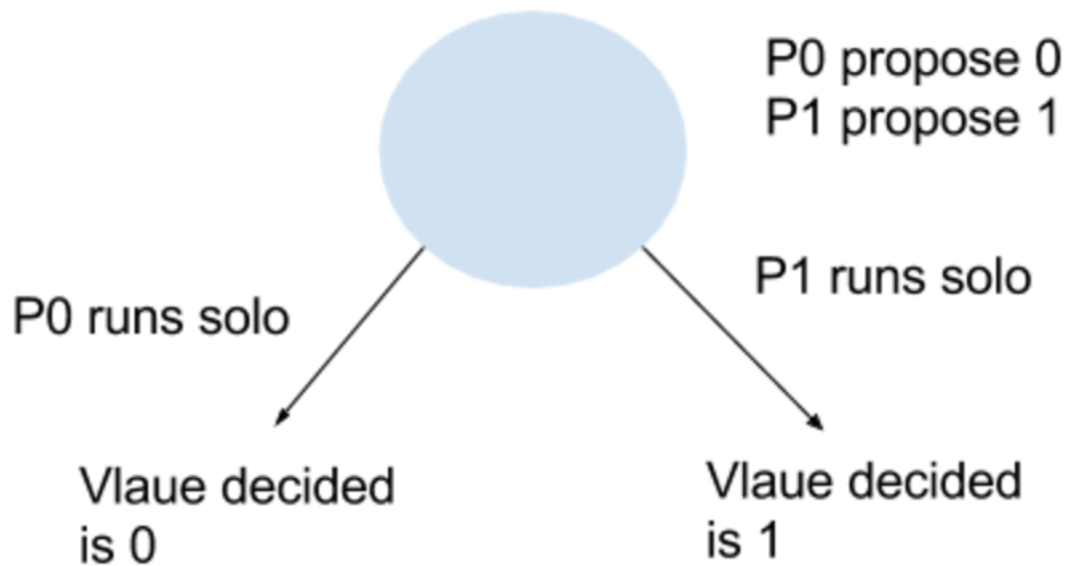
A protocol is in a bivalent state if both the values are possible as decision values starting from that global state.

A bivalent state is a critical state if all possible moves from that state result in nonbivalent states.

### 18.2.1 Consensus Claims

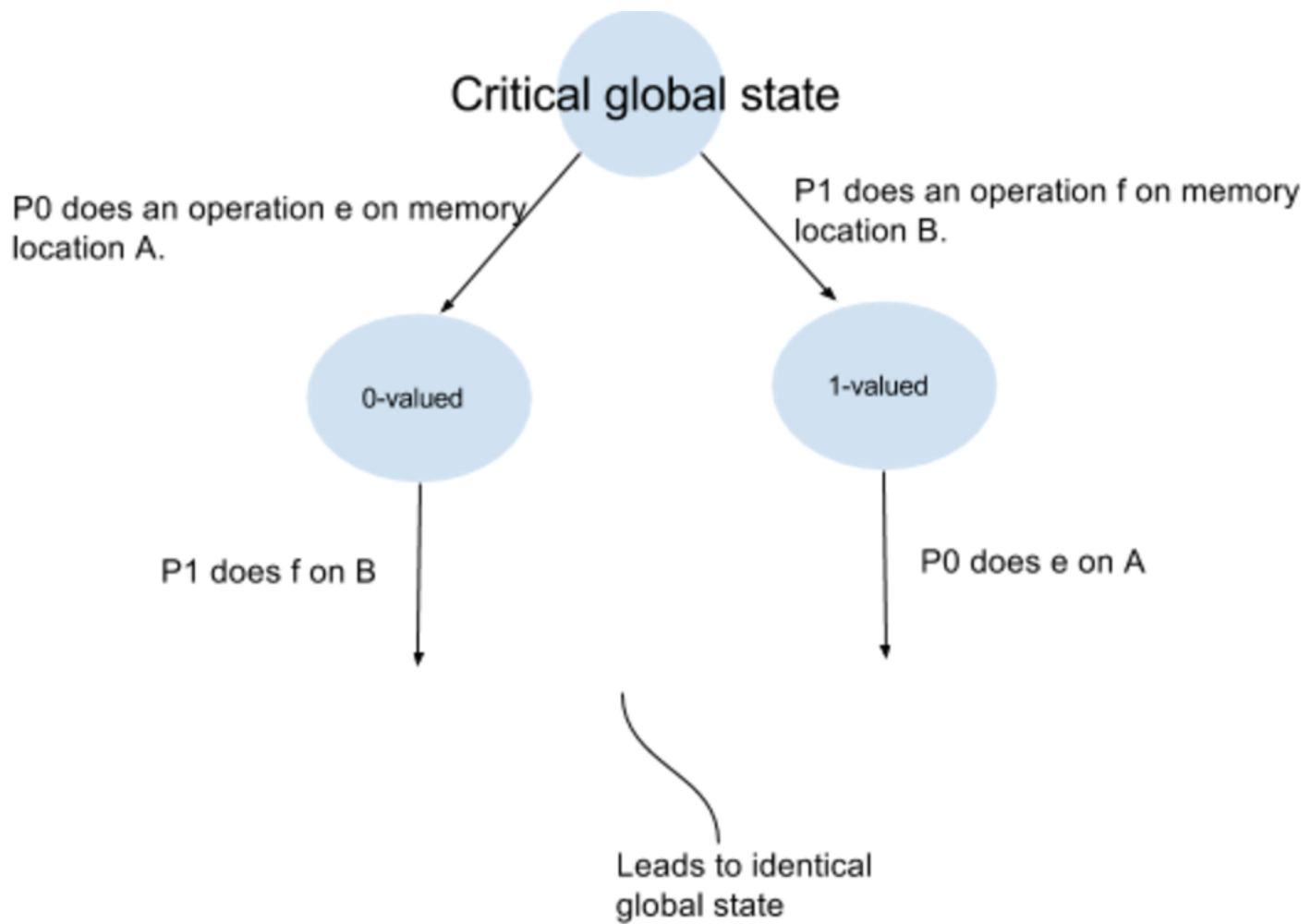
**Claim 1:** There exists an initial bivalent global state for any consensus protocol.

**Proof:** Because there exist at least two runs from that state that result in different decision values. In the first run, the process with input 0 gets to execute and all other processes are very slow. Because of wait freedom, this process must decide, and it can decide only on 0 to ensure validity. A similar run exists for a process with its input as 1.

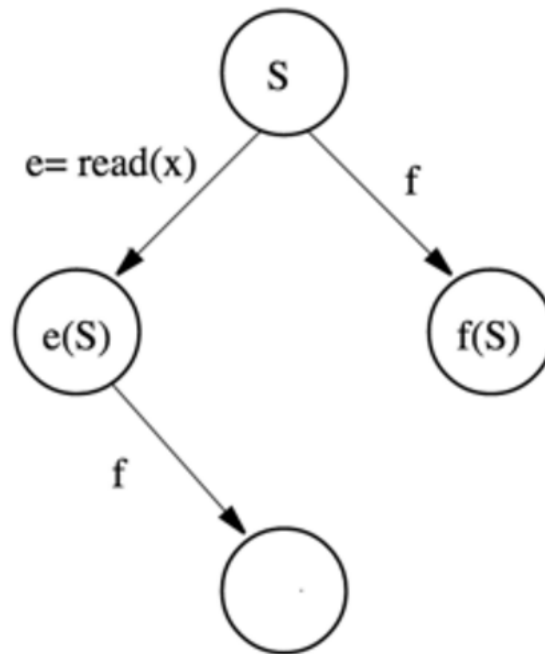


**Claim 2:** There exists a critical global state for every consensus protocol.

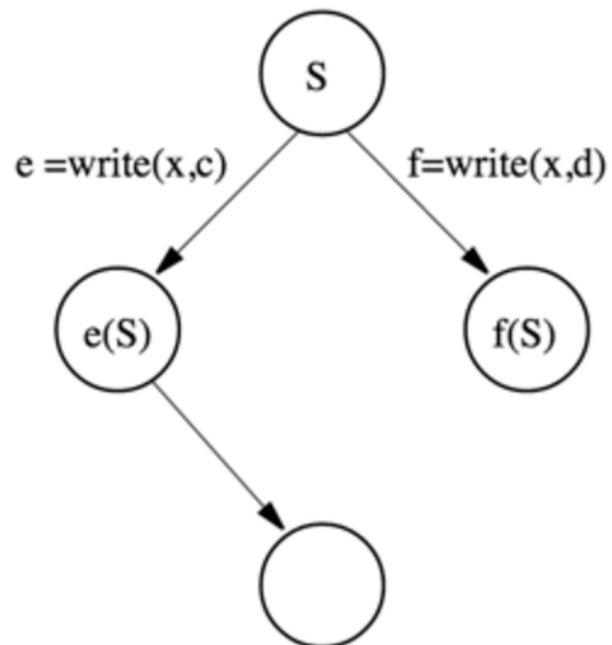
**Claim 3:** There does not exist any protocol to solve the consensus using atomic registers. **Proof:** We show that even in a two-process system, atomic registers cannot be used to go to non-bivalent states in a consistent manner. We perform a case analysis of events that can be done by two processes, say, P and Q in a critical state S. Let e be the event at P and event f be at Q be such that e(S) has a decision value different from that of f(S). We now do a case analysis:



Case 1:  $e$  and  $f$  are on different registers. In this case, both  $ef$  and  $fe$  are possible in the critical state  $S$ . Further, the state  $ef(S)$  is identical to  $fe(S)$  and therefore cannot have different decision values. But we assumed that  $f(S)$  and  $e(S)$  have different decision values, which implies that  $e(f(S))$  and  $f(e(S))$  have different decision values because decision values cannot change.



Case 2: Either  $e$  or  $f$  is a read. Assume that  $e$  is a read. Then the state of  $Q$  does not change when  $P$  does  $e$ . Therefore, the decision value for  $Q$  from  $f(S)$  and  $e(S)$ , if it ran alone, would be the same; a contradiction.



Case 3: Both  $e$  and  $f$  are writes on the same register. Again the states  $f(S)$  and  $f(e(S))$  are identical for  $Q$  and should result in the same decision value.

## 18.3 SPSC

Assume we have a queue storing values of 'win' and 'lose'.

Pi:

Write my proposal in the prop array.

Deq from Queue

If I win choose my proposal, otherwise choose other guys proposal.

**Theorem:** There is no wait-free algorithm to build SPMC Queue using atomic read write registers.

**Proof:** Consensus number of a shared object class O is the maximum number of processes that can use objects from class O to solve consensus.

**Consensus number** of a shared is the maximum number of processes that can use that object to solve consensus. The following is the operation with its corresponding consensus number.

Operation: Consensus Number

R/W Register: 1

Test And Set: 1

Get And Increment: 2

Swap: 2

CAS:  $\infty$

### CAS Operation

Initialize to -1

Pi:

Write my proposal in the prop array

Do R.CAS(-1, my\_pid):

Fail: decide prop[R];

Succeed: decide prop[my\_id];

## References

- [1] V. K. GARG, Introduction to Multicore Computing