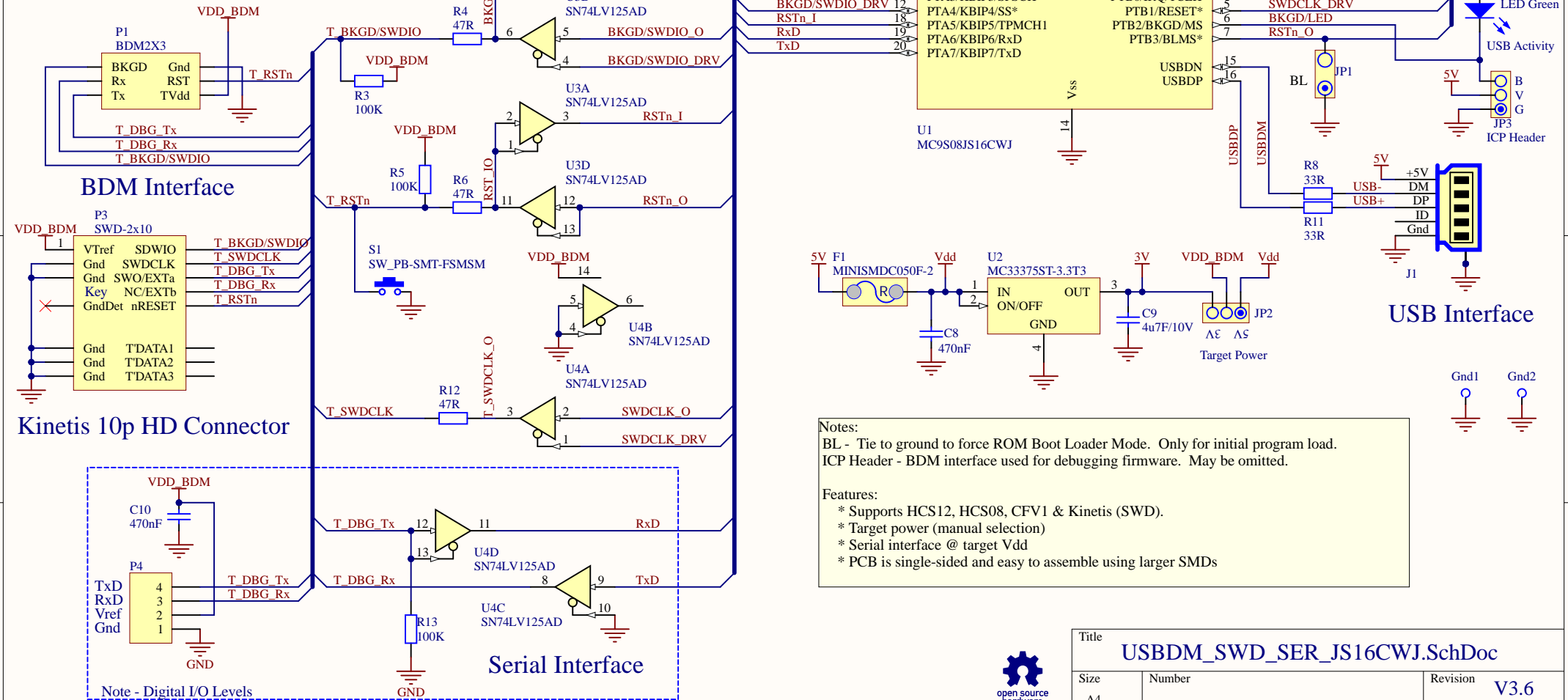
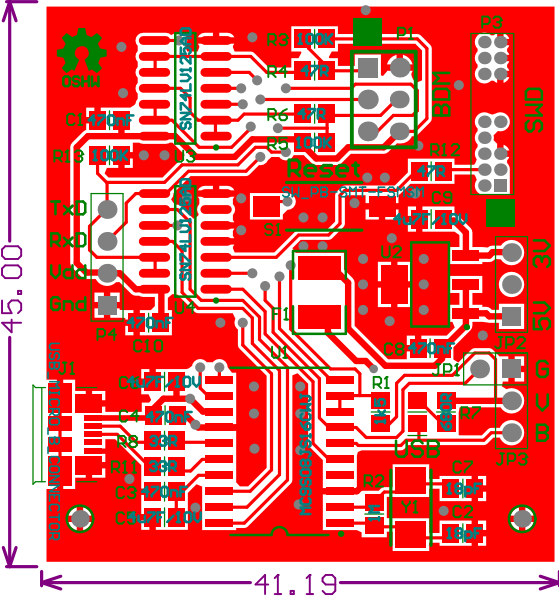
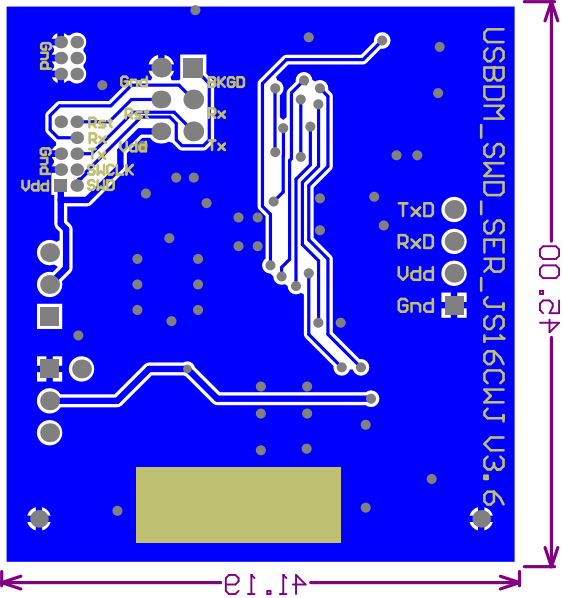


Revision:  
1.0 1st Release  
1.1 Changed R7 to 1M, no PCB changes  
2.0 Addes Serial Interface and complete PCB redo  
2.1 Added RST\_DRV  
3.0 Deleted CTS,RTS added SWD interface  
3.1 Deleted pins from P3 to fit 10p/20p plug  
3.2 Added Rx/Tx to P1 & P3, Gnd TPs  
3.3 Board shrink  
3.4 Rationalised component values  
3.5 Removed SWO, changed J1 to microUSB  
3.6 Added reset switch



Title			USBDM_SWD_SER_JS16CWJ.SchDoc
Size	Number	Revision	
A4		V3.6	
Date:	1/19/2021	Sheet of	
File:	H:\Documents\...\USBDM_SWD_SER_JS16CWJ.SchDoc		







C1 470nF

R13 100K

TxD

RxD

Vdd

Gnd

P4

470nF

C10

C6 470F/10V

C4 470nF

R8 33R

R11 33R

C3 470nF

C5 470F/10V

USB\_MICRO\_B\_CONNECTOR



SN74LV125AD

U3

SN74LV125AD

U4

R3 100K

R4 47R

R6 47R

R5 100K

Reset

SW\_PB-SMT-FSMSM

S1



F1



U1

MC9S08JS16CJ

P1



BDM

R12

47R

C9

4u7F/10V

U2



C8 470nF

R1

1k5

USB

R2

1M

Y1

C7

18pF

C2

18pF

P3



SWD



3V

5V

G

B

C

JP2

JP1

JP3



45.00

41.19

