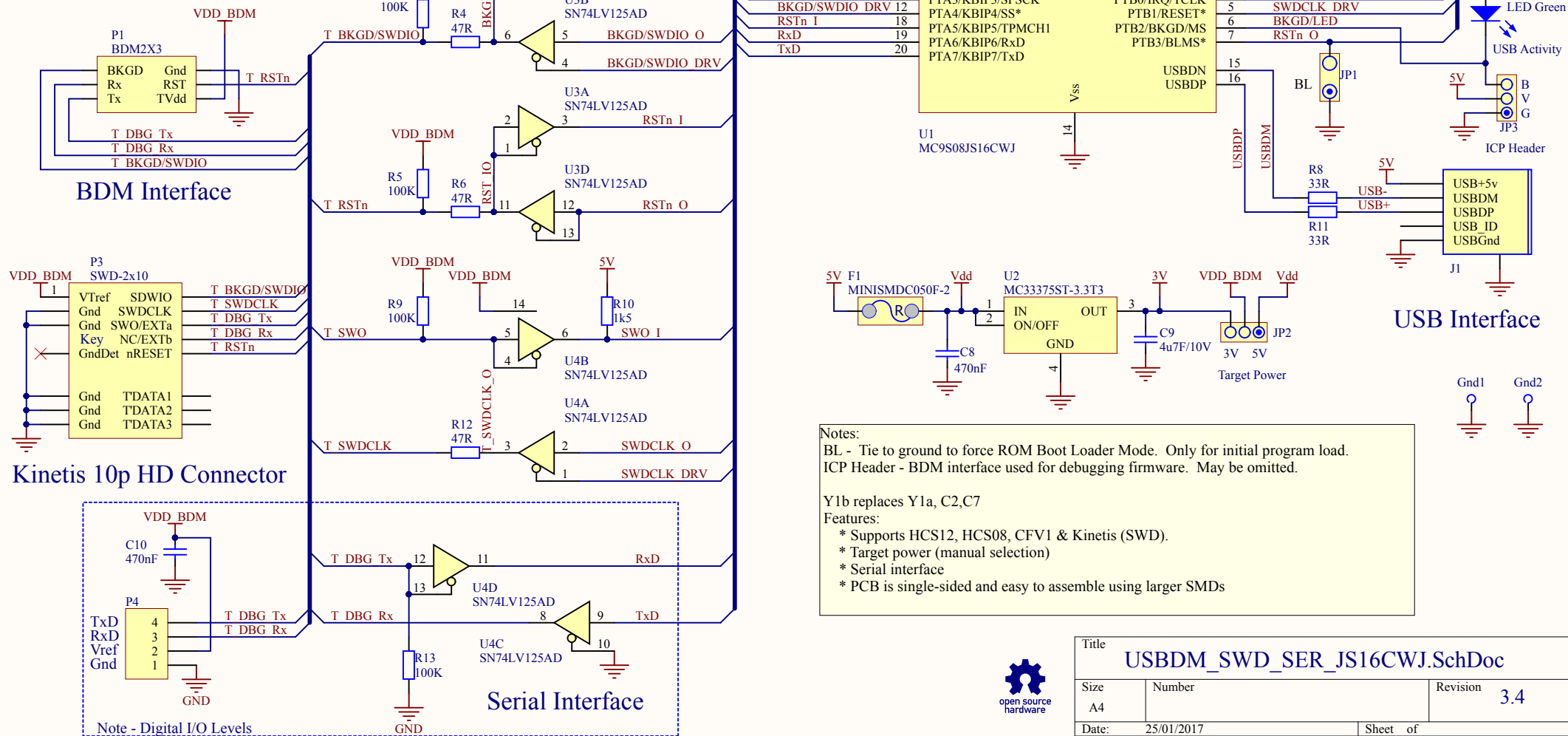
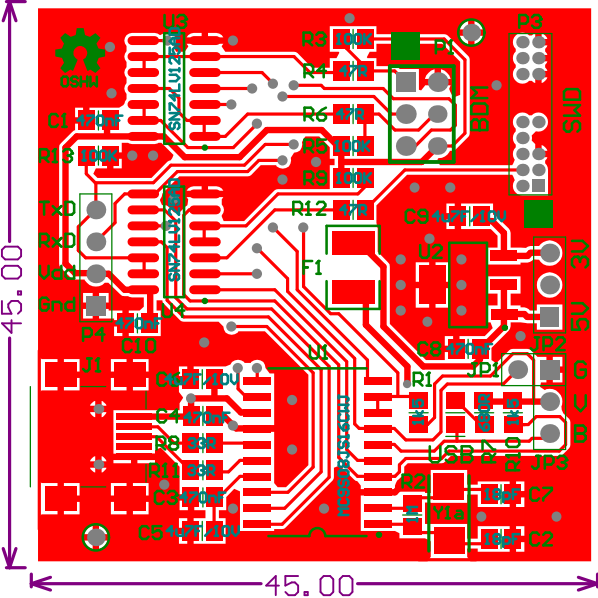


Revision:  
 1.0 1st Release  
 1.1 Changed R7 to 1M, no PCB changes  
 2.0 Adds Serial Interface and complete PCB redo  
 2.1 Added RST\_DRV  
 3.0 Deleted CTS,RTS added SWD interface  
 3.1 Deleted pins from P3 to fit 10p/20p plug  
 3.2 Added Rx/Tx to P1 & P3, Gnd TPs  
 3.3 Board shrink  
 3.4 Rationalised component values



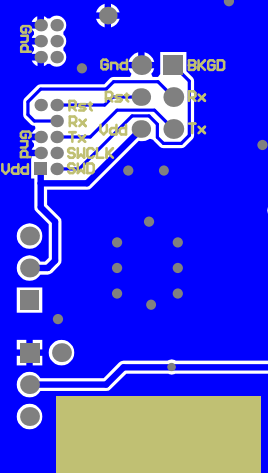
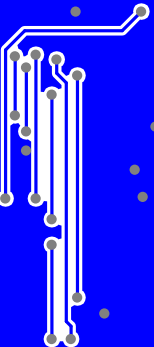
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Size	Number	Revision
A4		3.4
Date:	25/01/2017	Sheet of
File:	C:\Users\... \USBDM_SWD_SER_JS16CWJ.SchDoc	Drawn by:



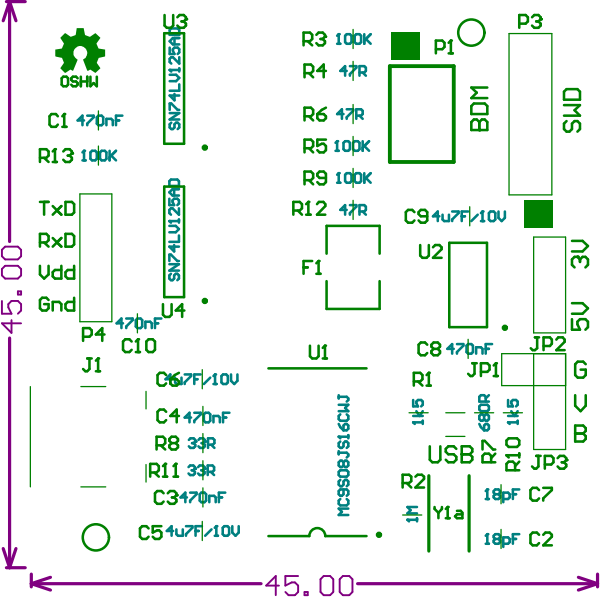
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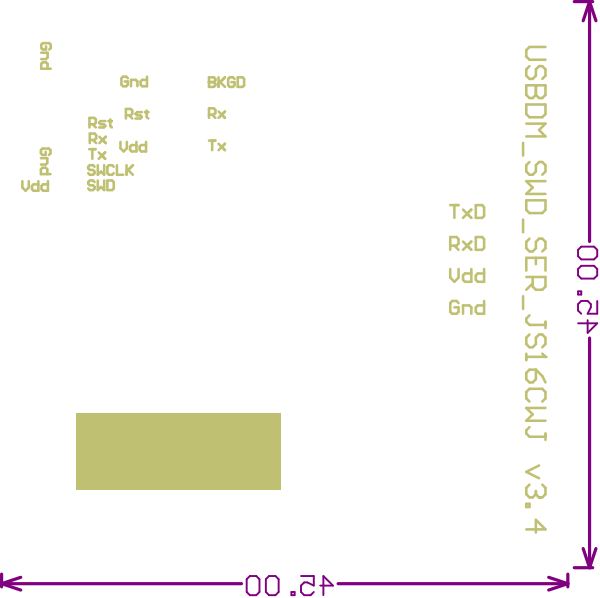
USBDM\_SMD\_SER\_JS16CMJ v3.4

TxD  
RxD  
Vdd  
Gnd



42.00





**USBDM\_JS16CWJ\_V2**

**USBDM\_SWD\_SER\_JS16CWJ.PRJPCB**

**USBDM SWD SER JS16CWJ.PRJPCB**

None

4:29:14 PM

4:29:20 PM

Approved		Notes				

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