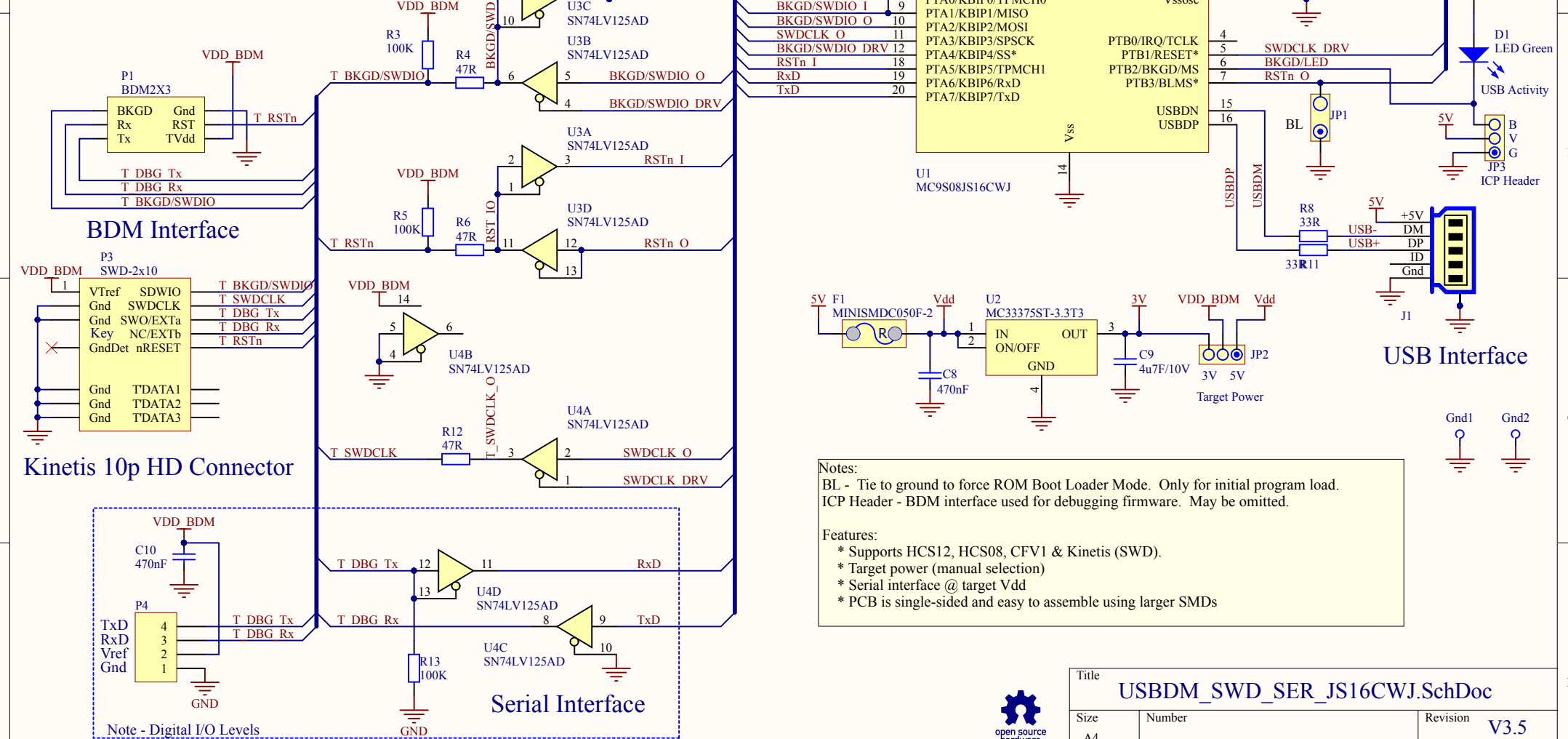
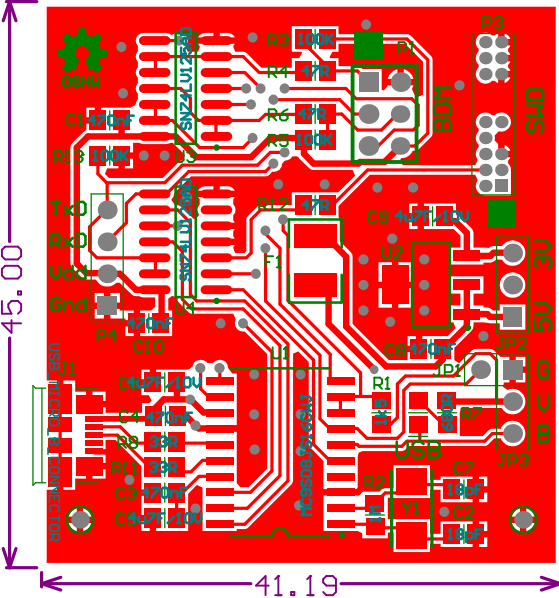


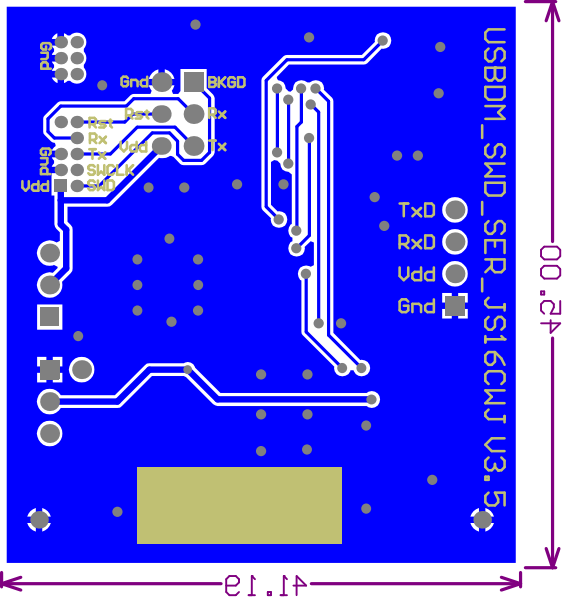
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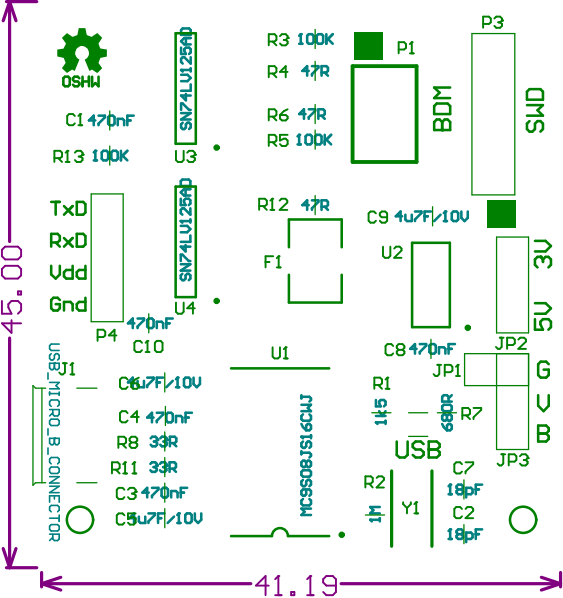
- 1.0 1st Release
- 1.1 Changed R7 to 1M, no PCB changes
- 2.0 Adds Serial Interface and complete PCB redo
- 2.1 Added RST_DRV
- 3.0 Deleted CTS,RTS added SWD interface
- 3.1 Deleted pins from P3 to fit 10p/20p plug
- 3.2 Added Rx/Tx to P1 & P3, Gnd TPs
- 3.3 Board shrink
- 3.4 Rationalised component values
- 3.5 Removed SWO, changed J1 to microUSB

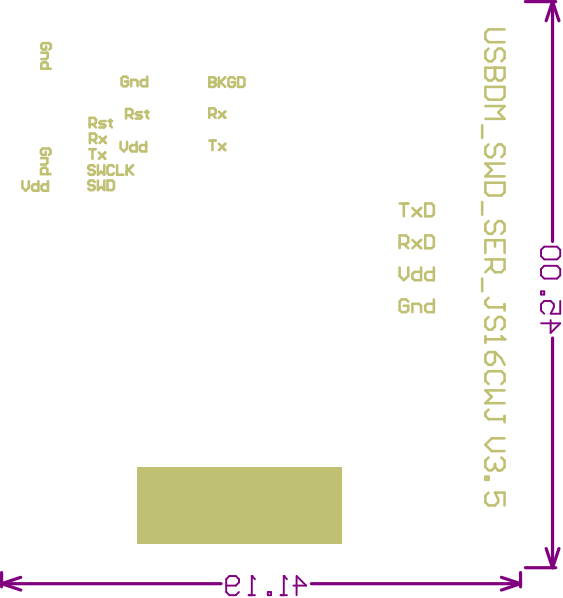


Title			USBDM_SWD_SER_JS16CWJ.SchDoc
Size	Number	Revision	
A4		V3.5	
Date:	2/04/2017	Sheet of	
File:	C:\Users\... \USBDM SWD SER JS16CWJ	Drawn by:	









Bill of Materials For Project [USBDM_SWD_SER_JS16CWJ.PRJPCB] (No PCB Document Selected)