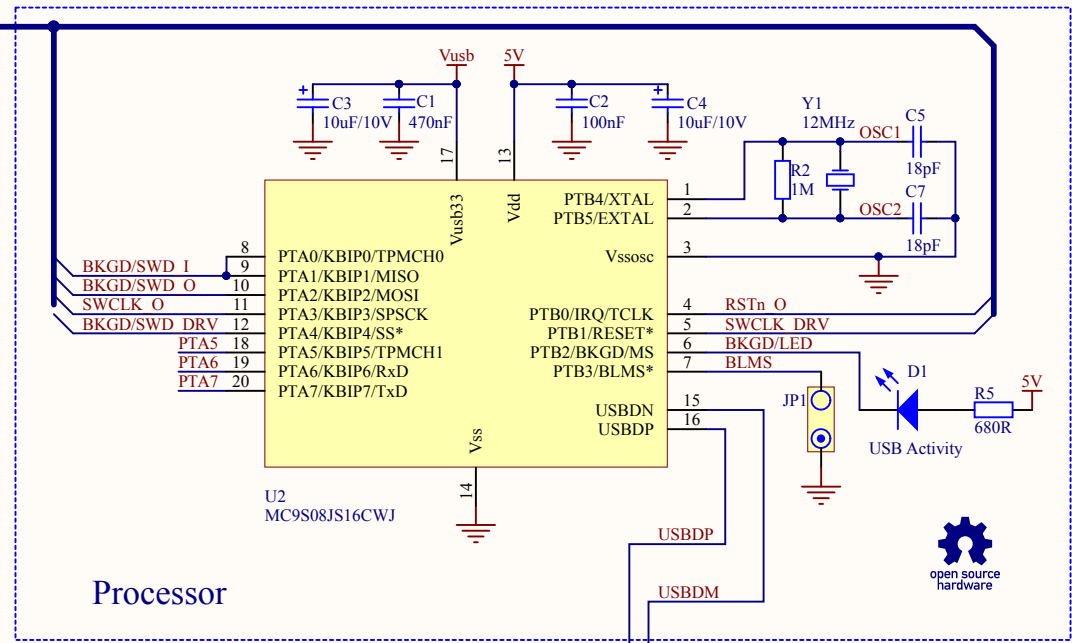


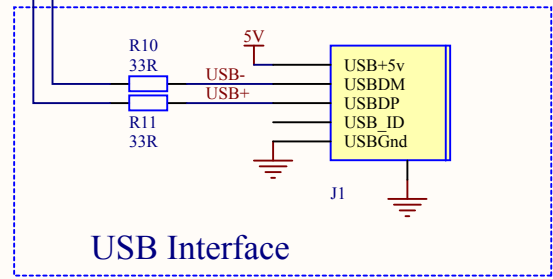
Kinetis 10p HD Connector



Processor

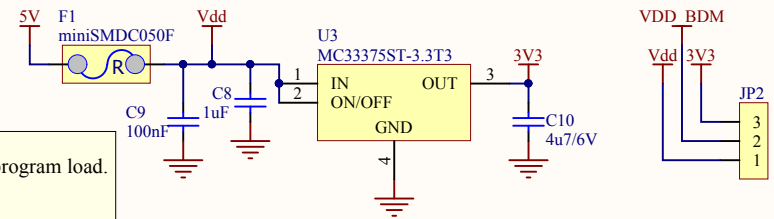
NOTE:
TI SN74LV125 devices must be used since they are 5V I/O tolerant.
Similar devices from other manufacturers may not be.

Revision:
1.0 1st Release
1.1 Added missing Gnd connection on PCB
2.1 Added regulator etc

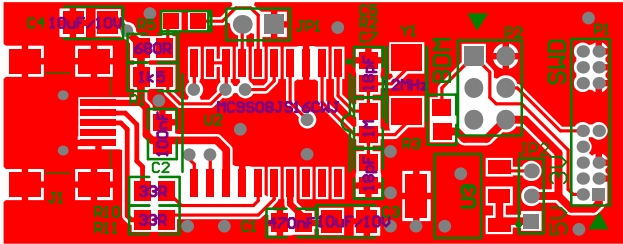


USB Interface

Notes:
JP1 - Tie to ground to force ROM Boot Loader Mode. Only for initial program load.
This version only supports HCS12, HCS08, & CFV1, Kinetis (SWD)
It does not support RS08 or CFV2,3,4 devices
It does not supply target power.
It does not have external target reset detection.



Title USBDM_SWD_JS16CWJ.SchDoc		
Size A4	Number	Revision 2.1
Date: 4/12/2016	Sheet of	
File: C:\Users\...\USBDM_SWD_JS16CWJ.SchDoc	Down By:	



USBDM_SWD_JS16CWJ V2.1

47R

R4

47R

R9

47R

R7

100K

R6

100nF

C6

R8

100K

C10

C8

SN74LV0254D

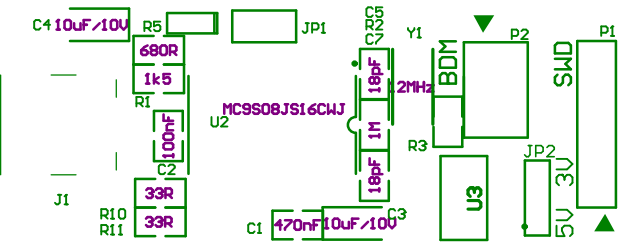
U1

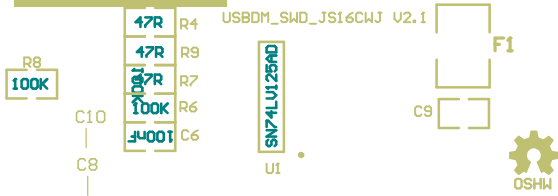
C9

F1



OSHW





USBDM_SOIC20_JS16(Minimal)

[illegible]