

A

B

C

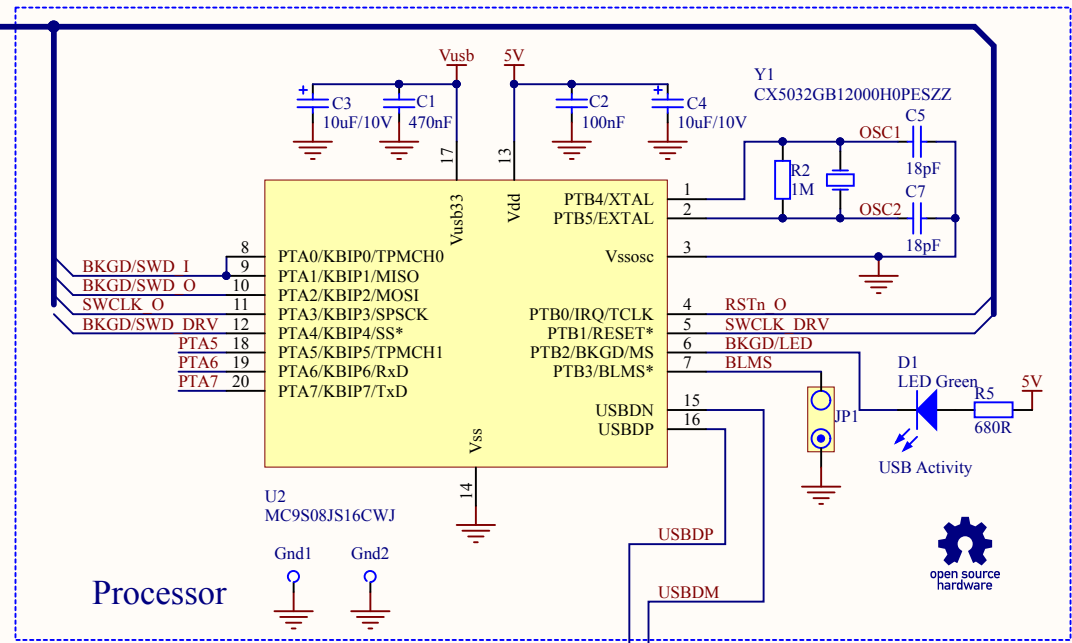
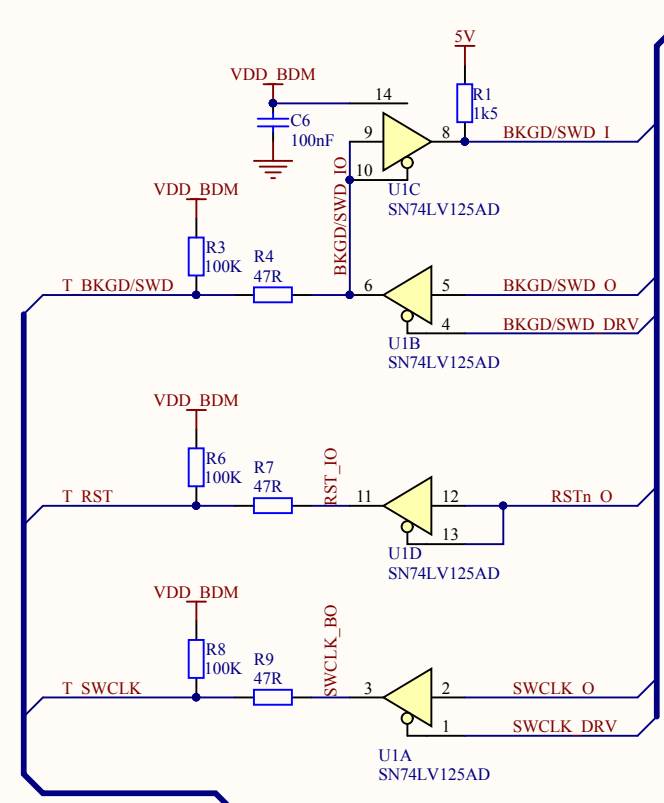
D

A

B

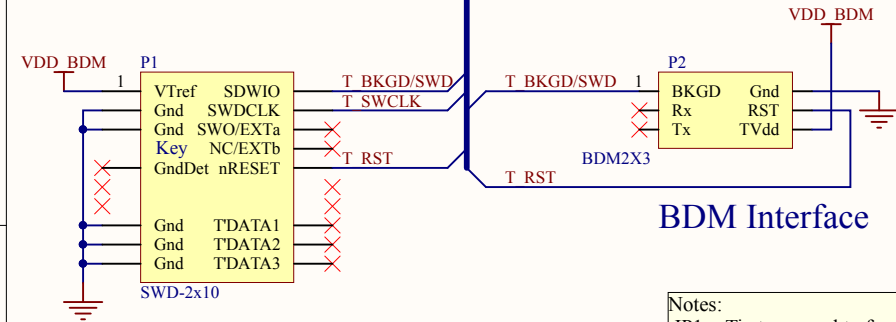
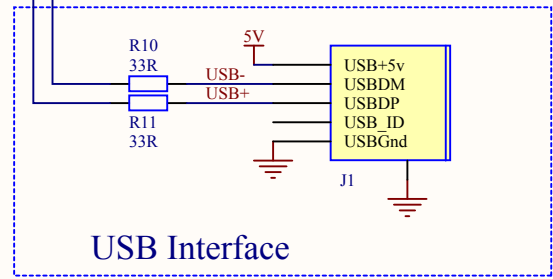
C

D



NOTE:  
TI SN74LV125 devices must be used since they are 5V I/O tolerant.  
Similar devices from other manufacturers may not be.

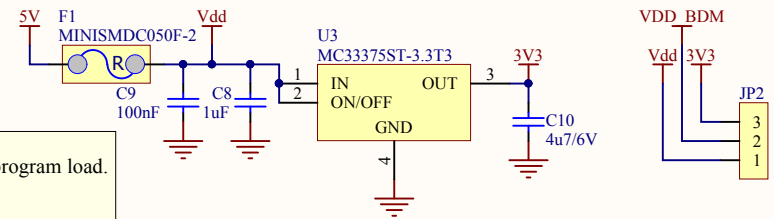
Revision:  
1.0 1st Release  
1.1 Added missing Gnd connection on PCB  
2.1 Added regulator etc



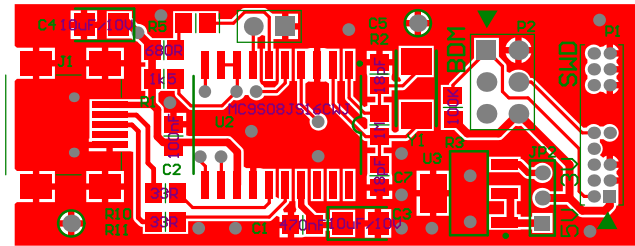
Kinetis 10p HD Connector

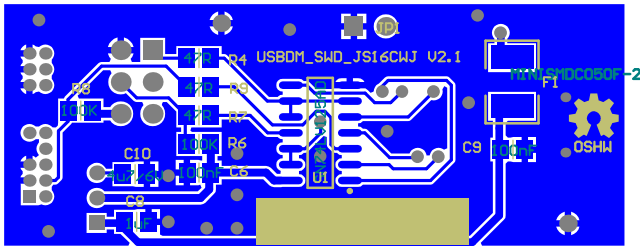
BDM Interface

Notes:  
JP1 - Tie to ground to force ROM Boot Loader Mode. Only for initial program load.  
This version only supports HCS12, HCS08, & CFV1, Kinetis (SWD)  
It does not support RS08 or CFV2,3,4 devices  
It does not supply target power.  
It does not have external target reset detection.



Title USBDM_SWD_JS16CWJ.SchDoc		
Size A4	Number	Revision 2.1
Date: 8/01/2017	Sheet of	
File: C:\Users\...\USBDM_SWD_JS16CWJ.SchDoc	Down By:	





C4 10 $\mu$ F/10V

R5

680R

1k5

R1

100nF

33R

33R

J1

MC9S08JS16CWJ  
U2

C5

R2

18pF

1M

18pF

C7

Y1

U3

R3

100K

BDM

P2

JP2

SWD

5V 3V

P1

C1

470nF

10 $\mu$ F/10V

C3

R8  
100K

C10  
4u7/6V  
C8  
1uF

47R R4  
47R R9  
47R R7  
100K R6  
100nF C6

SN74LV125AD

JP1  
USBDM\_SWD\_JS16CWJ V2.1

C9 100nF

MINISMD C050F-2  
F1



## USBDM\_SOIC20\_JS16(Minimal)

[illegible]