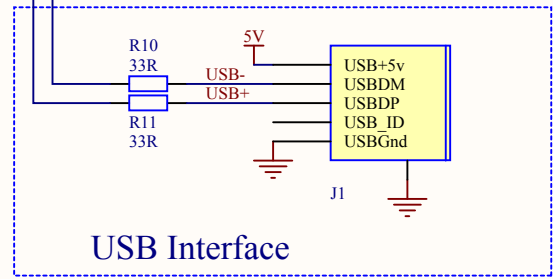
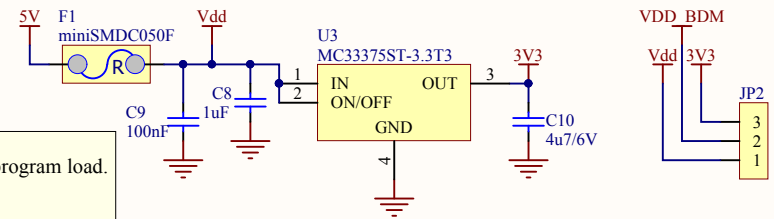


NOTE:  
TI SN74LV125 devices must be used since they are 5V I/O tolerant.  
Similar devices from other manufacturers may not be.

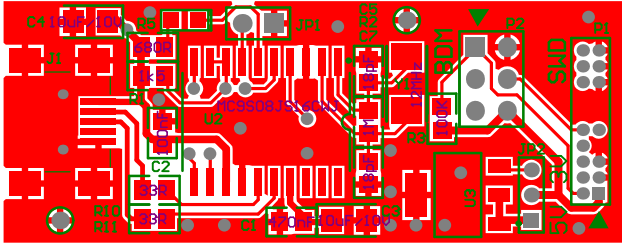
Revision:  
1.0 1st Release  
1.1 Added missing Gnd connection on PCB  
2.1 Added regulator etc

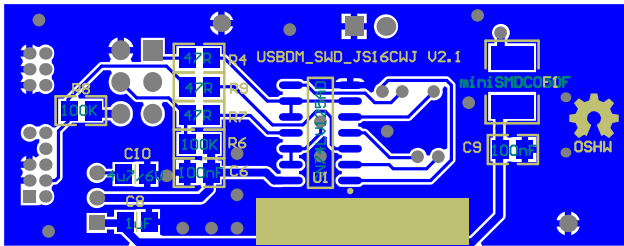


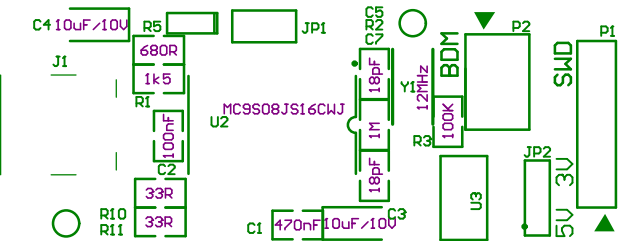
Notes:  
JP1 - Tie to ground to force ROM Boot Loader Mode. Only for initial program load.  
This version only supports HCS12, HCS08, & CFV1, Kinetis (SWD)  
It does not support RS08 or CFV2,3,4 devices  
It does not supply target power.  
It does not have external target reset detection.

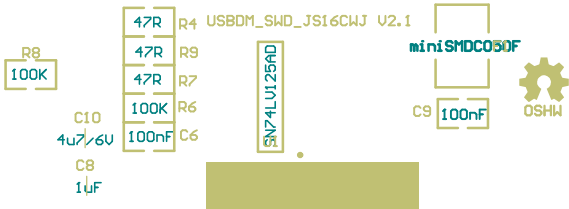


Title USBDM_SWD_JS16CWJ.SchDoc		
Size A4	Number	Revision 2.1
Date: 5/12/2016	Sheet of	
File: C:\Users\...\USBDM_SWD_JS16CWJ.SchDoc	Down By:	









## USBDM\_SOIC20\_JS16(Minimal)