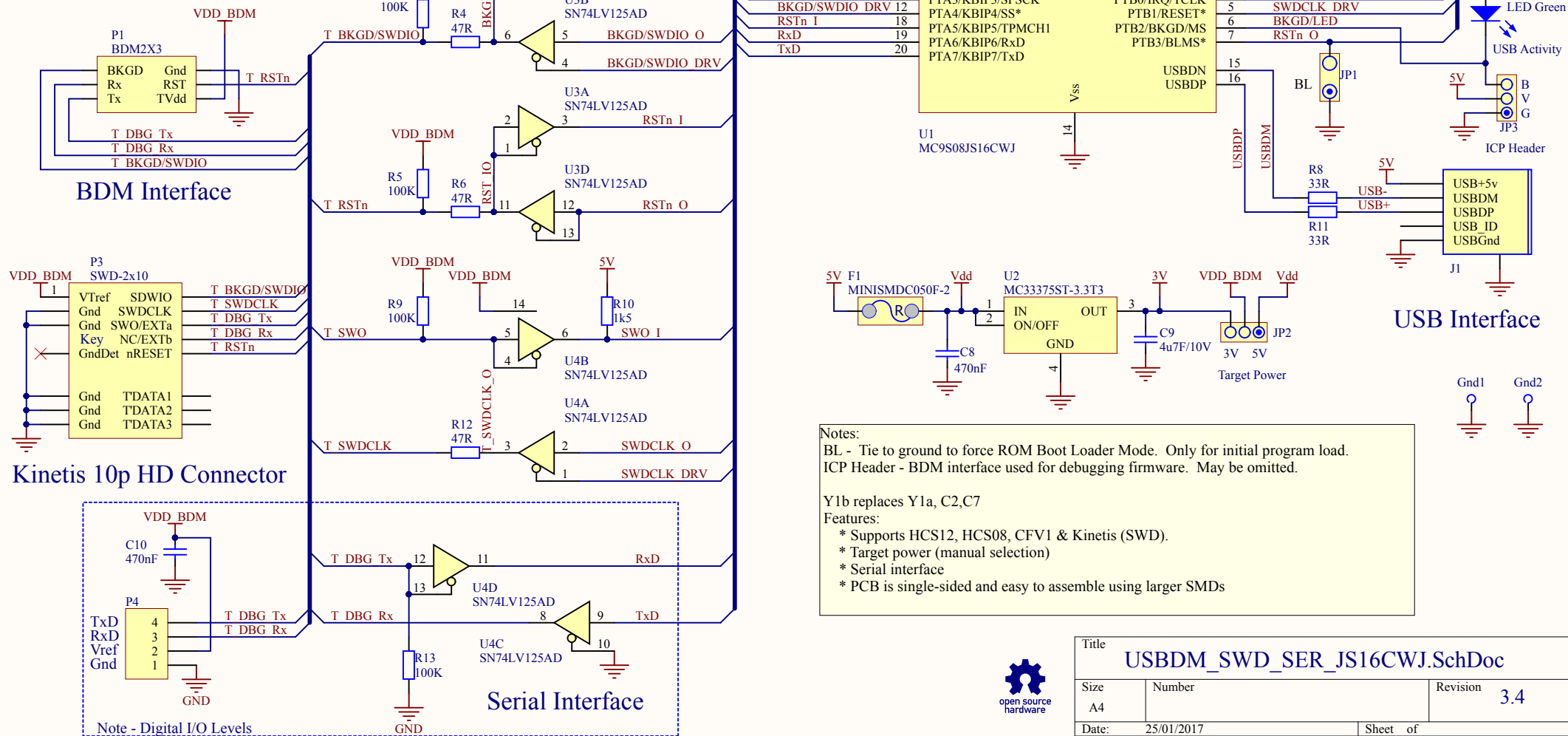
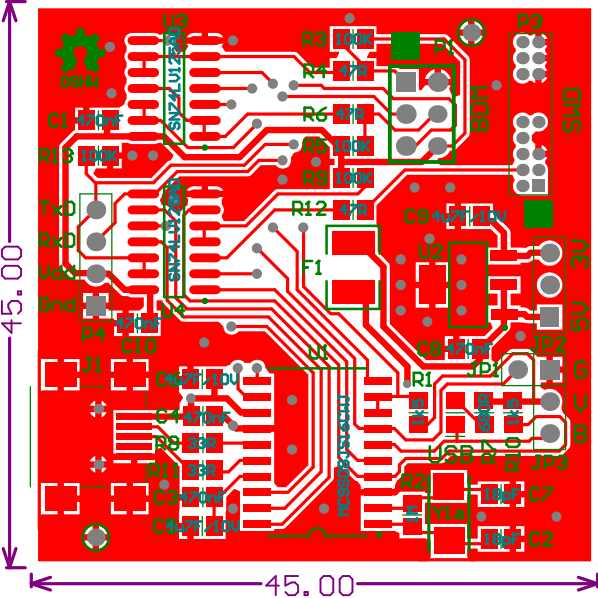


Revision:
 1.0 1st Release
 1.1 Changed R7 to 1M, no PCB changes
 2.0 Adds Serial Interface and complete PCB redo
 2.1 Added RST_DRV
 3.0 Deleted CTS,RTS added SWD interface
 3.1 Deleted pins from P3 to fit 10p/20p plug
 3.2 Added Rx/Tx to P1 & P3, Gnd TPs
 3.3 Board shrink
 3.4 Rationalised component values



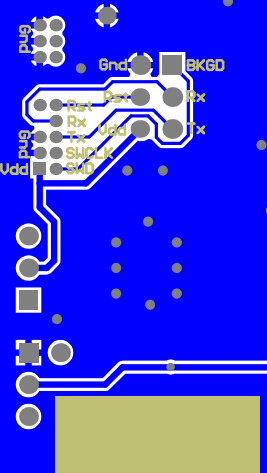
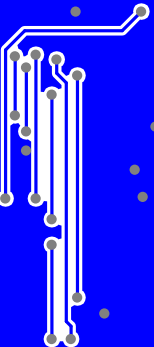
Title		
USBDM_SWD_SER_JS16CWJ.SchDoc		
Size	Number	Revision
A4		3.4
Date:	25/01/2017	Sheet of
File:	C:\Users\... \USBDM_SWD_SER_JS16CWJ.SchDoc	Drawn by:



00.24

USBDM_SMD_SER_JS16CMJ v3.4

TxD
RxD
Vdd
Gnd



42.00



OSHWA

C1 470nF

R13 100K

TxD

RxD

Vdd

Gnd

P4 470nF

J1

C10

C6 470nF/10V

C4 470nF

R8 33R

R11 33R

C3 470nF

C5 470nF/10V

U3

SN74LV125AD

SN74LV125AD

U4

R3 100K

R4 47R

R6 47R

R5 100K

R9 100K

R12 47R

F1

U1

MC9S08JS16CJ7

P1

BDM

C9 4u7F/10V

U2

C8 470nF

R1 1k5

R7 680R

R10 1k5

R2 1M

Y1a

18pF

18pF

P3

BDM

SMD

C9 4u7F/10V

U2

C8 470nF

R1 1k5

R7 680R

R10 1k5

R2 1M

Y1a

18pF

18pF

C7

C2

C2

P3

SMD

C9 4u7F/10V

U2

C8 470nF

R1 1k5

R7 680R

R10 1k5

R2 1M

Y1a

18pF

18pF

C7

C2

C2

P3

SMD

C9 4u7F/10V

U2

C8 470nF

R1 1k5

R7 680R

R10 1k5

R2 1M

Y1a

18pF

18pF

C7

C2

C2

P3

SMD

C9 4u7F/10V

U2

C8 470nF

R1 1k5

R7 680R

R10 1k5

R2 1M

Y1a

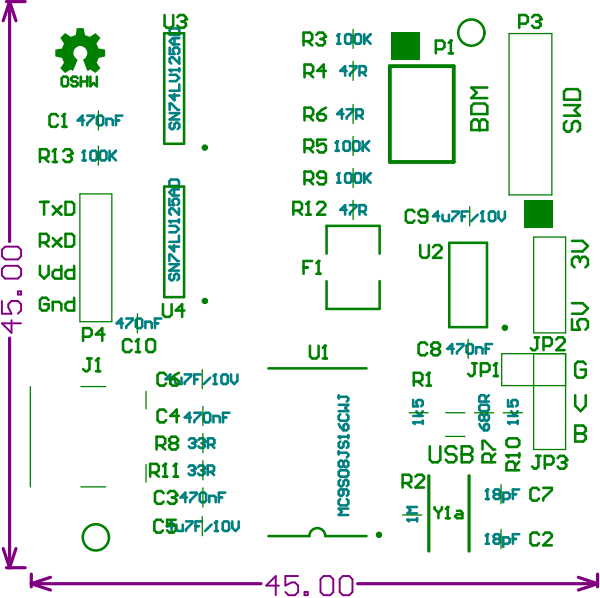
18pF

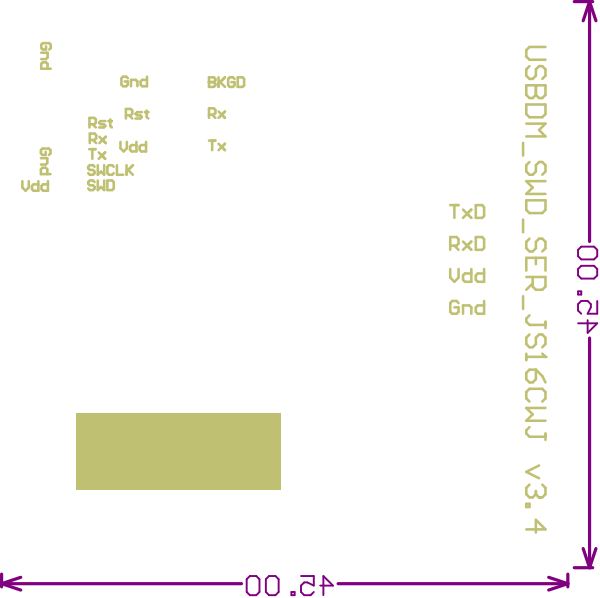
18pF

C7

C2

C2





USBDM_JS16CWJ_V2