

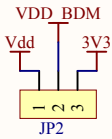
NOTE:
TI SN74LV125 devices must be used since they are 5V I/O tolerant.
Similar devices from other manufacturers may not be.

Revision:
1.0 1st Release
2.1 Added missing Gnd connection on PCB
2.1 Added regulator etc
2.2 Reduced individual component count, changed USB con.

BDM Interface

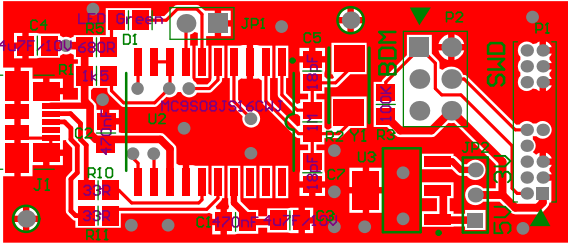
Notes:
JP1 - Tie to ground to force ROM Boot Loader Mode. Only for initial program load.
This version only supports HCS12, HCS08, & CFV1, Kinetis (SWD)
It does not support RS08 or CFV2,3,4 devices
It does not supply target power.
It does not have external target reset detection.

USB Interface



Title		
USBDM_SWD_JS16CWJ.SchDoc		
Size A4	Number	Revision 2.3
Date: 25/01/2017	Sheet of	
File: C:\Users\...\USBDM_SWD_JS16CWJ.SchDoc	Drawn By:	

USB_MICRO_B_CONNECTOR



USBDM_SWD_JS16CWJ V2.3

U1

SN74LV0254D

47R

P4

47R

R9

47R

R7

100K

R6

C10

4u7F/100V

470nF

C6

C8

4u7F/100V

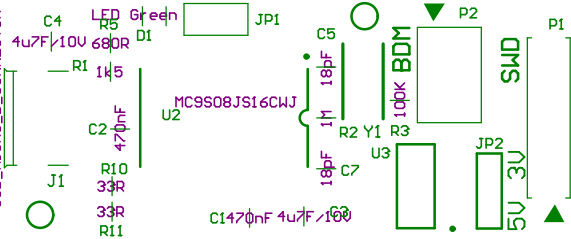
F1

470nF

C9

DSHW

USB_MICRO_B_CONNECTOR



R8
100K

C10
4u7F/10V

C8
4u7F/10V

47R R4

47R R9

47R R7

100K R6

470nF C6

USBDM_SWD_JS16CWJ V2.3

U1

SN74LV125AD

MINISMD060F-2

F1

470nF C9



USBDM_SOIC20_JS16(Minimal)

/J.PRJPCB

296-1674-5-ND

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