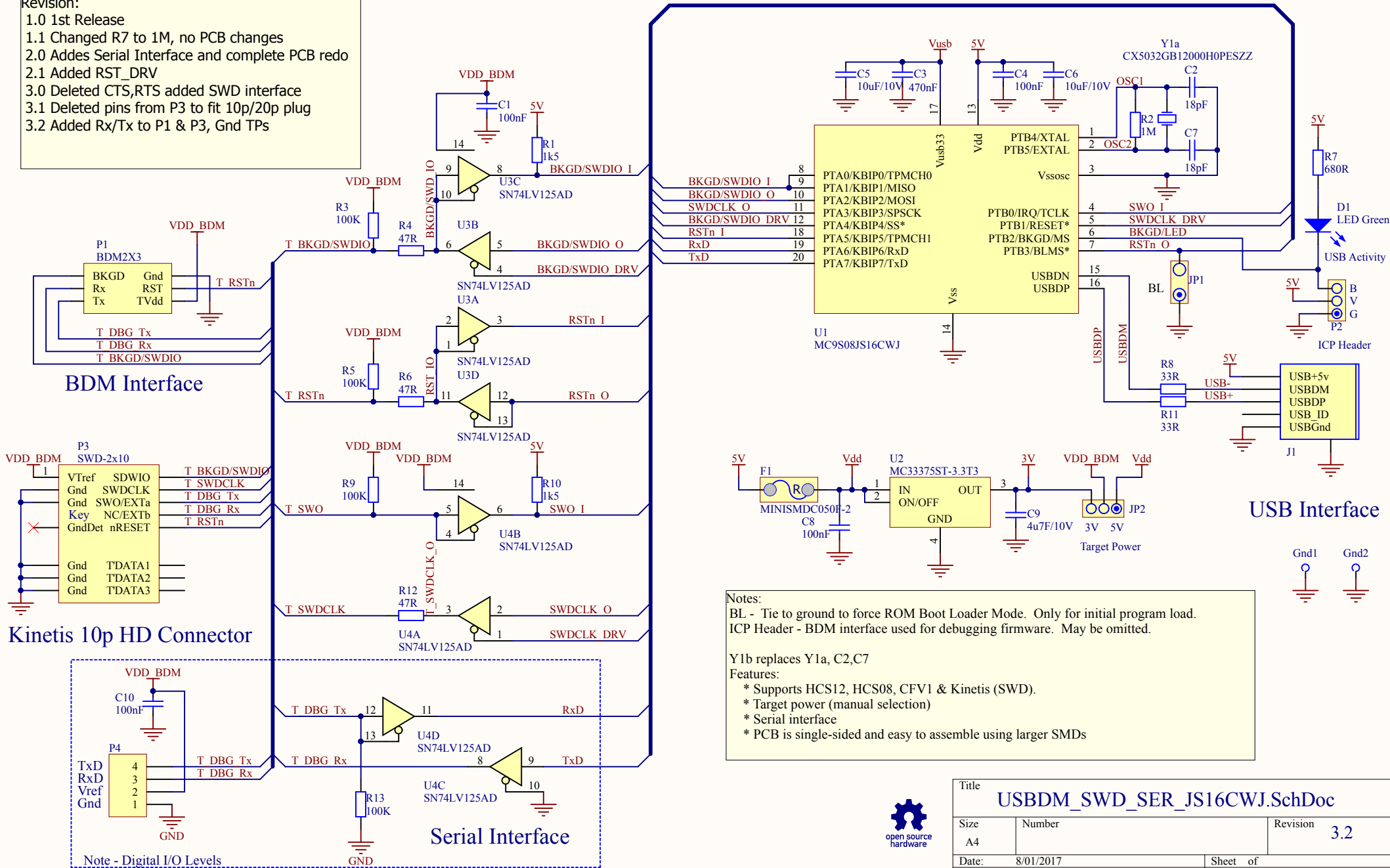


Revision:
 1.0 1st Release
 1.1 Changed R7 to 1M, no PCB changes
 2.0 Adds Serial Interface and complete PCB redo
 2.1 Added RST_DRV
 3.0 Deleted CTS,RTS added SWD interface
 3.1 Deleted pins from P3 to fit 10p/20p plug
 3.2 Added Rx/Tx to P1 & P3, Gnd TPs



Notes:
 BL - Tie to ground to force ROM Boot Loader Mode. Only for initial program load.
 ICP Header - BDM interface used for debugging firmware. May be omitted.

Y1b replaces Y1a, C2,C7

Features:
 * Supports HCS12, HCS08, CFV1 & Kinetis (SWD).
 * Target power (manual selection)
 * Serial interface
 * PCB is single-sided and easy to assemble using larger SMDs

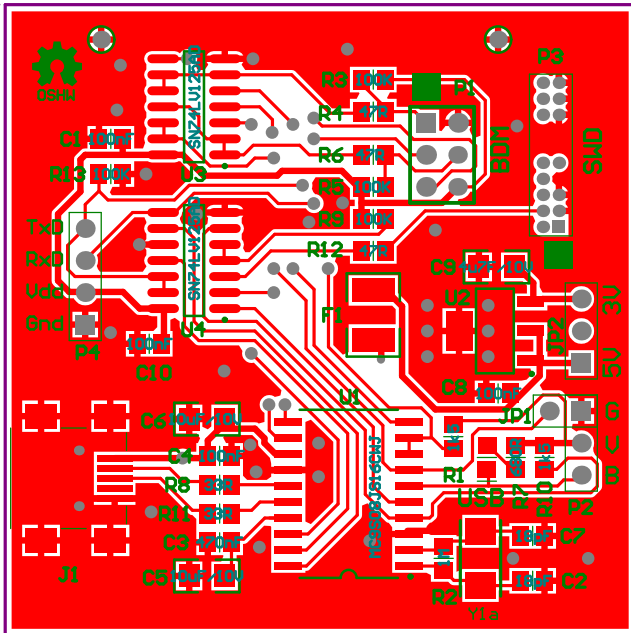
Title		
USBDM_SWD_SER_JS16CWJ.SchDoc		
Size	Number	Revision
A4		3.2
Date:	8/01/2017	Sheet of
File:	C:\Users\... \USBDM_SWD_SER_JS16CWJ\USBDM_SWD_SER_JS16CWJ.SchDoc	1

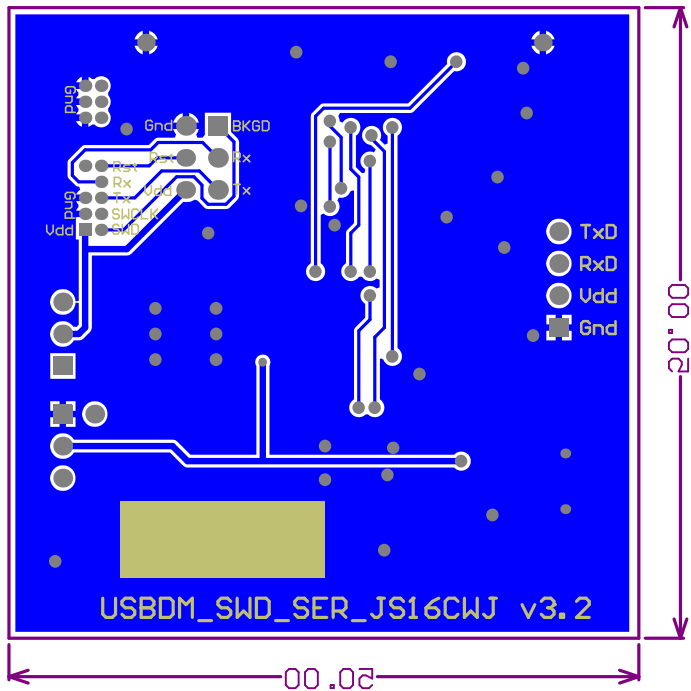
Board Outline this layer

50.00

50.00

USBDM_SWD_SER_JS16CJWJ.PcbDoc





Board Outline this layer

50.00

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USBDM_SWD_SER_JS16CJ.J_PcbDoc



OSHW

C1 100nF

R13 100k

TxD

RxD

Vdd

Gnd

P4

100nF

C10

C6 10uF/10V

C4 100nF

R8 33R

R11 33R

C3 470nF

C5 10uF/10V

J1

SN74LV125AP

U3

SN74LV125AP

U4

R3 100k

R4 47R

R6 47R

R5 100k

R9 100k

R12 47R

F1

U1

MC9S08JS16CJ.J



P1

BDM

P3

SWD

C9 4u7F/10V

U2

C8 100nF

JP1

R1 1k5

USB

R7 680R

R10 1k5

18pF

18pF

Y1a

R2 1k

JP2

3V

5V

G

G

G

G

G

G

G

G

G

G

G

G

G

USBDM_SWD_SER_JS16CWJ v3.2

USBDM_SWD_SER_JS16CWJ v3.2

Udd
pug
Gnd
SWCLK
SWD
Tx
Rx
Udd
Rst
Gnd
BKGD
Rx
Tx

Gnd
Udd
RxD
TxD

20.00

20.00

Board Outline this layer

USBDM_JS16CWJ_V2