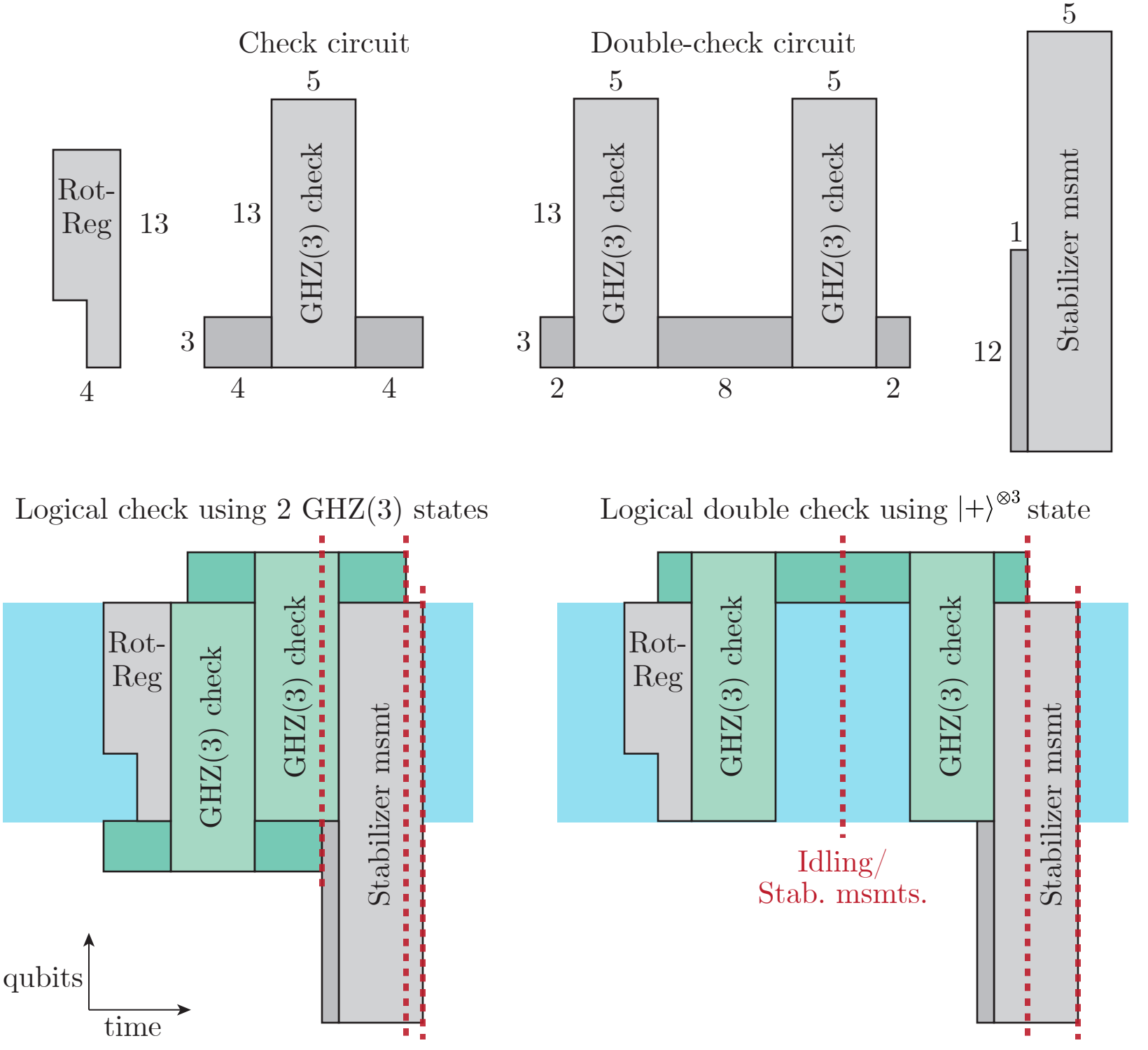
Check circuit
Rot-
Reg
13
4
5
13
GHZ(3) check
3
4
4
Double-check circuit
5
5
13
GHZ(3) check
GHZ(3) check
3
2
8
2
5
1
12
Stabilizer msmt
Logical check using 2 GHZ(3) states
Rot-
Reg
GHZ(3) check
GHZ(3) check
Stabilizer msmt
qubits
time
Logical double check using $|+\rangle^{\otimes 3}$ state
Rot-
Reg
GHZ(3) check
GHZ(3) check
Stabilizer msmt
Idling/
Stab. msmts.