Hien Vu

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A researcher specializing in the end-to-end development and optimization of machine learning models, wireless sensing, and embedded systems. Seeking an R&D role (co-op or full time) to translate complex research into tangible technological solutions.

EDUCATION

Ph.D. in Electrical and Computer Engineering , GPA: 3.65 (candidate) (expected) Aug 2024–2026
Purdue University, West Lafayette, Indiana, USA

M.Sc. in Electrical and Computer Engineering, GPA: 3.82 Aug 2021 – May 2023
University of Wisconsin–Madison (UW-Madison), Wisconsin, USA

B.Sc. in Electronics and Telecommunications Engineering, GPA: 3.51 Aug 2014 – May 2018
Hanoi University of Science and Technology, Hanoi, Vietnam

KEY SKILLS

- **Hardware & Embedded**: Verilog, MIPS, RISC-V, PCB Design (Altium), mmWave, UART/I2C/SPI, RF and high-speed design.
- **Software & Algorithms**: Python, C/C++, MATLAB, assembly, bash; sensor fusion, computer vision, signal processing.

PROFESSIONAL EXPERIENCE

Research Assistant, **NEIS Lab, Purdue University, Indiana, USA** Aug 2024 – Present

- Engineered an advanced mmWave radar and Edge AI system to track respiration of multiple moving subjects, achieving high accuracy in harsh environments for health monitoring applications.
- Architected an end-to-end multimodal sensor fusion pipeline for behavior classification, providing strategic insights into design trade-offs to optimize system cost and complexity.
- Recruited and trained a team of 20+ people to curate a large-scale multimodal dataset (20k RGB images, 300k seconds of data) for accelerating research in temporal-behavior classification.

Research Assistant, **WISEST Lab, UW-Madison, Wisconsin, USA** Aug 2021 – Jul 2024

- Designed a wearable sensor tag with a shared-coil architecture for simultaneous RFID and wireless charging, solving mutual coupling problem to realize a compact wearable design.
- Developed an optimized embedded firmware that reduced RFID energy costs by 15x, and engineered a closed-loop wireless power transfer protocol to enable perpetual device operation.
- Collaborated with cross-discipline teams to design and deploy a multi-node IoT network, analyzing over 2,000 hours of field data to validate system reliability in complex operational settings.

Research Assistant, **EC Lab, Soongsil University, Seoul, South Korea** Mar 2019 – Jul 2021

- Designed a flexible, portable supercapacitor power device delivering 840W in 5-second pulses, optimizing energy density and form factor for rapid and critical military applications (photos).
- Developed a bidirectional buck-boost control algorithm that enables uninterrupted power delivery and continuous system operation under dynamic load conditions.
- Implemented a high-speed FPGA-based design for 512GB SLC-NAND flash storage, ensuring signal integrity and minimizing propagation delay across 1,000+ components (photos).
- Implemented an Extended Kalman Filter on a RISC-V MCU to process 9-DOF IMU data, integrating with Parallel Tracking and Mapping (PTAM) for real-time 3D pose estimation in AR applications (demo).

System Engineer, **Interland Inc., Hanoi, Vietnam** Jun 2018 – Feb 2019

- Investigated sensing solutions for measuring dissolved oxygen in water, and integrated with IBM-based cloud service for large-scale automated shrimp farming.

SELECTED PROJECTS

MmCows: Multimodal Deep Learning Benchmark (NeurIPS) 2024

- Created a large-scale multimodal dataset accepted as a Spotlight paper (top 5% of ratings) at NeurIPS 2024. Benchmarked state-of-the-art action recognition models to advance edge computing capabilities.

eTag: Energy-Neutral Wireless Sensing System (ACM MobiCom) 2023

- Developed an end-to-end energy-neutral sensing system published at ACM MobiCom (24% acceptance rate), leveraging backscattering for communication and inductive coupling for power delivery, enabling maintenance-free health monitoring.

5-Stage Pipelined CPU Design | Course Project 2022

- Designed and verified a fully functional 32-bit MIPS processor in Verilog with 5-stage pipeline and L1-level cache. Engineered hazard detection units and data forwarding paths to resolve data and control hazards.