The original port name was o_0[7:0]. So, the modified constraints are as follows

```
## LEDs
set_property PACKAGE_PIN U16 [get_ports {o_0[0]}]
        set_property IOSTANDARD LVCMOS33 [get_ports {o_0[0]}]
set_property PACKAGE_PIN E19 [get_ports {o_0[1]}]
        set_property IOSTANDARD LVCMOS33 [get_ports {o_0[1]}]
set_property PACKAGE_PIN U19 [get_ports {o_0[2]}]
        set_property IOSTANDARD LVCMOS33 [get_ports {o_0[2]}]
set_property PACKAGE_PIN V19 [get_ports {o_0[3]}]
        set_property IOSTANDARD LVCMOS33 [get_ports {o_0[3]}]
set_property PACKAGE_PIN W18 [get_ports {o_0[4]}]
        set_property IOSTANDARD LVCMOS33 [get_ports {o_0[4]}]
set_property PACKAGE_PIN U15 [get_ports {o_0[5]}]
        set_property IOSTANDARD LVCMOS33 [get_ports {o_0[5]}]
set_property PACKAGE_PIN U14 [get_ports {o_0[6]}]
        set_property IOSTANDARD LVCMOS33 [get_ports {o_0[6]}]
set_property PACKAGE_PIN V14 [get_ports {o_0[7]}]
        set_property IOSTANDARD LVCMOS33 [get_ports {o_0[7]}]
```