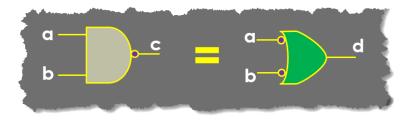
Combinational Circuit-Logic Gates: Quiz Solution

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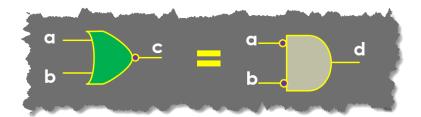
This file is a resource of the Udemy course: Digital System Design with High-Level Synthesis for FPGA: Combinational Circuits https://www.udemy.com/course/his-combinational-circuits/?referralCode=8D449A491B9F4582DDEF

If we have a look at the truth-table of the two gates in the first rule we will notice that the outputs c and d are identical, which means the gates are equivalent.



а	b	С	d
0	0	1	1
0	1	1	1
1	0	1	1
1	1	0	0

We have the same situation for the second rule.



а	b	С	d
0	0	1	1
0	1	0	0
1	0	0	0
1	1	0	0