

# 1 Digital System Design with High-Level Synthesis in FPGA

## Basic Output Configuration: Quiz Solution

[www.highlevel-synthesis.com](http://www.highlevel-synthesis.com)

This file is a resource of the UdeMy course: Digital System Design with High-Level Synthesis for FPGA: Combinational Circuits  
<https://www.udemy.com/course/hls-combinational-circuits/?referralCode=8D449A491B9F4582DDEF>

The location code of the LD6 is U14 which is shown the following figure.

