Digital System Design with High-Level Synthesis in FPGA

FPGAConcept-Design Approach: Quiz Solution

www.highlevel-synthesis.com

Quiz-1

Using C/C++ languages along with HLS design techniques is more suitable for implementing complex algorithms (e.g., machine learning algorithms) on FPGAs. Because designers only need to focus on their functionality instead of the low-level timing or resource usage in contrast to HDL languages such as VHDL.