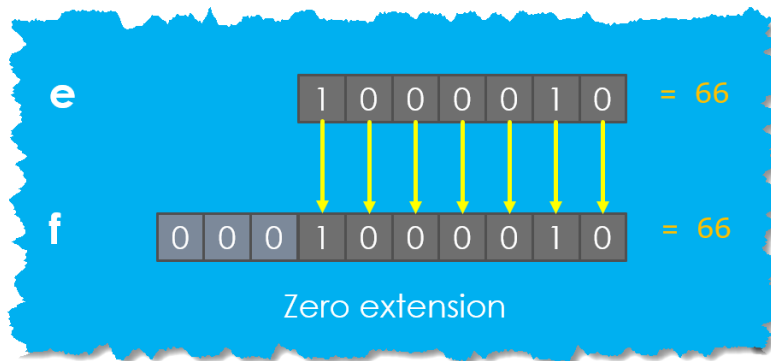


```
ap_uint<7> e = 0x42;  
ap_uint<10> f;  
f = e;  
std::cout << " e = " << e << std::endl;  
std::cout << " f = " << f << std::endl;
```

The output of this code is

e = 66
f = 66



2 Digital System Design with High-Level Synthesis in FPGA

```
ap_uint<20> n = 0x562a;  
ap_uint<9> k ;  
k = n;  
std::cout << " n = " << n << std::endl;  
std::cout << " k = " << k << std::endl;
```

The output of this code is

n = 22058

k = 42

