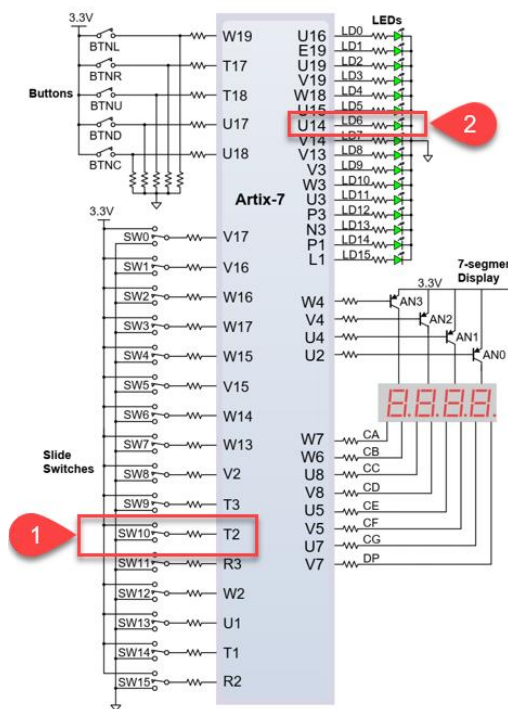


1 Digital System Design with High-Level Synthesis in FPGA

The circuit uses the SW10 as the slide switch and LD6 as the LED.



The following figure shows the corresponding FPGA name to SW10 and LD6, which are T2 and U14, respectively.



This figure shows the pins locations.

