FPGA Concept- FPGA Basic: Quiz Solution

www.highlevel-synthesis.com

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The look-up table should keep all the possible values of expression output which can be represented by the last column on its truth table. The following figure shows the content of the last column of the expression truth table that can be copied to the LUT.

a	b	С	d
0	0	0	0
1	0	0	0
0	1	0	1
1	1	0	1
0	0	1	0
1	0	1	0
0	1	1	0
1	1	1	1

