

1 Digital System Design with High-Level Synthesis in FPGA

Basic Input-Output Configuration: Quiz Solution

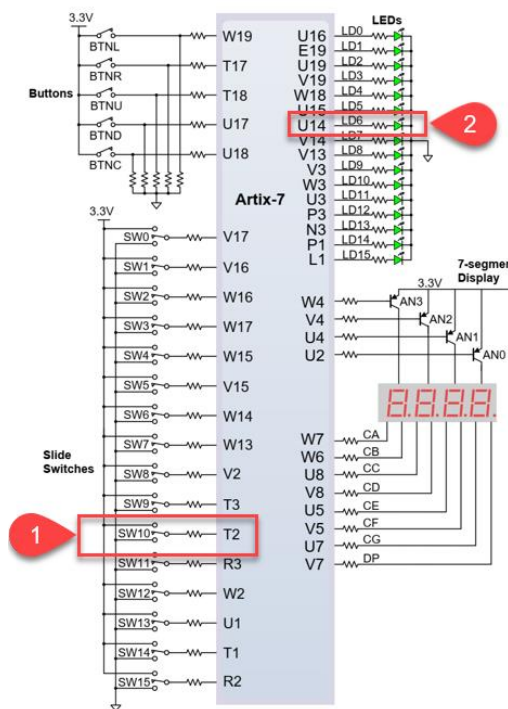
www.highlevel-synthesis.com

This file is a resource of the UdeMy course: Digital System Design with High-Level Synthesis for FPGA: Combinational Circuits
<https://www.udemy.com/course/hls-combinational-circuits/?referralCode=8D449A491B9F4582DDEF>

The circuit uses the SW10 as the slide switch and LD6 as the LED.



The following figure shows the corresponding FPGA name to SW10 and LD6, which are T2 and U14, respectively.



This figure shows the pins locations.

