Digital System Design with High-Level Synthesis in FPGA

Basic Output Configuration: Quiz Solution

www.highlevel-synthesis.com

A C/C++ function can describe only the logic circuit functionality inside the FPGA.

Physical constraints can describe the connections between the logic circuit inside the FPGA and the FPGA pins.

Finally, the connections between the FPGA pins and the LEDs on the board should be described in the layout editor tools.