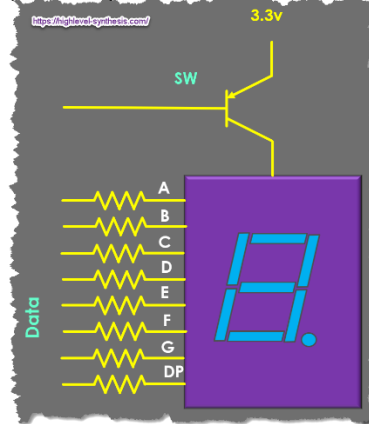
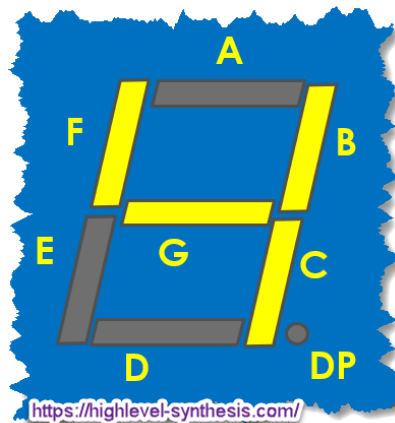


This file is a resource of the UdeMy course: Digital System Design with High-Level Synthesis for FPGA: Combinational Circuits
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To turn on the 7-segment, the control signal connected to the PNP transistor switch should be 0.

To show the number 4, the data signal should turn on the LEDs on the four segments B, C, F and G. So, as the 7-segment configuration is common cathode the data is 0b10011001



DP	G	F	E	D	C	B	A
1	0	0	1	1	0	0	1