Digital System Design with High-Level Synthesis in FPGA

FPGA Concept-HW/SW Analogy: Quiz Solution

www.highlevel-synthesis.com

This file is a resource of the Udemy course: Digital System Design with High-Level Synthesis for FPGA: Combinational Circuits https://www.udemy.com/course/hls-combinational-circuits/?referralCode=8D449A491B9F4582DDEF

The first group are added to help HLS tools in interface synthesis process.

The second group are added to help HLS tools in function optimisation process.