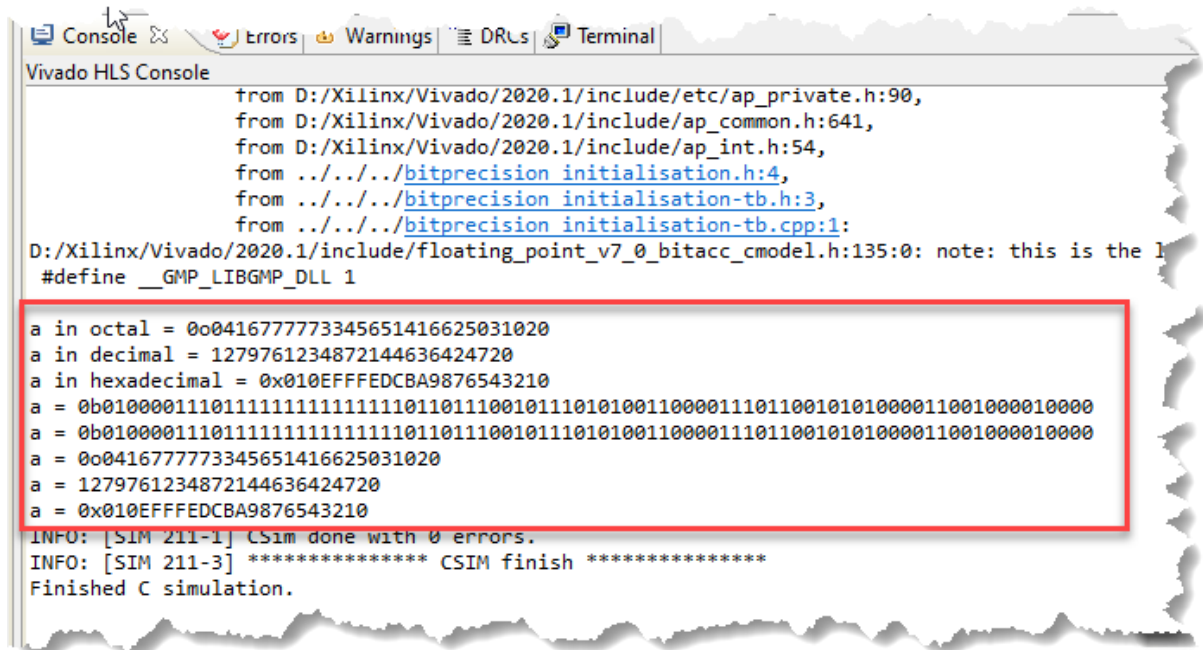


The following figure shows the output of the code



```

Vivado HLS Console
from D:/Xilinx/Vivado/2020.1/include/etc/ap_private.h:90,
from D:/Xilinx/Vivado/2020.1/include/ap_common.h:641,
from D:/Xilinx/Vivado/2020.1/include/ap_int.h:54,
from ../../../../bitprecision_initialisation.h:4,
from ../../../../bitprecision_initialisation-tb.h:3,
from ../../../../bitprecision_initialisation-tb.cpp:1:
D:/Xilinx/Vivado/2020.1/include/floating_point_v7_0_bitacc_cmodel.h:135:0: note: this is the 1
#define __GMP_LIBGMP_DLL 1

a in octal = 0o041677773345651416625031020
a in decimal = 1279761234872144636424720
a in hexadecimal = 0x010EFFFEDCBA9876543210
a = 0b0100001110111111111111101101101100101110101001100110010101000011001000010000
a = 0b0100001110111111111111101101101100101110101001100110010101000011001000010000
a = 0o041677773345651416625031020
a = 1279761234872144636424720
a = 0x010EFFFEDCBA9876543210

INFO: [SIM 211-1] CSim done with 0 errors.
INFO: [SIM 211-3] ***** CSIM finish *****
Finished C simulation.

```