

1 Digital System Design with High-Level Synthesis in FPGA

Multiplexer HLS: Quiz Solution

www.highlevel-synthesis.com

This file is a resource of the UdeMy course: Digital System Design with High-Level Synthesis for FPGA: Combinational Circuits
<https://www.udemy.com/course/hls-combinational-circuits/?referralCode=8D449A491B9F4582DDEF>

The following figure shows the circuit structure of the switch-case statements in the quiz.

