



## Dear All,

High-level synthesis (HLS) is a pretty new and dynamic area in hardware design and function acceleration. Every month new people and companies try to use the techniques, tools and platforms provided by key players in the industry and academia. I have designed the high-level synthesis online courses to explain these techniques and tools in a very simple language that many people can understand. For this purpose, I have tried to prepare a step-by-step approach along with several examples and exercises. However, the HLS is a dynamic area, as we regularly see some new concepts, techniques, tools, platforms and applications. To cope with this dynamic atmosphere, I decided to publish a newsletter every month. It is short and sweet and gives you some information about my courses, new activities in industry and academia. Hopefully, you enjoy and support that.

Mohammad Hosseinabady, PhD



## Our HLS Community



In addition to the Q&A and messaging mechanism in each course, you can use the following websites to ask questions that may not fit the course subjects or communicate with other people with the same interests.

- 1- I have created a public Facebook group (<a href="https://www.facebook.com/groups/372632747203348">https://www.facebook.com/groups/372632747203348</a>) for High-Level Synthesis. You can apply for the membership and start communicating with other people working in the same area.
- 2- The course weblog is <a href="https://highlevel-synthesis.com/">https://highlevel-synthesis.com/</a>, where I regularly publish articles about HLS.
- 3- The YouTube channel is <a href="https://www.youtube.com/channel/UCbepp45-ABAGDSp2MmtdD8A">https://www.youtube.com/channel/UCbepp45-ABAGDSp2MmtdD8A</a>, where I regularly put some video about HLS.
- 4- Also, you can find me on Twitter at @HighLevelSynth1

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## Industry



Xilinx recently has introduced KRIA, an adaptive System-on-Module (SoM) targeting Edge computing and IoT applications. It is based on Zynq UltraScale+ MPSoC and designed specifically for AI vision application. In future, I will more talk about this SoM. The SoM is probably the cheapest FPGA board based on Zynq MPSoC in the market (only \$199).

For more information, please refer to the Xilinx website or this article on the hackster website.