

A

B

C

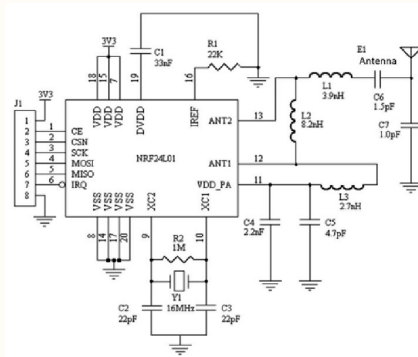
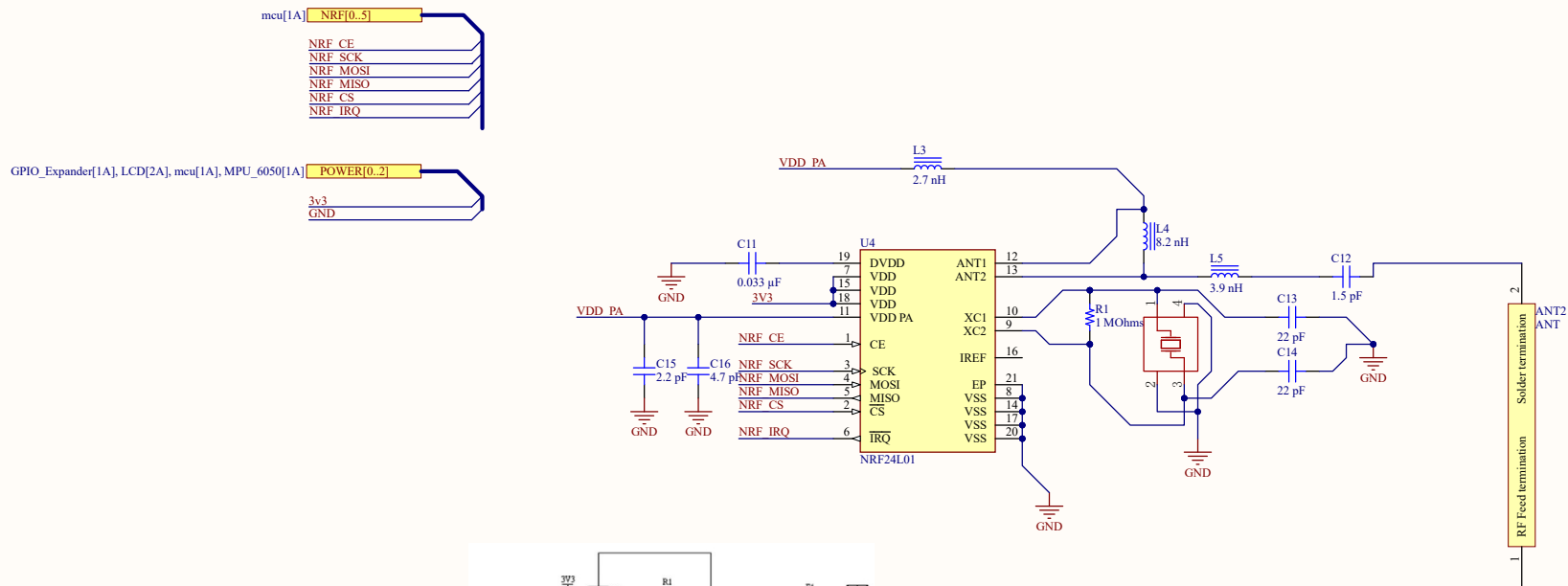
D

A

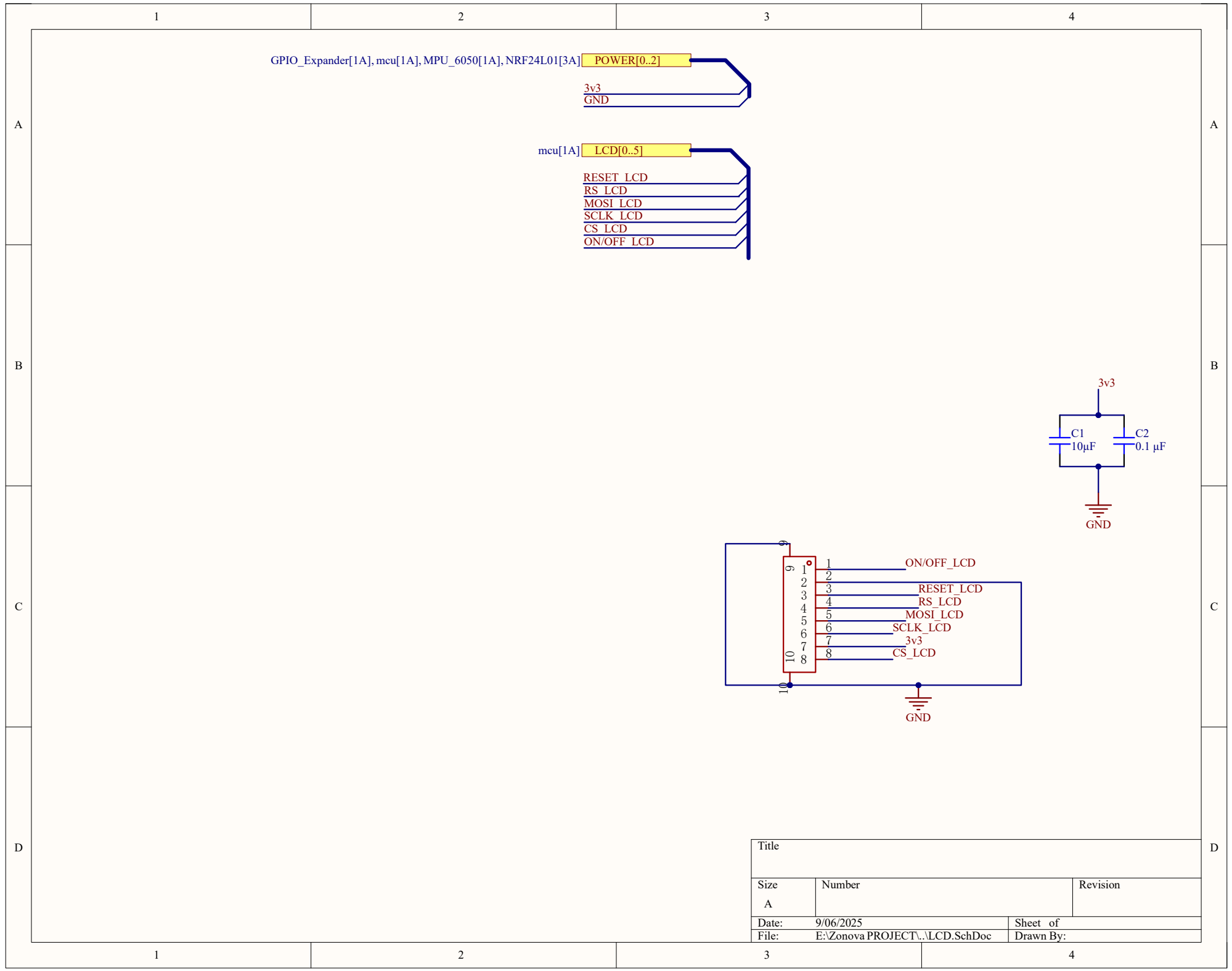
B

C

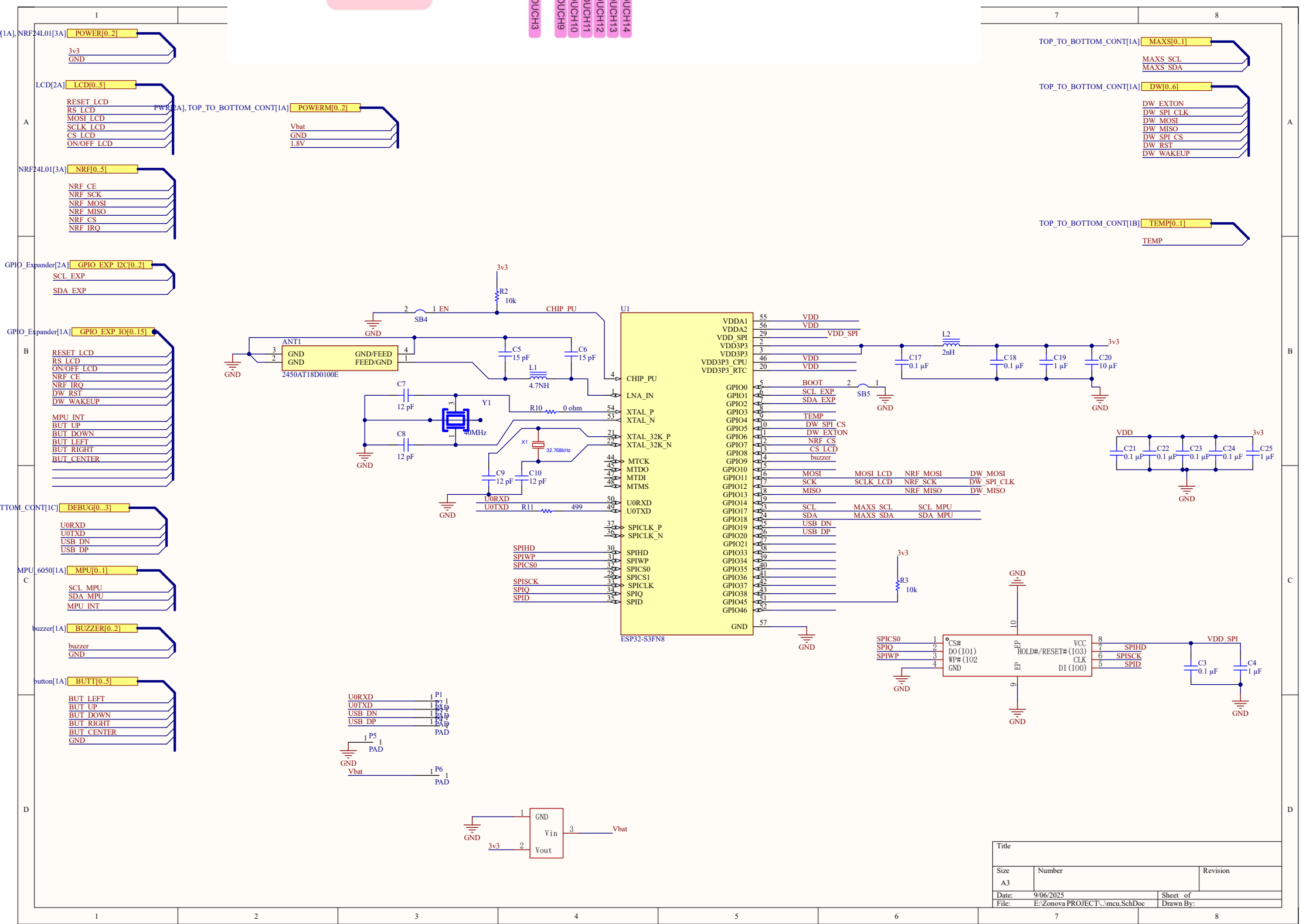
D

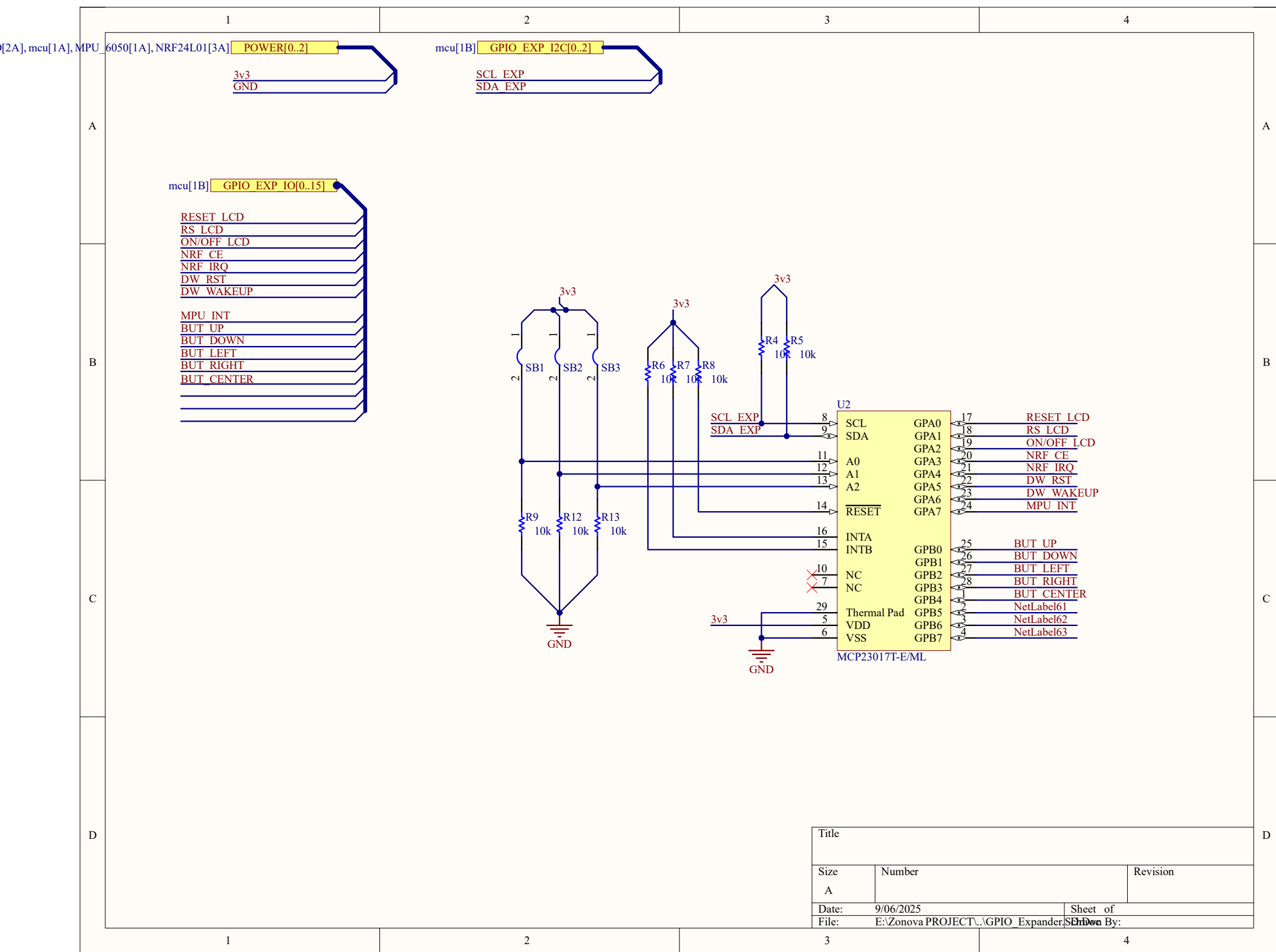


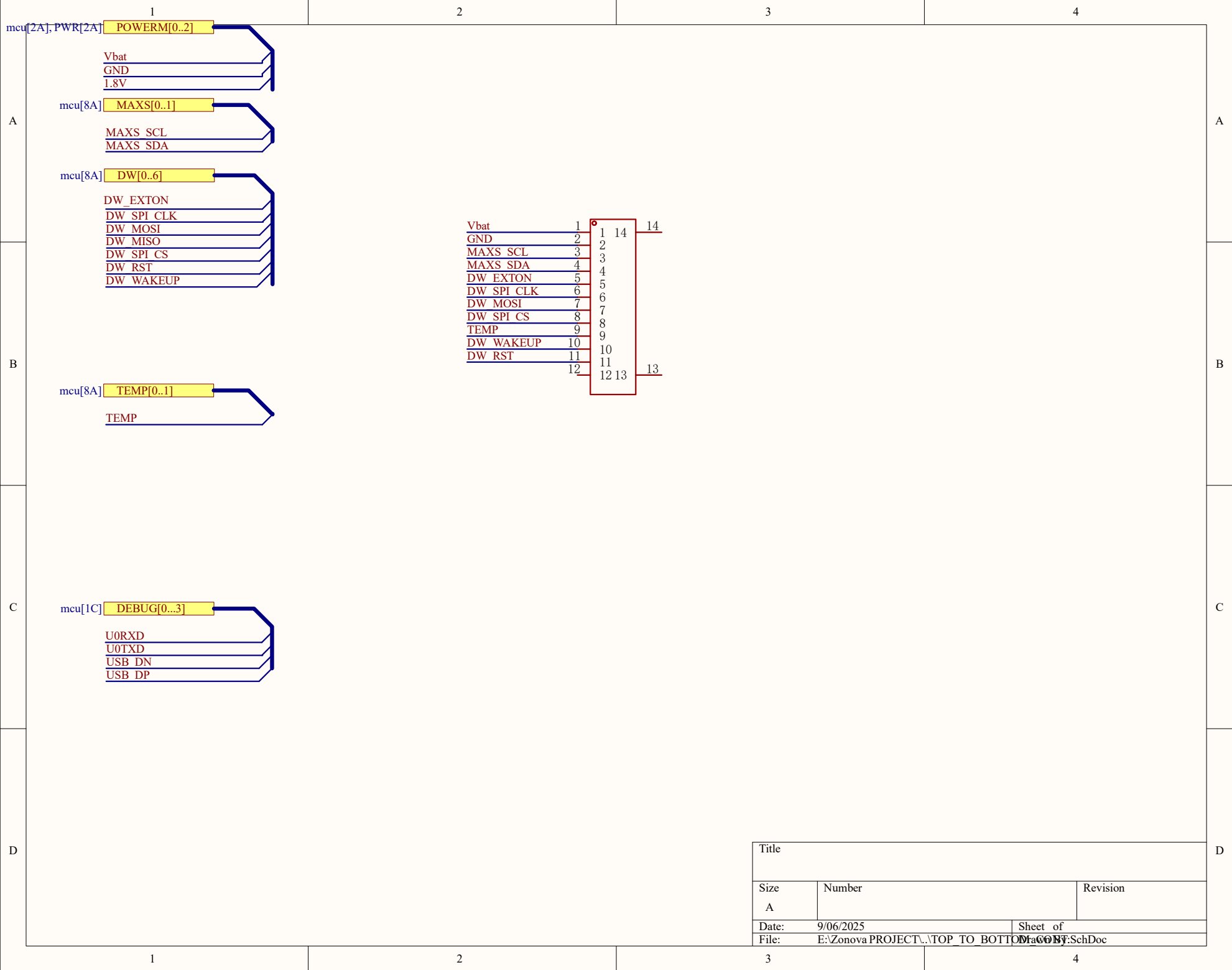
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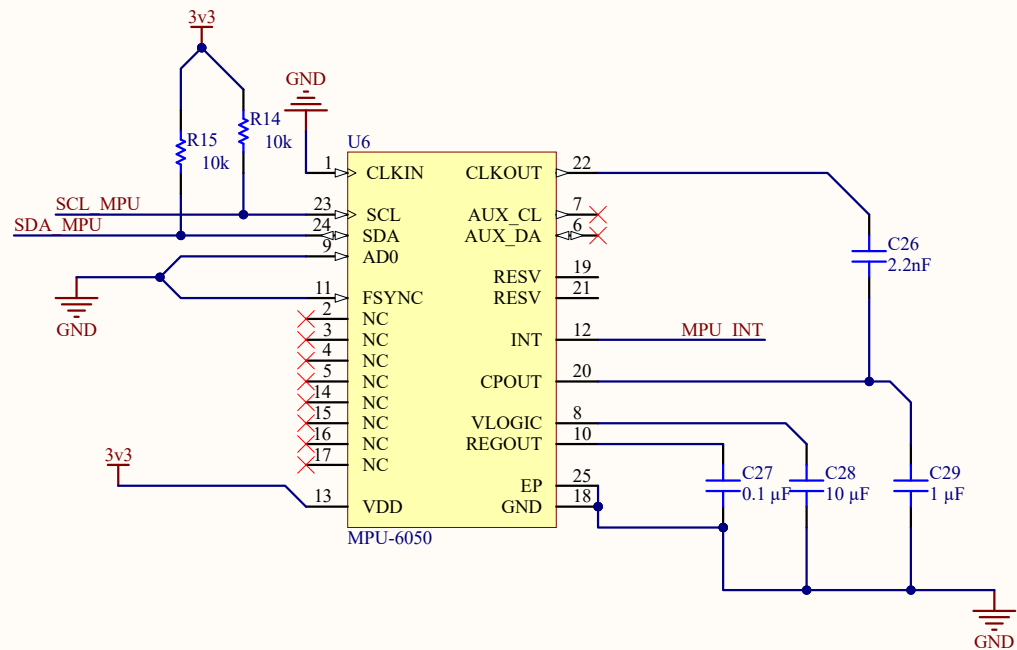
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POWER[0..2]

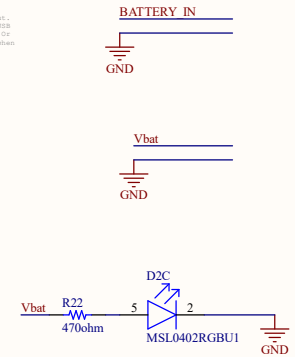
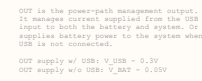
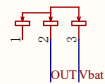
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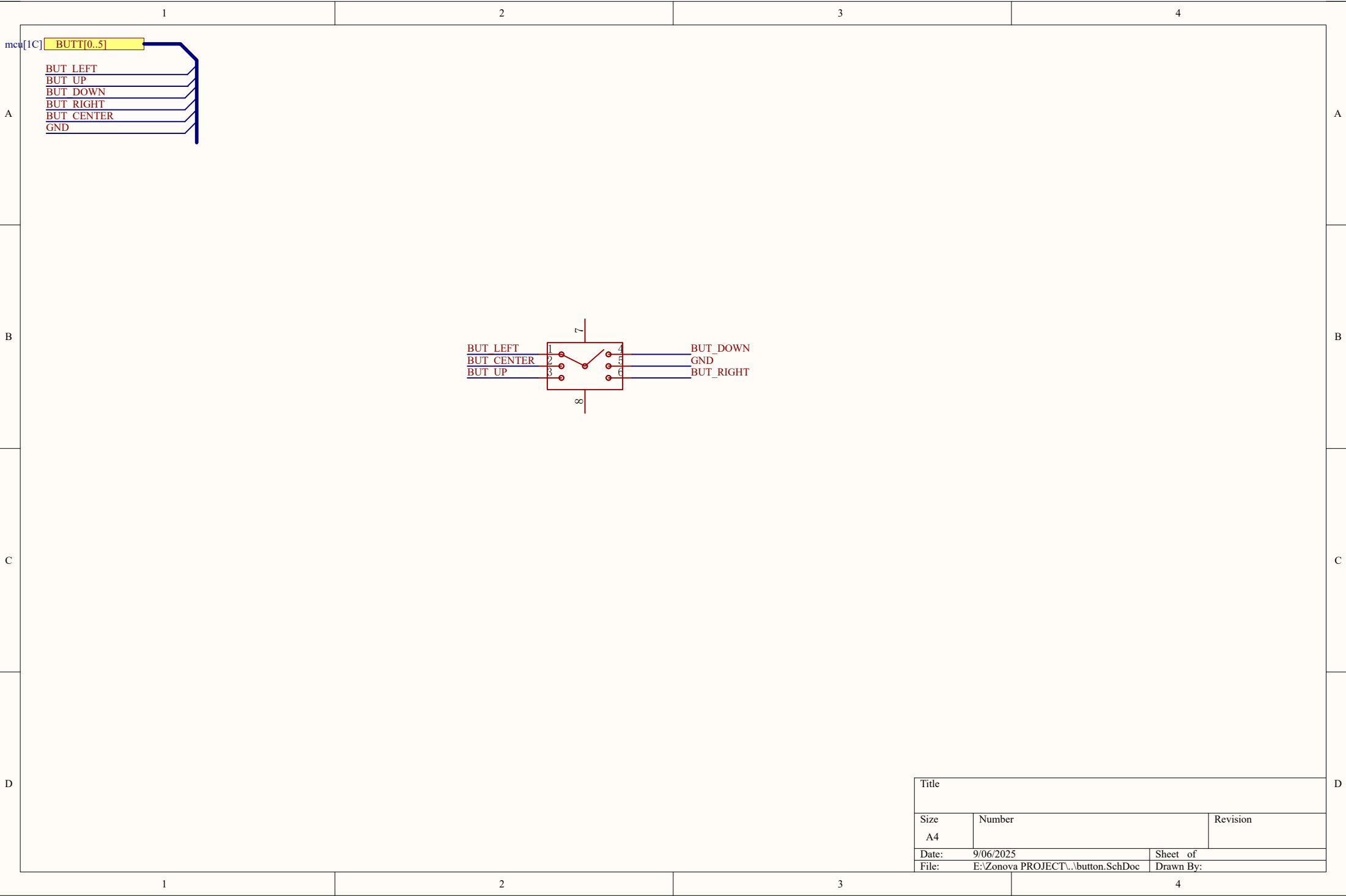
mcu[1C] MPU[0..1]

SCL MPU
SDA MPU
MPU INT



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D



Title		
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