AC6328B Datasheet

Zhuhai Jieli Technology Co.,LTD

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AC6328B Features

High performance 32-bit RISC CPU

- RISC 32-bit CPU
- DC-96MHz operation
- 73KB data RAM
- 8KB I-cache 2way
- 1KB Rocache 1way
- 64 Vectored interrupts
- 8 Levels interrupt priority

Flexible I/O

- 2 GPIO pins
- All GPIO pins can be programmable as input or output individually
- All GPIO pins are internal pull-up/pull-down selectable individually
- CMOS/TTL level schmitt triggered input
- External wake up/interrupt on all GPIOs

Peripheral Feature

- One Full Speed USB OTG controller
- One full-duplex advanced UART(DMA)
- One IIC interface supports host and device mode
- 2 channels 10-bit ADC
- 1 channel 8 levels Low Power Detector
- Embedded PMU support low power mode
- Watchdog

Power-on reset

Bluetooth Feature

- CMOS single-chip fully-integrated radio and baseband
- Compliant with BluetoothV5.4+BR+EDR+BLE specification
- Bluetooth Piconet and Scatternet support
- Meet class2 and class3 transmitting power requirement
- Support GFSK and π/4 DQPSK all packet types
- Maximum +8dBm transmitting power
- EDR receiver with -93dBm sensitivity
- Support a2dp\avctp\avdtp\avrcp\hfp\spp\smp\
 att\gap\gatt\rfcomm\sdp\l2cap profile

Power Supply

- **VBAT** is 1.8V to 5.5V
- VDDIO is 1.8V to 3.4V

Packages

SOP8

Temperature

- Operating temperature: -40°C to +85°C
- Storage temperature: -65°C to +150°C

1.Block Diagram

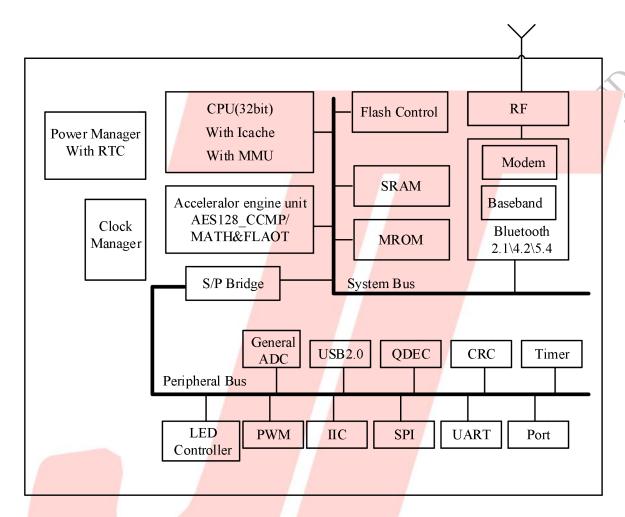


Figure 1-1 AC6328B_SOP8 Block Diagram

2.Pin Definition

2.1 Pin Assignment

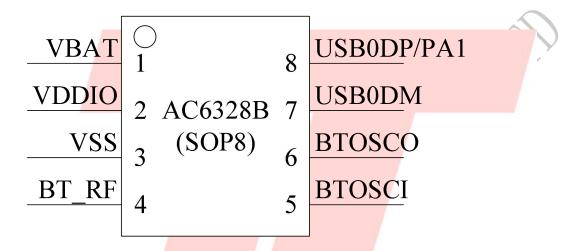


Figure 2-1 AC6328B_SOP8 Package Diagram

2.2 Pin Description

Table 2-1 AC6328B_SOP8 Pin Description

PIN NO.	Name	I/O Type	Function	Other Function
1	VBAT	P	LDO Power	-
2	VDDIO	P	IO Power 3.3V	-
3	VSS	P	GND	-
4	BT_RF	-	RF Antenna	-
5	BTOSCI	I	BTOSCI	-
6	BTOSCO	О	BTOSCO	-
7	USB0DM	I/O	GPIO (pull down)	IIC_SDA_A: IIC SDA(A); ADC11: ADC Channel 11; UART1_RXD: Uart1 Data In(D);
8	USB0DP	I/O	GPIO (pull down)	IIC_SCL_A: IIC SCL(A); ADC10: ADC Channel 10; UART1_TXD: Uart1 Data Out(D);
8	PA1	I/O	GPIO	PWM0: Timer0 PWM Output; ADC0: ADC Channel 0; UART0_TXC: Uart0 Data Out(C);

3. Electrical Characteristics

3.1 Absolute Maximum Ratings

Table 3-1

Symbol	Parameter	Min	Max	Unit
Topt	Operating temperature	-40	+85	°C
Tstg	Storage temperature	-65	+150	°C
VBAT	Supply Voltage	-0.3	5.5	V
V _{VDDIO}	3.3V IO Input Voltage	-0.3	3.6	V

Note: The chip can be damaged by any stress in excess of the absolute maximum ratings listed below

3.2 Recommended Operating Conditions

Table 3-2

Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions
VBAT	Voltage Input	1.8	3.7	5.5	V	-
VDDIO	Voltage Input	-	3.0	7-/	V	-
I _{VDDIO}	Loading current	/ -	-	60	mA	VBAT = 4.2V

3.3 IO Input/Output Electrical Logical Characteristics

Table 3-3

IO input ch	aracteristics					
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions
V_{IL}	Low-Level Input Voltage	-0.3	-	0.3* VDDIO	V	VDDIO = 3.3V
$ m V_{IH}$	High-Level Input Voltage	0.7* VDDIO	-	VDDIO+0.3	V	VDDIO = 3.3V
IO output c	haracteristics					
V_{OL}	Low-Level Output Voltage	-	-	0.33	V	VDDIO = 3.3V
$ m V_{OH}$	High-Level Output Voltage	2.7	-	-	V	VDDIO = 3.3V

3.4 Internal Resistor Characteristics

Table 3-4

Port	Drive Strength	Internal Pull-Up Resistor	Internal Pull-Down Resistor	Comment
PA1	drive_select[11] 24mA drive_select[10] 24mA (with 120ohm res) drive_select[01] 8mA drive_select[00] 8mA (with 120ohm res)	10K	10K	USB0DM&USB0DP default pull down Internal pull-up/pull-down resistance accuracy ±20%
USB0DP	4mA	1.5K	15K	
USB0DM	4mA	180K	15K	

3.5 BT Characteristics

3.5.1 Transmitter

Basic Rate

Table 3-5

Paramete	er /	Min	Тур	Max	Unit	Test Conditions
RF Transmit I	Power	-	4	6	dBm	
RF Power Contro	ol Range	- /	20	- 3	dB	25°C,
20dB Bandw	ridth	- /	950	- /	KHz	Power Supply
In-band spurious	$F=F_0\pm 1MHz$	-//	-20	-/	dBm	VBAT=3.7V
Emissions	$F=F_0\pm 2MHz$	7-7	-45	7	dBm	2441MHz
(BQB Test Mode	$F=F_0\pm 3MHz$	7-/	-35		dBm	DH5
RF_Tx Power=4dBm)	$F=F_0\pm>3MHz$	/ /	-40	-	dBm	Dillo

Enhanced Data Rate

Table 3-6

Paramete	Parameter		Тур	Max	Unit	Test Conditions	
Relative Po	wer	-	-1	-	dB		
π/4 DQPSK	DEVM RMS	-	4	-	%	25°C,	
	DEVM 99%	-	12	-	%	Power Supply	
Modulation Accuracy	DEVM Peak	-	9	-	%	VBAT=3.7V	
In-band spurious	$F=F_0\pm 1MHz$	-	-4	-	dBm		
Emissions	F=F ₀ ±2MHz	-	-30	-	dBm	2441MHz	
(BQB Test Mode	$F=F_0\pm 3MHz$	-	-30	-	dBm	2DH5	
RF_Tx Power=4dBm)	$F=F_0\pm>3MHz$	-	-37	-	dBm		

3.5.2 Receiver

Basic Rate

Table 3-7

Paramete	r	Min	Тур	Max	Unit	Test Conditions
Sensitivit	y	-	-91	-	dBm	
Co-channel Interferen	ice Rejection	-	6	-	dB	25°C,
	+1MHz	-	-7	-	dB	Power Supply
	-1MHz	-	-7	-	dB	
Adjacent Channel	+2MHz	-	-37	-	dB	VBAT=3.7V
selectivity C/I	-2MHz	- //	-39		dB	2441MHz
	+3MHz	1	-32	/ /-	dB	DH5
	-3MHz	-	-43	-	dB	

Enhanced Data Rate

Table 3-8

Paramete	er	Min	Тур	Max	Unit	Test Conditions
Sensitivit	y	-	-93	-	dBm	
Co-channel Interferer	nce Rejection	-	8	-	dB	25°C,
	+1MHz	-	-14	-	dB	Power Supply
	-1MHz	-	-15	- ,	dB	
Adjacent Channel	+2MHz	- /	-36	- /	dB	VBAT=3.7V
selectivity C/I	-2MHz	- /	-39	- 7	dB	2441MHz
	+3MHz	-/ /	-29	- 7	dB	2DH5
	-3MHz	- /	-43	1	dB	

3.5.3 BLE

1M Data Rate

Table 3-9

Parameter		Min	Тур	Max	Unit	Test Conditions
Sensitivit	y	-	-95	-	dBm	
RF Transmit I	Power	-	6.5	8	dBm	
In-band Spurious	M-N =2MHz	-	-35	-	dBm	
Emission	M-N ≥3MHz	-	-33	-	dBm	25°C
	Δf1 avg	-	250	-	KHz	Power Supply
Modulation Characteristics	Δf2 99%	-/	210	7-7-	KHz	VBAT=3.7V
Characteristics	Δflavg/Δf2avg	-	0.9	-	/	2440MHz
Carrier Frequency Offset		-15	- /	+15	KHz	211011112
Frequency Drift		-25	- 9	+25	KHz	
Frequency Dri	ft Rate	-5	-/ /	+5	KHz/50us	

2M Data Rate

Table 3-10

Parameter		Min	Тур	Max	Unit	Test Conditions
Sensitivit	y	-	-92	-	dBm	
RF Transmit F	ower	-	6.5	8	dBm	
	M-N =4MHz	-	-40	- /	dBm	
In-band Spurious Emission	M-N =5MHz	-	-40	- /	dBm	25°C
Elinission	M-N ≥6MHz	-///	-40	-	dBm	Power Supply
	Δfl avg	- /- /-	500	/-	KHz	
Modulation Characteristics	Δf2 99%	/-/	430	-	KHz	VBAT=3.7V
Characteristics	Δflavg/Δf2avg	1 1	0.9	-	/	2440MHz
Carrier Frequency Offset		-20	-	+20	KHz	
Frequency Drift		-25	-	+25	KHz	
Frequency Drift	ft Rate	-5	-	+5	KHz/50us	

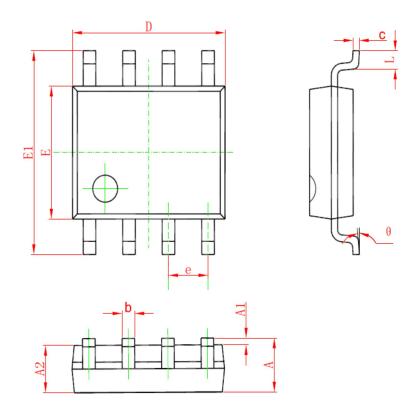
Long Range

Table 3-11

Parameter	Min	Тур	Max	Unit	Test Conditions
Sensitivity LE 125K(S8)	-	-102	-	dBm	VBAT=3.7V,25°C
Sensitivity LE 500K(S2)	-	-99	-	dBm	2440MHz

4. Package Information

4.1 SOP8(4.9mm*3.9mm)



Symbol	Dimension In Millimeters		Dimension In Inches	
	Min	Max	Min	Max
Α	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
С	0.170	0.250	0.007	0.010
D	4.700	5.100	0.185	0.201
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
е	1.27TYP		0.050TYP	
L	0.400	1.270	0.016	0.050
θ	00	8 ⁰	00	8 ⁰

Figure 4-1 AC6328B_SOP8 Package

5.Package Type Specification



- ①Represents different packages
- ②Represents different memory sizes
 - 2: 2Mbit Flash
 - 4: 4Mbit Flash

6.Revision History

Date	Revision	Description
2021.05.19	V1.0	Initial Release
2022.07.19	V1.1	Update Bluetooth Feature
2023.11.28	V1.2	Add BLE parameter
2023.12.13	V1.3	Update Bluetooth Feature
2024.06.27	V1.4	Update Pin Description