AC6369C Datasheet

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AC6369C Features

CPU

- 32-bit DSP supports hardware Float Point Unit (FPU)
- Up to 160MHz programmable processor
- 64Vectored interrupts
- 4 Levels interrupt priority

Bluetooth

- Compliant with BluetoothV5.4+BR+EDR+BLE specification
- Meet class1 class2 and class3 transmitting power requirement
- Support GFSK and π/4 DQPSK all packet types
- Provides +6dbm transmitting power
- receiver with -90dBm sensitivity
- Fast AGC for enhanced dynamic range
- Supports a2dp\avctp\avdtp\avrcp\hfp\spp\smp\att\gap\ gatt\rfcomm\sdp\l2cap profile

Peripherals

- One full speed USB 2.0 OTG controller
- Six multi-function 32-bit timers, support capture and PWM mode
- Three full-duplex basic UART, UART0 and UART1 supports DMA mode

- Two SPI interface supports host and device mode
- One hardware IIC interface supports host and device mode
- 10-bit ADC for analog sampling
- External wake up/interrupt on all GPIOs

PMU

- Low voltage LDO for internal digital and analog circuit supply
- 3uA current consumption in the soft-off mode
- Built-in LDO for the core, I/O, Bluetooth and flash
- **VBAT** is 2.2V to 3.4V
- VDDIO is 2.2V to 3.4V

Temperature

- Operating temperature: -40°C to +125°C
- Storage temperature: -65°C to +150°C

Packages

SOP16

Applications

Bluetooth IOT

1. Pin Definition

1.1 Pin Assignment

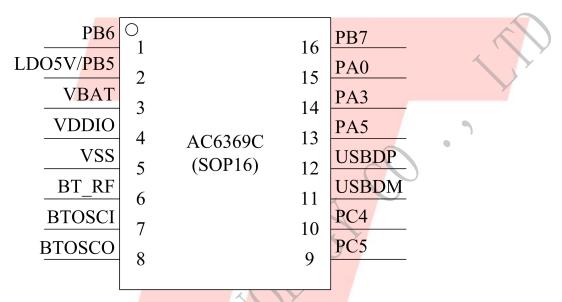


Figure 1-1 AC6369C Package Diagram

1.2 Pin Description

Table 1-1 AC6369C Pin Description

PIN		I/O	Drive		
NO.	Name	Туре	(mA)	Function	Other Function
1	PB6	I/O	24/8	GPIO	IIC_SCL_C: IIC SCL(C); SPI2_CLKA: SPI2 Clock(A); ADC8: ADC Input Channel 8; TMR3: Timer3 Clock Input; UART1TXA: Uart1 Data Out(A);
2	PB5	I/O	8	GPIO (High Voltage Resistance)	PWM3: Timer3 PWM Output; SPI2_DIA: SPI2 Data In(A); CAP1: Timer1 Capture; UARTOTXC: Uart0 Data Out(C); UARTORXC: Uart0 Data In(C);
	LDOIN	P	/		Battery Charger In;
3	VBAT	P	/		Battery Power Supply;
4	VDDIO	P	/		IO Power 3.3v;
5	VSS	P	1		Ground;
6	BT_RF	/	1		BT Antenna;
7	BTOSCI	I	/	Y	BT OSC In;
8	BTOSCO	0	/	7 /	BT OSC Out;
9	PC5	I/O	24/8	GPIO	IIC_SDA_B: IIC SDA(B); ADC12: ADC Input Channel 12; TMR1: Timer1 Clock Input; UART2RXD: Uart2 Data In(D);
	٨ -)			IIC_SCL_B: IIC SCL(B);
10	PC4	I/O	24/8	GPIO	ADC11: ADC Input Channel 11; PWM1: Timer1 PWM Output; UART2TXD: Uart2 Data Out (D);
11	USBDM	I/O	4	USB Negative Data (pull down)	IIC_SDA_A: IIC SDA(A); SPI2_DOB: SPI2 Data Out(B); ADC14: ADC Input Channel 14; UART1RXD: Uart1 Data In(D);
12	USBDP	I/O	4	USB Positive Data (pull down)	IIC_SCL_A: IIC SCL(A); SPI2_CLKB: SPI2 Clock(B); ADC13: ADC Input Channel 13; UART1TXD: Uart1 Data Output(D);

13	PA5	I/O	24/8	GPIO	IIC_SCL_D: IIC SCL(D); PWM0: Timer0 PWM Output; UART0TXA: Uart0 Data Output(A);				
14	PA3	I/O	24/8	ADC2: ADC Input Channel 2; PWM5: Timer5 PWM Output; UART2TXA: Uart2 Data Output(A);					
15	PA0	I/O	24/8	GPIO	ADC0: ADC Input Channel 0; UART1TXC: Uart1 Data Output(C);				
16	PB7	I/O	24/8	GPIO	IIC_SDA_C: IIC DAT(C); SPI2_DOA: SPI2 Data Out(A); ADC9: ADC Input Channel 9; PWM5: Timer5 PWM Output; UART1RXA: Uart1 Data In(A);				

2, Electrical Characteristics

2.1 Absolute Maximum Ratings

Table 2-1

Symbol	Parameter	Min	Max	Unit
Tamb	Ambient Temperature	-40	+125	°C
Tstg	Storage temperature	-65	+150	°C
LDOIN	Charger Voltage	-0.3	6	V
VBAT	Supply Voltage	-0.3	4.5	V
V _{3.3IO}	3.3V IO Input Voltage	-0.3	3.6	V

Note: The chip can be damaged by any stress in excess of the absolute maximum ratings listed below

2.2 PMU Characteristics

Table 2-2

Symbol	Parameter	Min	Тур	Max	Unit	<i>></i>	Test Conditions
LDOIN	Charger Voltage	4.5	5	5.5	V		
VBAT	Voltage Input	2.2	3.7	4.2	V		
V_{VDDIO}	Voltage Input	2.2	3.0	3.4	V	y	
$I_{ m vddio}$	Loading current	- (150	mA		VBAT = 4.2V

2.3 Battery Charge

Table 2-3

Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions		
LDO_IN	Charge Input Voltage	4.5	5	5.5	V			
$ m V_{Charge}$	Charge Voltage	4.15	4.2	4.25	V	-		
ICharge	Charge Current	20		300	mA	Charge current at fast charge mode		
I_{Trikl}	Trickle Charge Current	20	45	70	mA	$V_{BAT} \!\! < \!\! V_{Trikl}$		

2.4 IO Input/Output Electrical Logical Characteristics

Table 2-4

IO input ch	IO input characteristics									
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions				
$V_{\rm IL}$	Low-Level Input Voltage	-0.3	-	0.3* VDDIO	V	VDDIO = 3.3V				
V_{IH}	High-Level Input Voltage	0.7* VDDIO	-	VDDIO+0.3	V	VDDIO = 3.3V				
IO output o	characteristics									
V _{OL}	Low-Level Output Voltage	-	_	0.33	V	VDDIO = 3.3V				
V_{OH}	High-Level Output Voltage	2.7	-	74	V	VDDIO = 3.3V				

2.5 Internal Resistor Characteristics

Table 2-5

Port		General Output	High Drive	Internal Pull-Up Resistor	Internal Pull-Down Resistor	Comment
PA5 PB4,PB6,PB7 PC4~PC5		8mA	24mA	10K	10K	
PA0	Output 0 Output 1	8mA	24mA 64mA	10K	10K	1. USBDM & USBDP default pull down 2. PB5 can pull-up resistance to 5V 3. internal pull-up/pull-down resistance accuracy ±20%
]	PB5	8mA		10K	10K	
USBDP		4mA		1.5K	15K	
US	SBDM	4mA	_	180K	15K	

2.6 BT Characteristics

2.6.1 Transmitter

Basic Rate

Table 2-6

Paramete	r	Min	Тур	Max	Unit	Test Conditions
RF Transmit F	ower	1	4	6	dBm	
RF Power Contro	ol Range	-	20	-	dB	25°C,
20dB Bandwidth		-/	950	17-	KHz	Power Supply
In-band spurious	$F=F_0\pm 1MHz$	4	-20	/-	dBm	VBAT=3.7V
Emissions	$F=F_0\pm 2MHz$	/-	-45	-	dBm	2441MHz
(BQB Test Mode	F=F ₀ ±3MHz	-	-35	-	dBm	DH5
RF_Tx Power=4dBm)	$F=F_0\pm>3MHz$	-	-45	^	dBm	

Enhanced Data Rate

Table 2-7

Paramete	er	Min	Тур	Max	Unit	Test Conditions
Relative Po	wer	-	-1	-	dB	
π/4 DQPSK	DEVM RMS	-61	4	-	%	25°C,
·	DEVM 99%	1	10	- 7	%	Power Supply
Modulation Accuracy	DEVM Peak		7	- /	%	11.2
In-band spurious	F=F ₀ ±1MHz	7-/	-4	-	dBm	VBAT=3.7V
Emissions	F=F ₀ ±2MHz	7- /	-30	7-	dBm	2441MHz
(BQB Test Mode	F=F ₀ ±3MHz	7 -	-30	-	dBm	2DH5
RF_Tx Power=4dBm)	F=F ₀ ±>3MHz	7 /-	-37	-	dBm	

2.6.2 Receiver

Basic Rate

Table 2-8

		1				I
Paramete	Parameter			Max	Unit	Test Conditions
Sensitivit	y	-	-88	-	dBm	
Co-channel Interferer	ice Rejection	-	6	-	dB	25°C,
	+1MHz	-	-6	-	dB	Power Supply
	-1MHz	-	-8	-	dB	VBAT=3.7V
Adjacent Channel	+2MHz	-	-17	-	dB	Y
selectivity C/I	-2MHz	-	-21	/-/-	dB	2441MHz
	+3MHz	4	-15	/-	dB	DH5
	-3MHz	/-	-31	-	dB	

Enhanced Data Rate

Table 2-9

Paramete	er	Min	Тур	Max	Unit	Test Conditions	
Sensitivit	Sensitivity				dBm		
Co-channel Interferen	ace Rejection	-	9		dB	25°C,	
	+1MHz	-	-10) _	dB	Power Supply	
	-1MHz	-(1)	-13	-	dB		
Adjacent Channel	+2MHz		-11	- 7	dB	VBAT=3.7V	
selectivity C/I	-2MHz		-21	- /	dB	2441MHz	
	+3MHz	7	-13	-	dB	2DH5	
	-3MHz	/- /	-40	-	dB		

2.6.3 BLE

1M Data Rate

Table 2-10

Paramete	Parameter		Тур	Max	Unit	Test Conditions
Sensitivit	y	-	-91	-	dBm	
RF Transmit F	Power	-	6	-	dBm	
In-band Spurious	M-N =2MHz	-	-41	-	dBm	
Emission	M-N ≥3MHz	-	-40	-	dBm	25°C
	Δf1 avg	-	250	-	KHz	Power Supply
Modulation Characteristics	Δf2 99%	-/	210	/-/-	KHz	VBAT=3.7V
Characteristics	Δflavg/Δf2avg	A	0.9	A -	/	2440MHz
Carrier Frequency Offset		-50	-	+50	KHz	
Frequency Drift		-25	-///	+25	KHz	
Frequency Dri	ft Rate	-5	7/	+5	KHz/50us	

2M Data Rate

Table 2-11

Parameter		Min	Тур	Max	Unit	Test Conditions
Sensitivity		- ,	-89	- /	dBm	
RF Transmit Power		-6	6	-	dBm	
In-band Spurious Emission	M-N =4 <mark>MHz</mark>		-45	-	dBm	
	M-N =5MHz		-45	-/	dBm	25°C Power Supply
	M-N ≥6MHz	-///	-45	- 1/ ₂ -	dBm	
Modulation Characteristics	Δfl avg	-	500	-	KHz	
	Δf2 99%	/ -/	430	-	KHz	VBAT=3.7V
	Δflavg/Δf2avg	/ ₋	0.9	-	/	2440MHz
Carrier Frequency Offset		-50	-	+50	KHz	
Frequency Drift		-25	-	+25	KHz	
Frequency Drift Rate		-5	-	+5	KHz/50us	

Long Range

Table 2-12

Parameter	Min	Тур	Max	Unit	Test Conditions
Sensitivity LE 125K(S8)	-	-99	-	dBm	VBAT=3.7V,25°C
Sensitivity LE 500K(S2)	-	-95	-	dBm	2440MHz

3. Package Information

3.1 SOP16

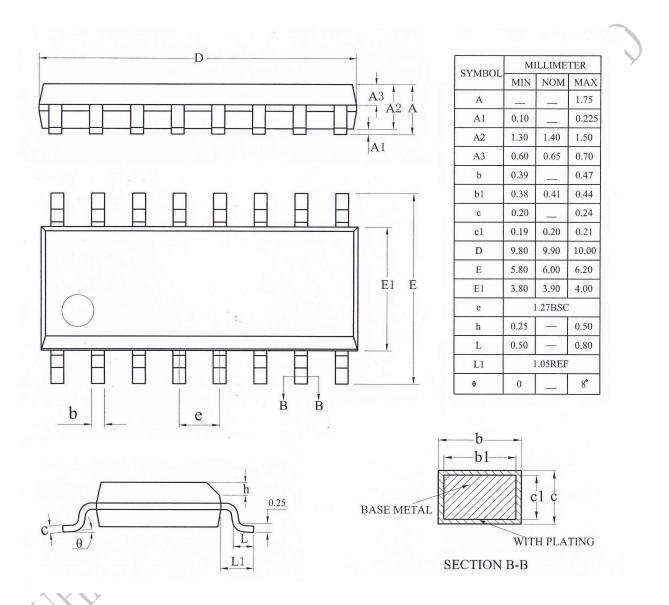


Figure 3-1 AC6369C Package

4. Revision History

Date	Revision	Description
2020.09.14	V1.0	Initial Release
2022.07.19	V1.1	Update Bluetooth Feature
2024.03.06	V1.2	Update Bluetooth Feature, Add BLE Parameter

