0.5. X Basics of Memory Management
CPU + Memory Ron-cont. Raging Segmentation
(need Some Space.)
(what we require?) Size (1) high Access Time (1)
Par Unit Cost ()
Seach space जितना बड़ा होगा, Element की search करते में (access करने में) time जाना ही ज्यादा लगेगा।
i.e why we are not having dingle memory.
CPU e Cache Memory Memory Memory Memory Memory
Main Memory Size Sec. Nemory Size Ilooo. Rogram executes Sequently

acceu Time= loms acceu loo ms How the process comes from 100 Bec. Money 14 Hid KLOWBING 100 aut of 90 Time we'll get data (10) + · 1 (10+ 100) MM if the use only Sec. mem. DA webbo losipole the wit y will be looms 1 Address assorts on Sec. Marron but now we have main memory too so the Lity. will be 20ms * Coche memory mapping is done under Computer Sychitecture

Allandin

proces store in Contiquous fashion Space allocation. 1 Contiguous accen Time Jons (2) Non Contiguous How the process comes from Sec. Memory to (00 and of 30 him one !! get data Addreu Translation (virtual Memory CPU generate dogical address. Logical Address works on Sec. Memory Logical address Physical address Translation of Logical addrew to Physical address.

