**TABLE OF CONTENTS**

|  |  |  |
| --- | --- | --- |
| **S.NO.** | **Objective** | **Date** |
|  | **Implementation of AND Gate.** | **27/4/17** |
|  | **Implementation of OR Gate.** | **27/4/17** |
|  | **Implementation of NAND Gate.** | **28/4/17** |
|  | **Implementation of NOR GATE.** | **28/4/17** |
|  | **Implementation of NOT Gate.** | **28/4/17** |
|  | **Implementation of XOR Gate.** | **28/4/17** |
|  | **Implementation of -XNOR Gate.** | **29/4/17** |
|  | **Implementation of JK-flip-flop and verify their characteristics table.** | **29/4/17** |
|  | **Implementation of SR-flip-flop and verify their characteristics table.** | **4/5/17** |
|  | **Implementation of D-flip-flop and verify their characteristics table.** | **4/5/17** |
|  | **Implementation of T flip-flop and verify their characteristics table.** | **5/5/17** |
|  | **Implementation of 8x1 Multiplexer.** | **5/5/17** |