

CAPSTONE PROJECT

“Optimizing
Compiler
Performance with
Advanced Memory
Management
Strategies”



AIM:

1. Implementing advanced memory allocation algorithms to optimize resource utilization within the compiler.
2. Introducing efficient data structures and algorithms to minimize memory fragmentation and overhead.
3. Integrating sophisticated caching mechanisms to enhance data access and retrieval, reducing latency.
4. Employing memory pooling techniques to mitigate the overhead of frequent memory allocations and deallocations.
5. Leveraging dynamic memory management strategies to adapt to varying workload demands and optimize compiler performance in real-time.

LITERATURE SURVEY



1. Reviewing existing literature on compiler design, performance optimization, and memory management strategies to establish a comprehensive understanding of the current state-of-the-art.
2. Analyzing various advanced memory management techniques such as memory allocation algorithms, data structures, caching mechanisms, and memory pooling strategies in the context of compiler optimization.
3. Identifying challenges and limitations associated with integrating advanced memory management strategies into compiler design, such as complexity and trade-offs between performance and resource consumption.
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5. Providing insights into future research directions and potential areas for further exploration in optimizing compiler performance through advanced memory management strategies.

PROPOSED ALGORITHM

Static Analysis: Perform static analysis on the source code to identify memory usage patterns, including variable lifetimes, allocation sites, and potential memory leak.

Memory Allocation Strategies: Implement sophisticated memory allocation strategies such as stack allocation, pool allocation, or region-based allocation to minimize dynamic memory overhead and fragmentation.

Memory Reuse: Implement techniques for memory reuse to minimize the number of allocations and deallocations, reducing overhead and improving cache locality.



Study objectives



Minimize Memory Footprint

Minimize Memory Footprint
Efficient Allocation
Data Structure Optimization



Enhance Data Structure Efficiency

Algorithm Optimization
Fragmentation Reduction



Enable scalability

Hardware Agnostic
Resource Utilization

CONCLUSION

1. Advanced memory management strategies play a crucial role in optimizing compiler performance.
2. Efficient memory allocation and utilization reduce overall memory footprint, enhancing system efficiency.
3. Cache-aware algorithms and data structures maximize cache utilization, minimizing cache misses and improving performance.
4. Dynamic memory overhead reduction techniques, such as stack allocation and memory pooling, enhance runtime efficiency.
5. Optimization of data structures and memory access patterns improves overall program execution speed and efficiency.

