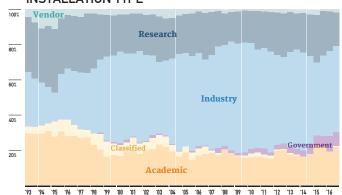
ARCHITECTURES SIMD 80% **Constellations** Clusters MPP **SMP** 20%

CHIP TECHNOLOGY 100% Alpha 80% Intel **MIPS** SPARC 20% **Proprietary**

INSTALLATION TYPE



ACCELERATORS/CO-PROCESSORS

