HIRENDRA KUMAR GARAI

RESEARCH FELLOW, SPMS, NTU, SINGAPORE

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Education

BITS Pilani, Hyderabad Campus • Telangana, India PhD • Mathematics • Advisor: Dr. Sabyasachi Dey	2020 - 2024
MAKATTI • West Bengal, India Bachelor of Education • Mathematics	2018 - 2020
Visva Bharati University • West Bengal, India Masters of Science • Mathematics	2016 - 2018
Bolpur College • West Bengal, India Bachelor of Science [HONS.] • Mathematics	2013 - 2016

Publications

Journal Articles

- [1] Cryptanalysis of Reduced Round ChaCha New Attack & Deeper Analysis IACR Transactions on Symmetric Cryptology 2023.
- [2] Enhanced Differential-Linear Attacks on Reduced Round ChaCha IEEE Trans. Inf. Theory 2023.
- [3] A multi-step key recovery attack on reduced round Salsa and ChaCha Cryptologia 2024.
- [4] Breaching Forró's Security With Differential-Linear Foray IEEE Access 2024.

Conference Proceedings

- [5] Revamped Differential-Linear Cryptanalysis on Reduced Round ChaCha EUROCRYPT 2022.
- [6] Grover on Chosen IV Related Key Attack Against GRAIN-128a INDOCRYPT 2024.

Paper presentation

• Presented "Cryptanalysis of Reduced Round ChaCha – New Attack & Deeper Analysis" at FSE 2023, Beijing, China (online).

Scholarships

- Awarded *Junior Research Fellowship* by CSIR in 2018 which enabled my doctoral research and assistant professor eligibility in India.
- Upgraded to Senior Research Fellowship in 2022.

Teaching and research experiences

• Graduate Teaching Assistant • BITS Pilani, Hyderabad Campus

Jan' 2021 – May' 2024

- Visited TCG-CREST in July'2023 for a collaborative work with Dr. Arpita Maitra.
- Postdoctoral Researcher IIT Madras, India

Oct' 24 – Dec' 24

• Research Fellow • NTU, Singapore

Jan' 25 - Ongoing

Technical skills

Computer languages •

C/C++, Python, HTML, CSS

Others •

Departmental Service • AGANIT Newsletter Editorial Team, Mathematics Department, BITS Hyderabad