1. Generating 'radioberry.rbf' file in Intel Quartus Prime Lite.

I was able to build it by installing 'Quartus Prime Lite' and using the make command only!
Also did user interface compilation instead of command line compilation

My PC specs
Core i5-7300u @2.6GHz, RAM 16GB
Windows11, 23H2
Erapsed time: 04m46s

Laptop-PC specs I used also.
Core i5-7Y57 @1.2GHz, RAM 8GB
Windows11, 21H2
Erapsed time: abt. 8min

1-1. Download 'Quartus Prime Lite Edition' (no licence file required).

https://www.intel.com/content/www/us/en/software-kit/825278/
intel-quartus-prime-lite-edition-design-software-version-23-1-1-for-windows.html

1-2. Double-click 'qinst-lite-windows-xxx.exe' to install

Select the 'Cyclone 10LP' device library during installation

1-3. Set the path to the "Environment Variables" in Windows.

C:¥intelFPGA_lite¥23.1std¥questa_fse¥win64
C:¥intelFPGA_lite¥23.1std¥quartus¥bin64   ← Need to add new path.
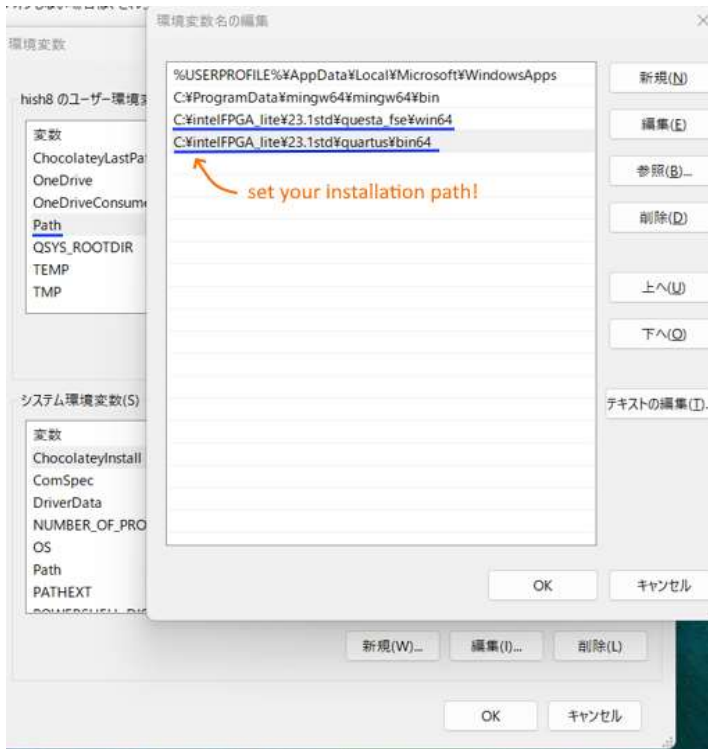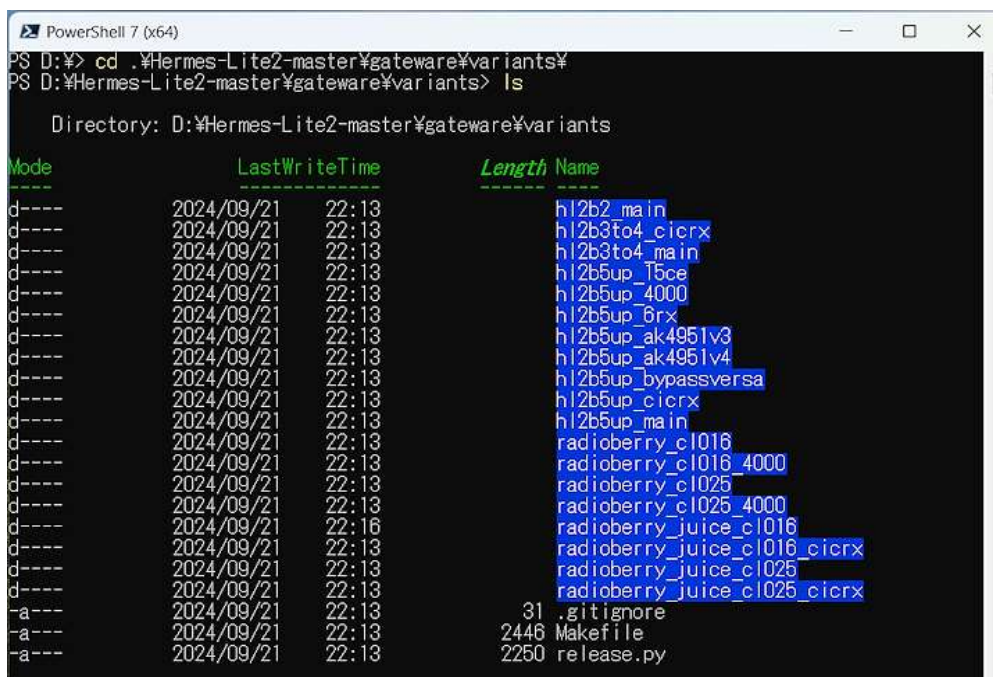

2. Download verilog codes

https://github.com/softerhardware/Hermes-Lite2
Get the code 'Hermes-Lite2-master.zip' and extract it to your directory

> cd Hermes-Lite2-master/gateware/variants
> ls

3. Compile the Verilog Code 'radioberry_juice_cl016'

Execute Windows powershell
> cd Hermes-Lite2-master/gateware/variants/radioberry_juice_cl016
> mkdir build
> make

> cd built   'radioberry.rbf' file here

> ls

> cat radioberry.fit.summary

4. Compile Verilog Code with GUI

```
> cd Hermes-Lite2-master/gateware/variants/radioberry_juice_cl016
> ls
```
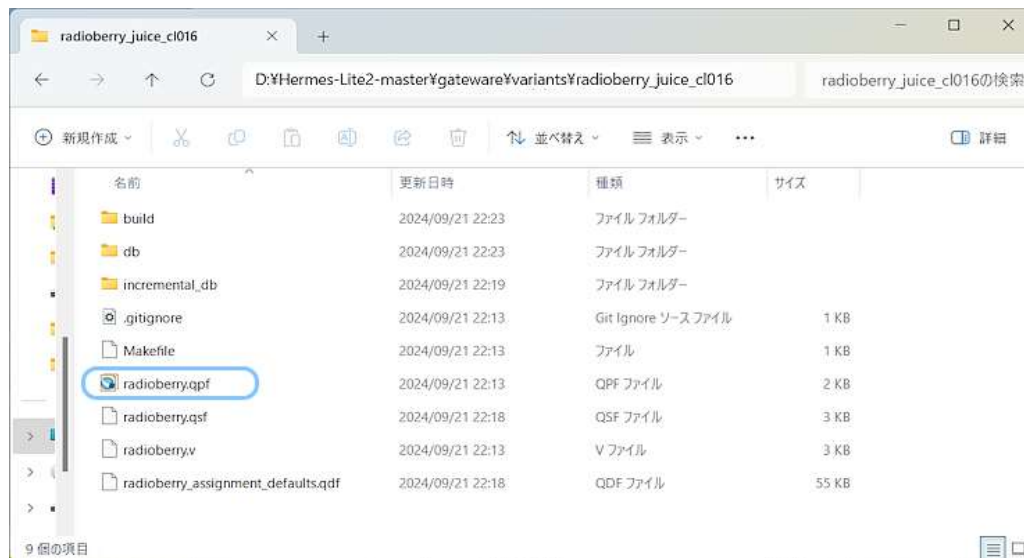

Double-click on 'radioberry.qpf' to start the Quartus Prime Lite edition.
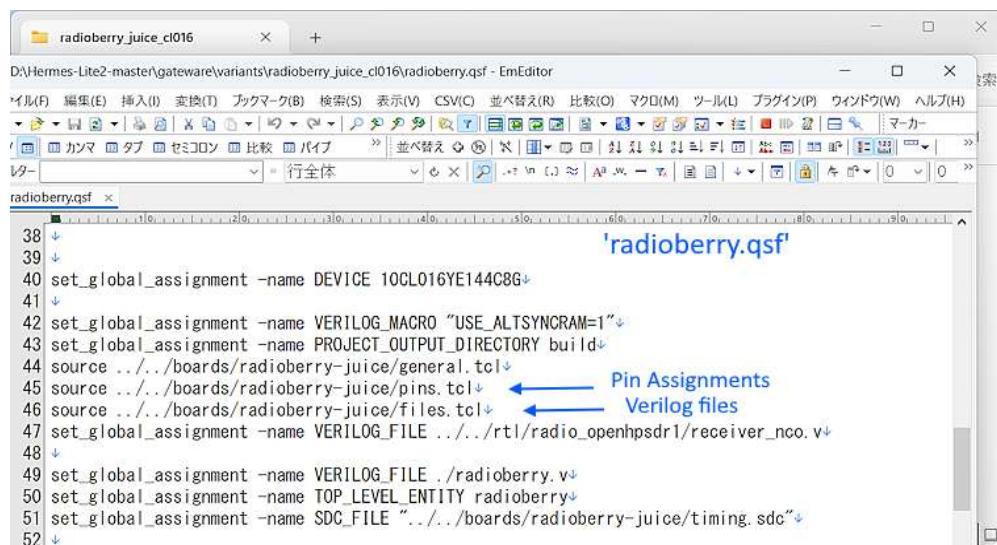
Double-click on 'Compile Design'
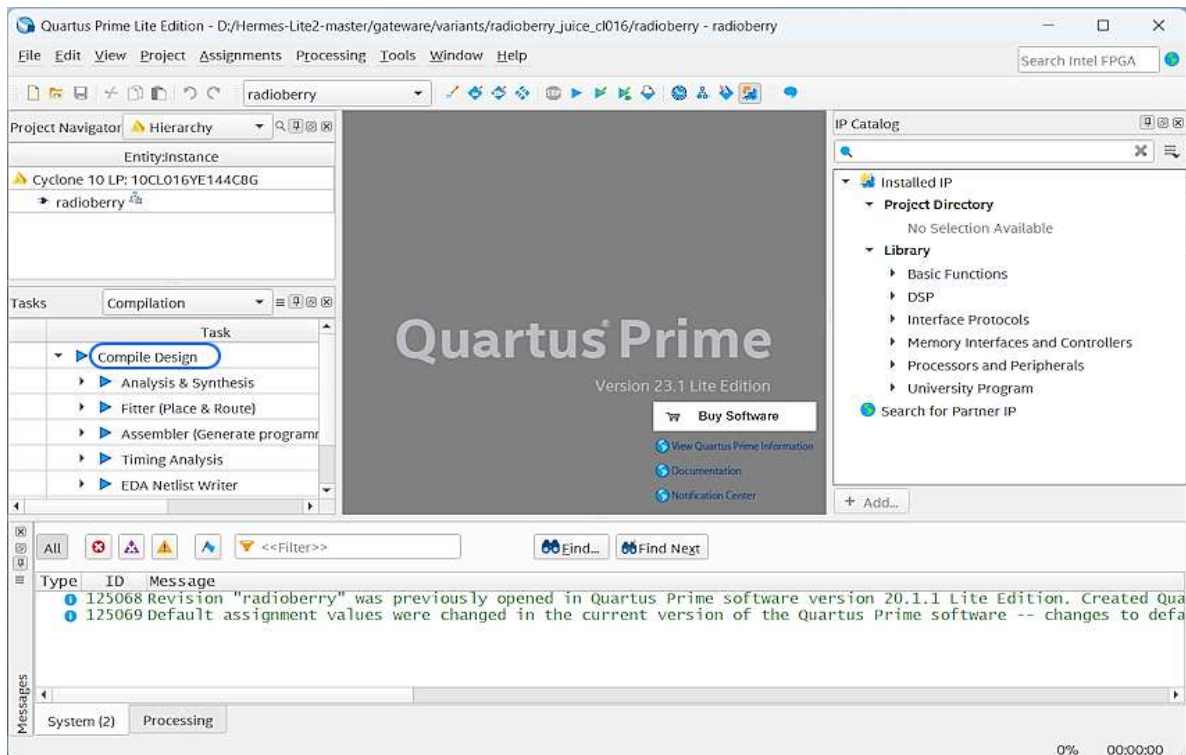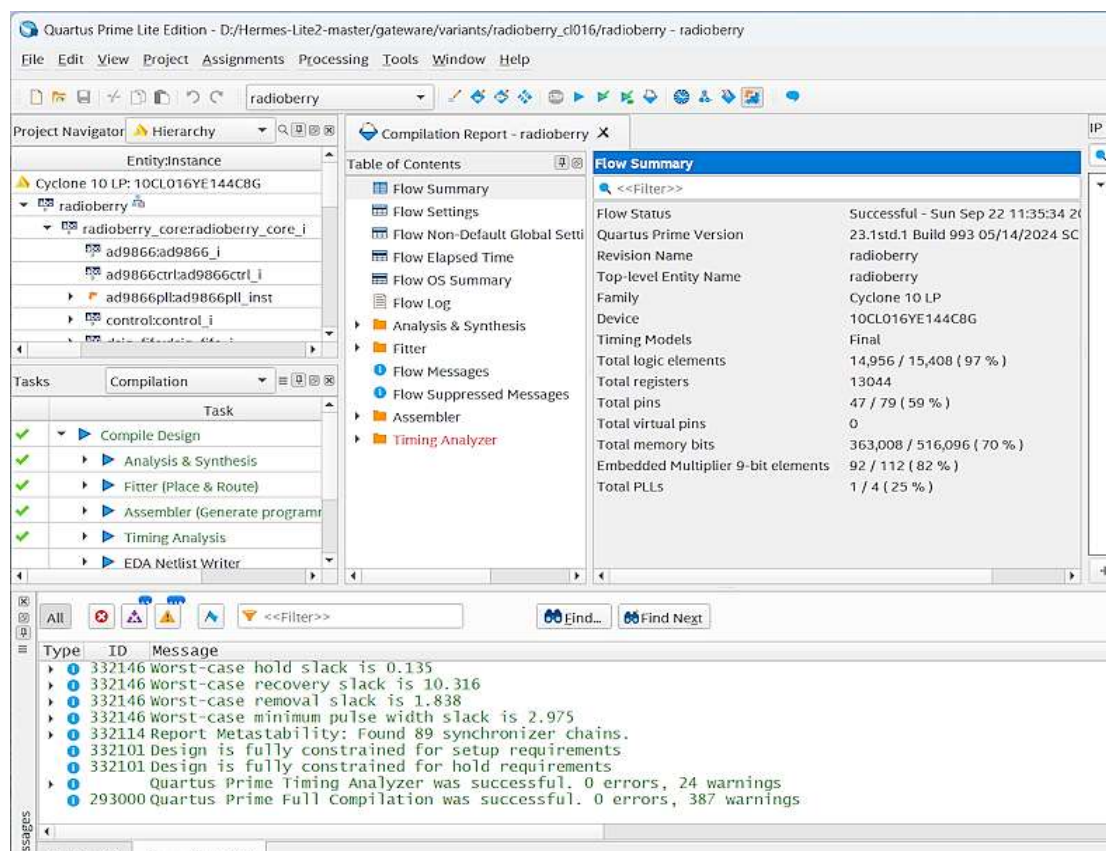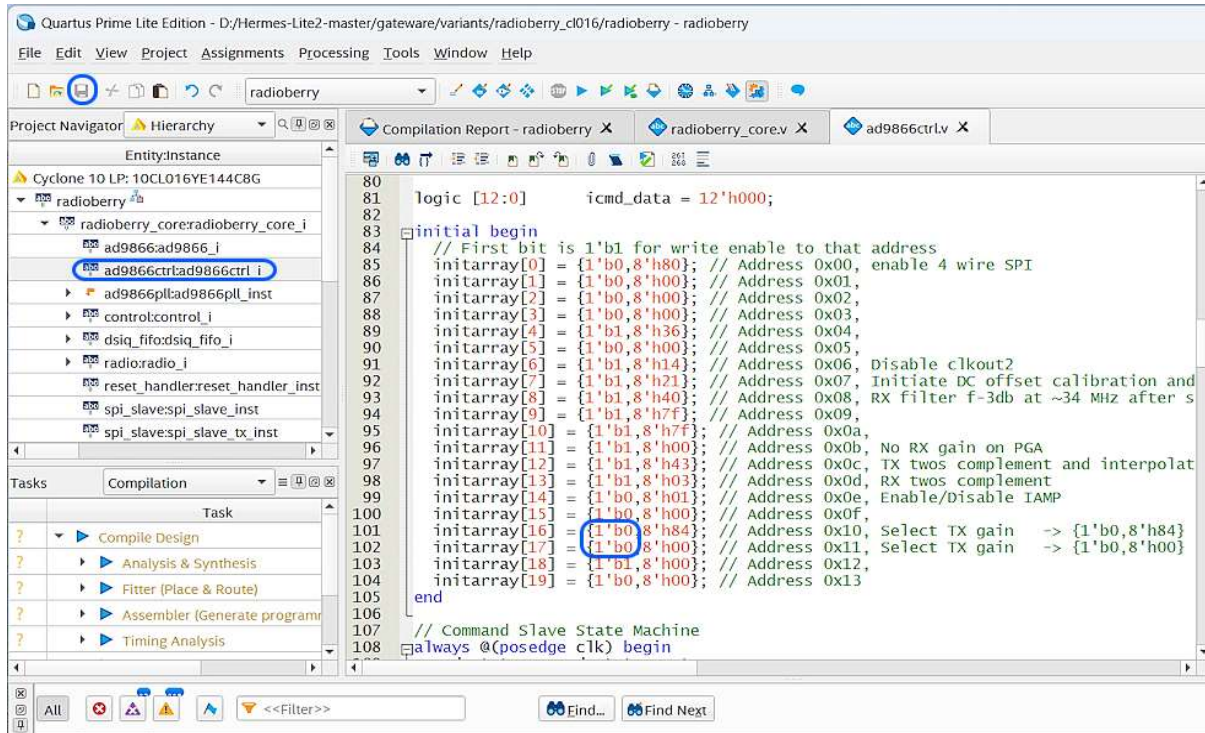Check that there are no errors.

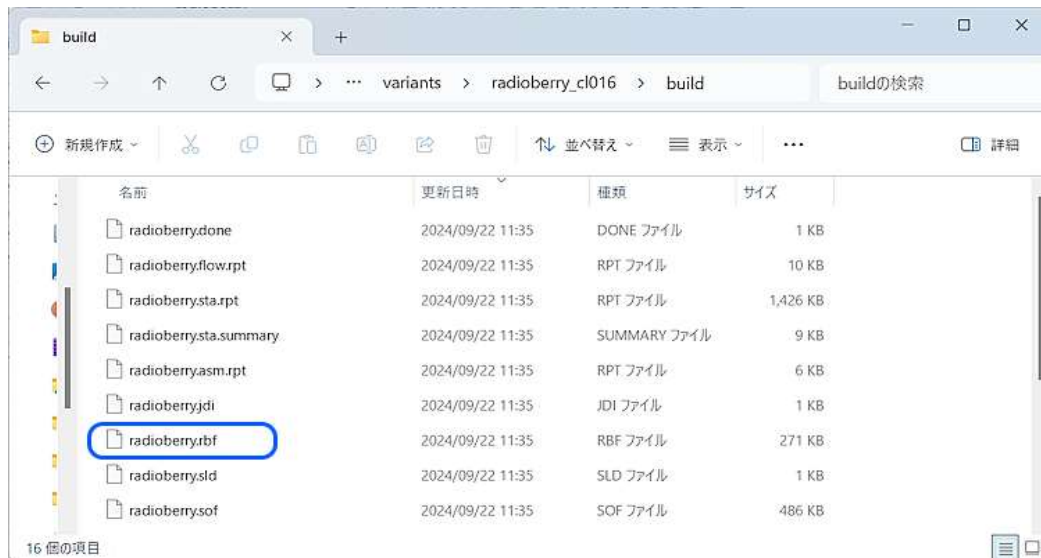The 'radioberry.rbf' file can be in the build directory.

Click on 'ad9866ctrl:' and Modify line 101,102. and save.

Double-click on 'Compile Design' again.
Check that there are no errors.

The 'radioberry.rbf' file can be in the build directory. :)

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2024.09.22 Yado-san, jg1twp