



## YunSDR GNU-Radio入门指南

北京威视锐科技有限公司

V3 Technology (Beijing) Ltd

#### 威视锐旗下品牌:











### GNU-Radio基础实验

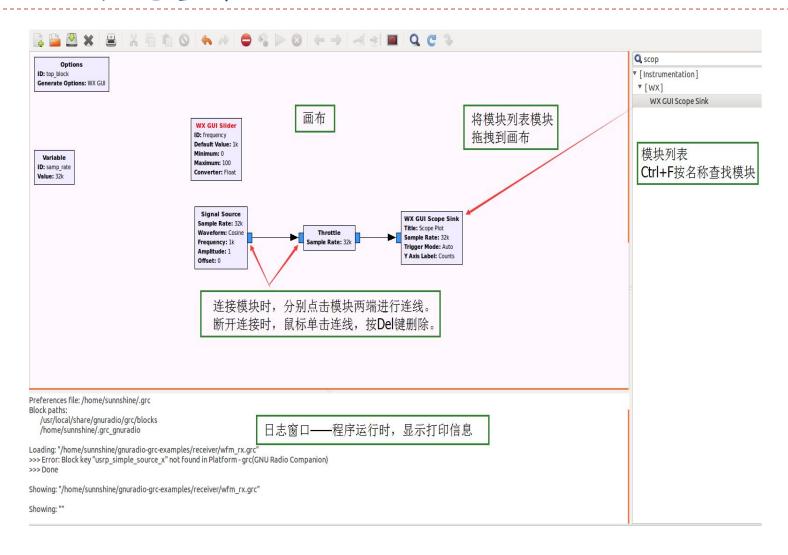
▶ 打开GNU Radio Companion;

▶ ——打开终端/控制台/命令提示符

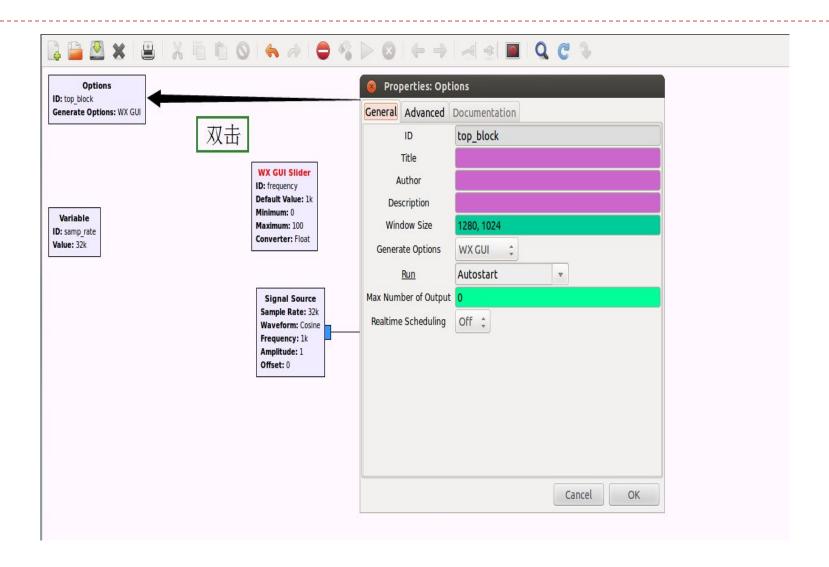
▶ ——运行'gnuradio-companion'



### Lab 1: 熟悉基本模块



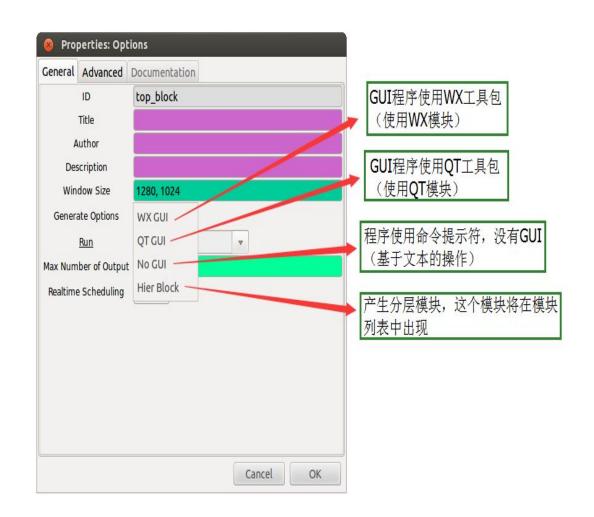




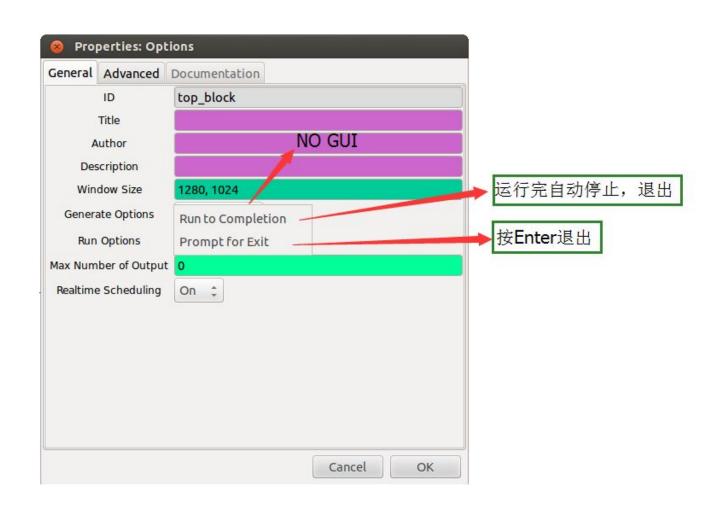


**Properties: Options** General Advanced Documentation top\_block 产生python文件的名称 ID 主GUI窗口的名称 Title Author Description Window Size 1280, 1024 GRC画布的尺寸 产生代码的类型 Generate Options WX GUI 开始和停止流图的方式 Autostart Run 采样率的最大输出 Max Number of Output 0 Realtime Scheduling Off ‡ Cancel OK

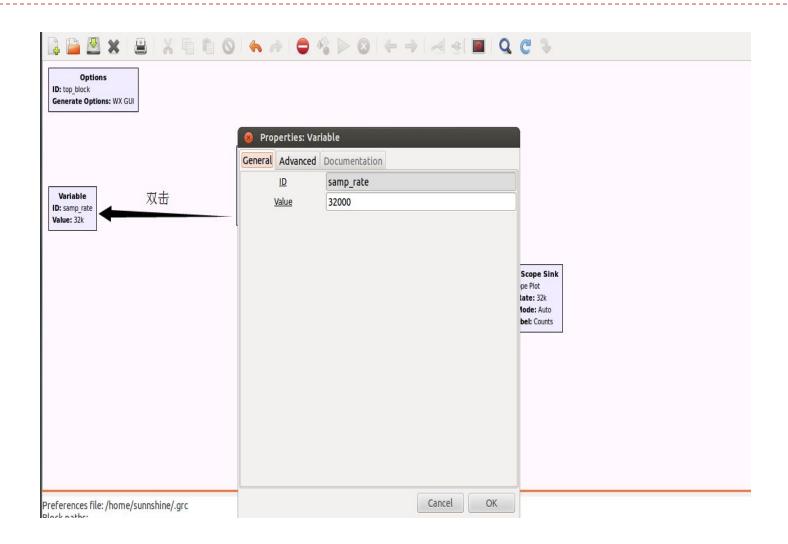








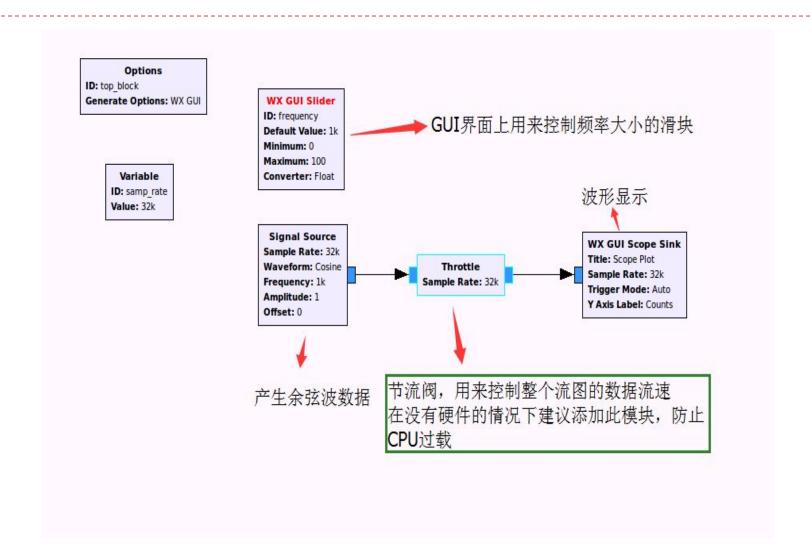




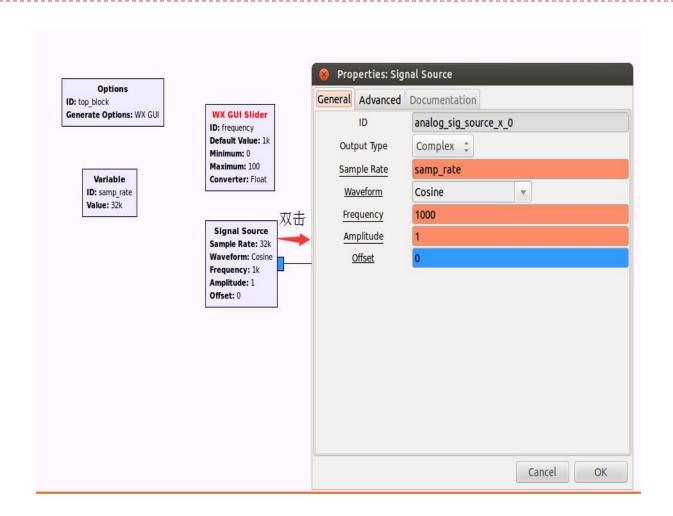


Properties: Variable General Advanced Documentation ID samp\_rate 32000 Value ID:(python)变量名 Value: 任意Python表达式 例如: 32000 (Default):整数 32e6 (32000.0): 浮点数 int (32e6): 浮点数转整数 Cancel OK









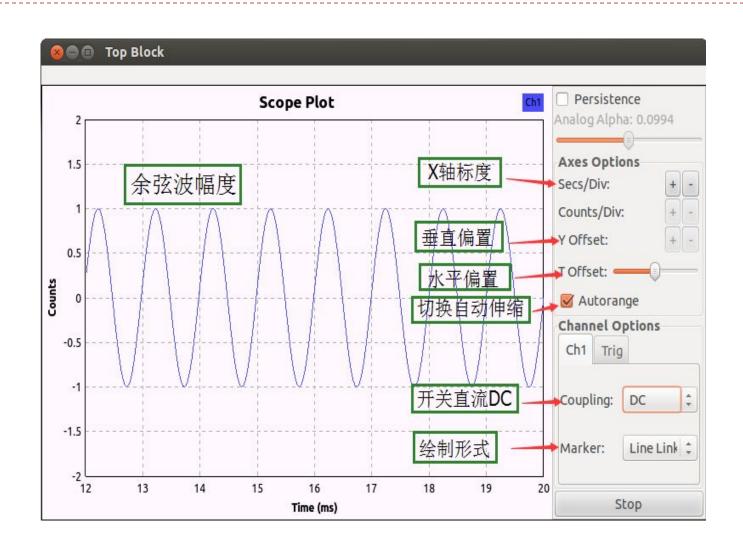


任何处理模块中的'Sample Rate'都是为DSP 计算使用,而不是控制产生采样的速率。这与 硬件控制采样速率有明显的区别

任何带下划线的参数都是可以以任意**Python**表达式 来表述的,也可以通过变量传递参数。使用较为灵活

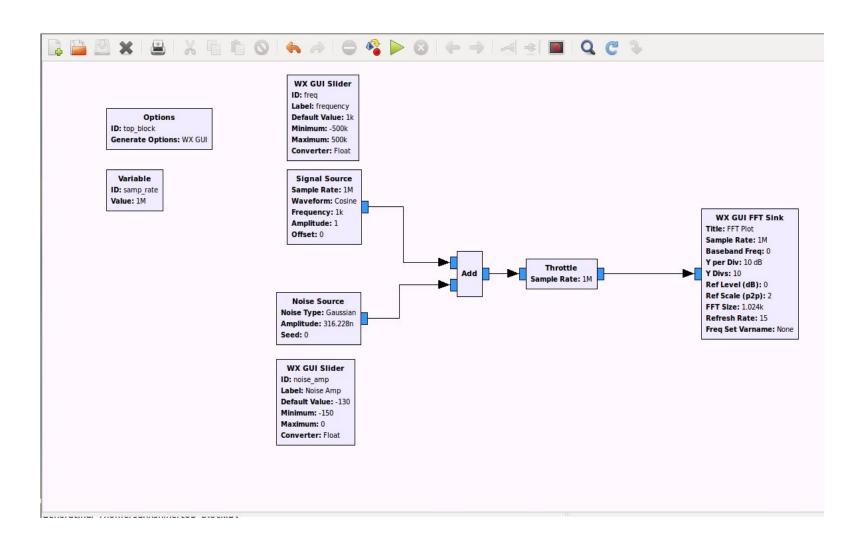




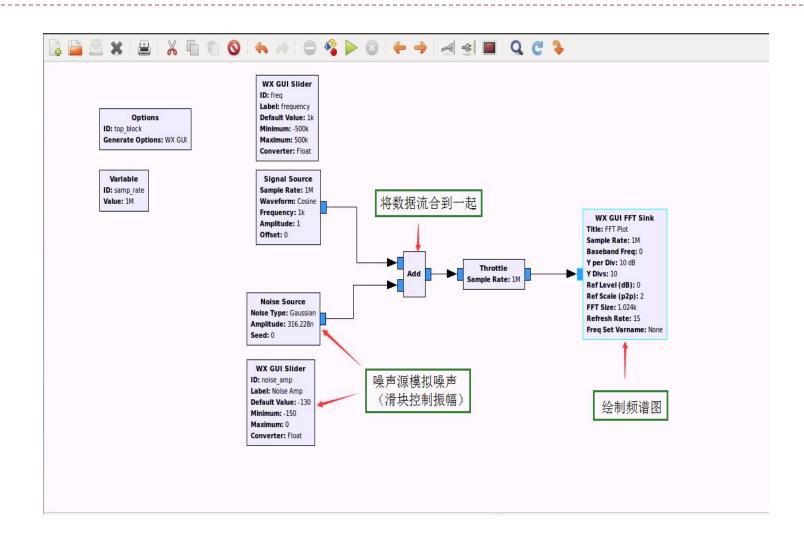




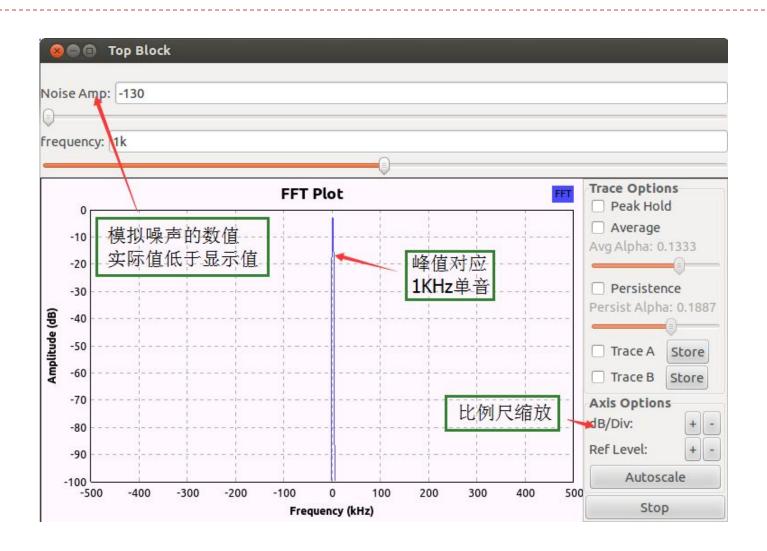
# Lab 2: Sink模块使用



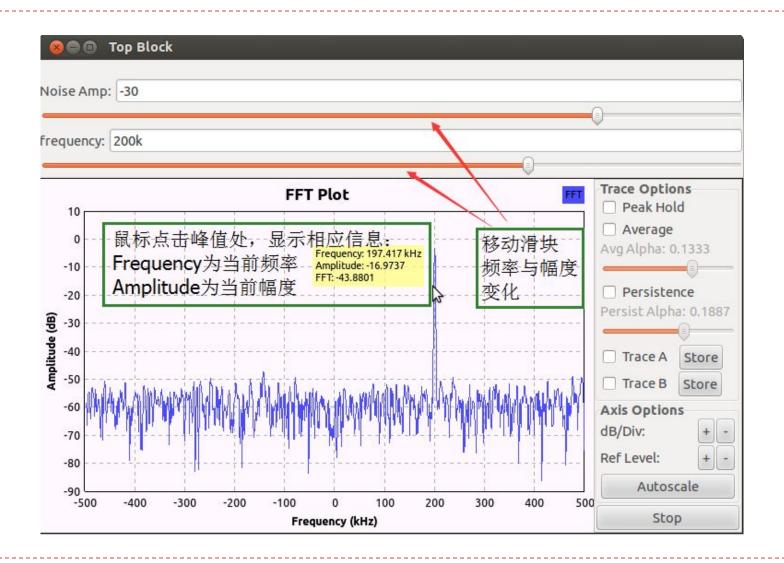




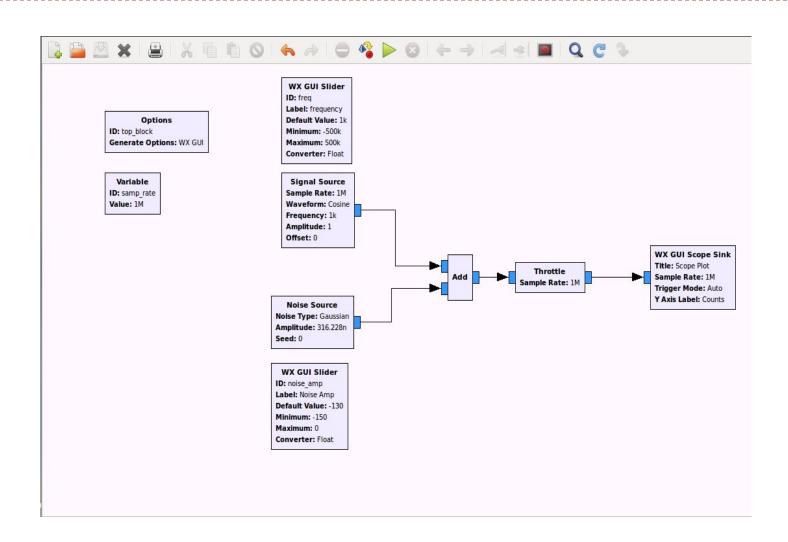




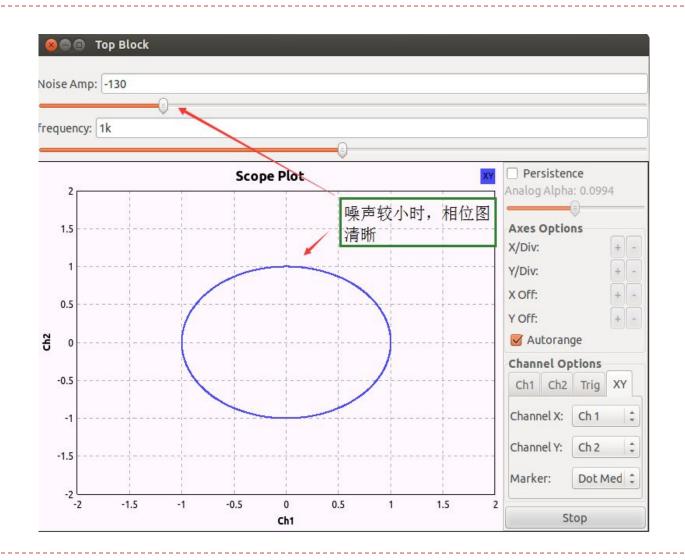




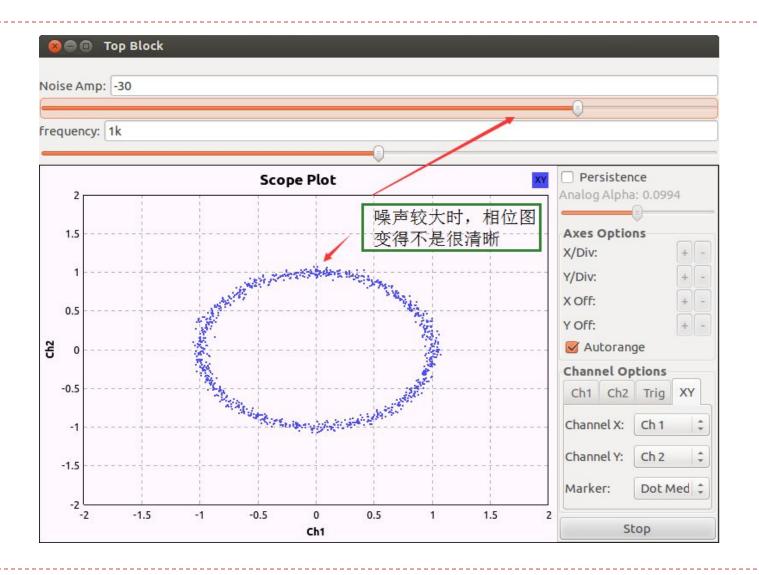






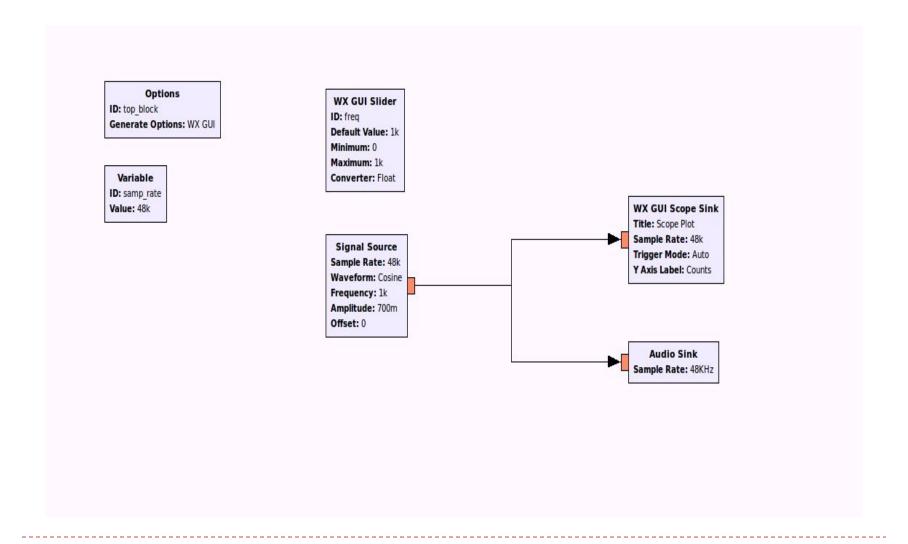




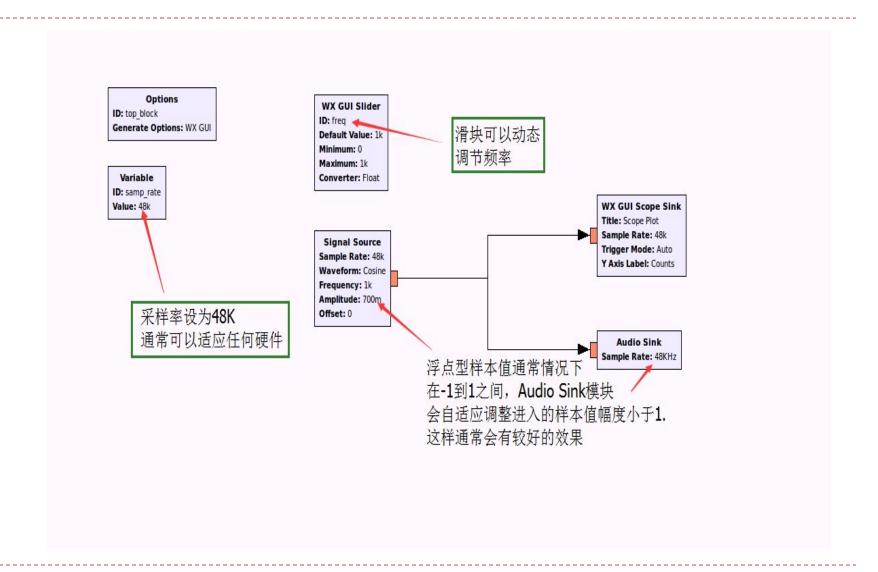




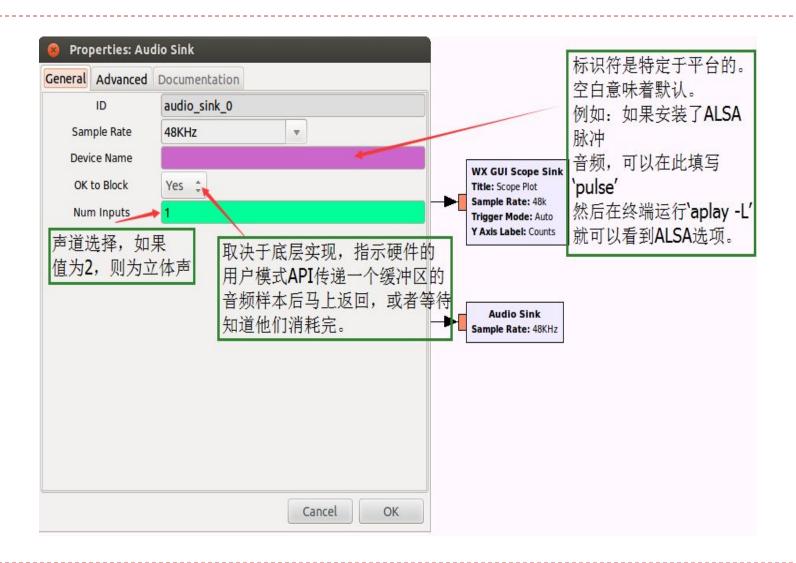
# Lab 3: 音频模块使用





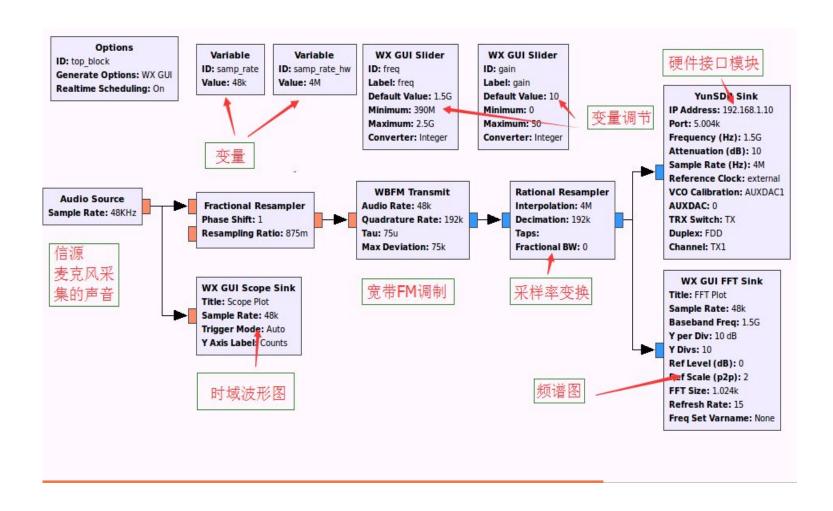




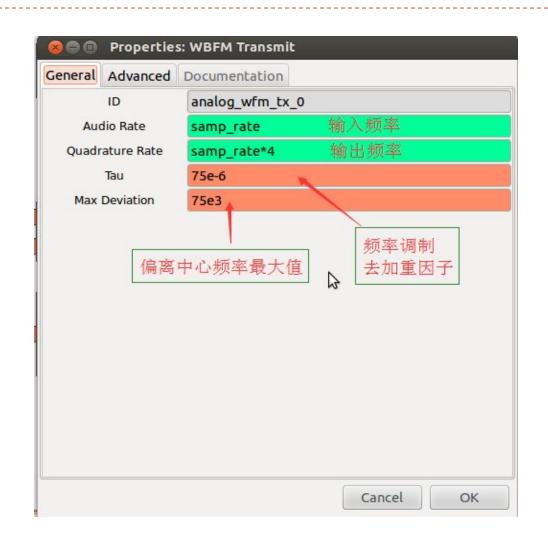




### Lab 4: FM音频发射









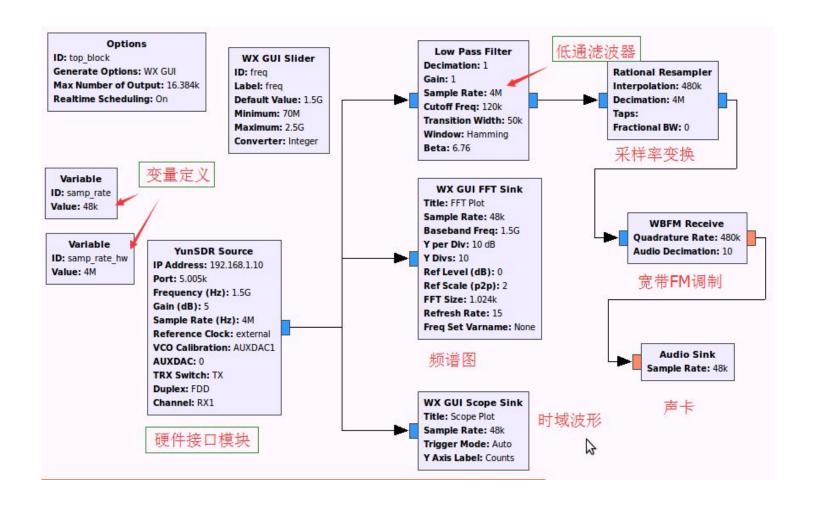
Properties: Rational Resampler General Advanced Documentation 数据类型 rational\_resampler\_xxx\_0 ID Complex->Complex (Complex Taps) ‡ Type Interpolation 插值参数 samp\_rate\_hw Decimation 192000 抽取参数 Taps 0 影响滤波器形状,默认值0为自动选 Fractional BW 择 采样率变换的目的是调整输入采样率与输出采样率的 倍数关系;输入输出往往不是整数倍关系,这就需要 采样率变换模块来调节 Cancel OK



Properties: YunSDR Sink General Advanced Documentation ID yunsdr\_yunsdr\_sink\_0 板卡IP地址 IP Address 192.168.1.10 使用的板卡端口号 5004 Port 频点配置 Frequency (Hz) freq 增益配置 Attenuation (dB) gain samp\_rate\_hw 硬件采样率 Sample Rate (Hz) 时钟选择 Reference Clock external  $\nabla$ VCO校准 VCO Calibration AUXDAC1 o 若选用内部时钟做VCO校准,需配置 AUXDAC TRX Switch TX TX/RX选择 Duplex FDD 双工方式选择 Channel 诵道选择 TX1 W 此模块由威视锐提供 OK Cancel



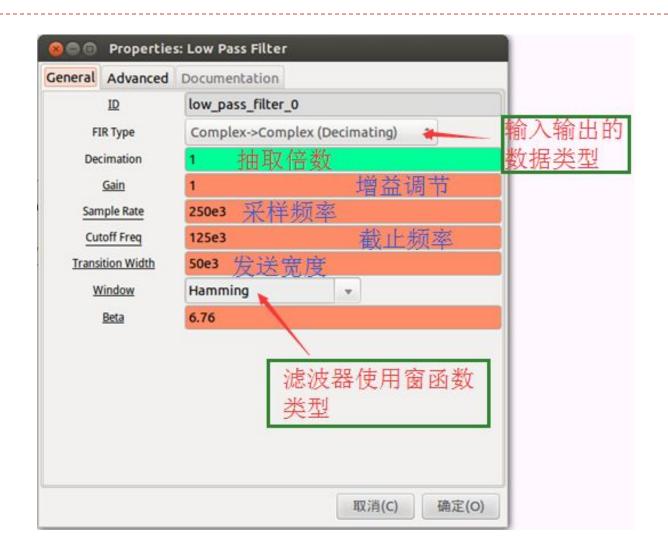
## Lab 5: FM音频接收





General	Advanced	Documentation	72	
ID		yunsdr_yunsdr_source_0		
IP Address		192.168.1.10		
Port		5005		
Frequency (Hz)		freq		
Gain (dB)		5		
Sample Rate (Hz)		samp_rate_hw		
Reference Clock		external	▼ )	
VCO Calibration		AUXDAC1	▼	
AUXDAC		0		
TRX Switch		TX	▼	
Duplex		FDD	▼	
Channel		RX1	▼	
		與与发端参数一 i通道有所区别	致	







Properties: Rational Resampler General Advanced Documentation rational\_resampler\_xxx\_0 ID Complex->Complex (complex Taps) ‡ Туре 插值参数 Interpolation samp\_rate\*10 抽取参数 Decimation samp\_rate\_hw Taps Fractional BW Cancel OK



Properties: WBFM Receive General Advanced Documentation analog\_wfm\_rcv\_0 ID Quadrature Rate samp\_rate\*10 Audio Decimation 10 对输入采样率做 10倍抽取;结果 为48KHz以适应 声卡频率 Cancel OK







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