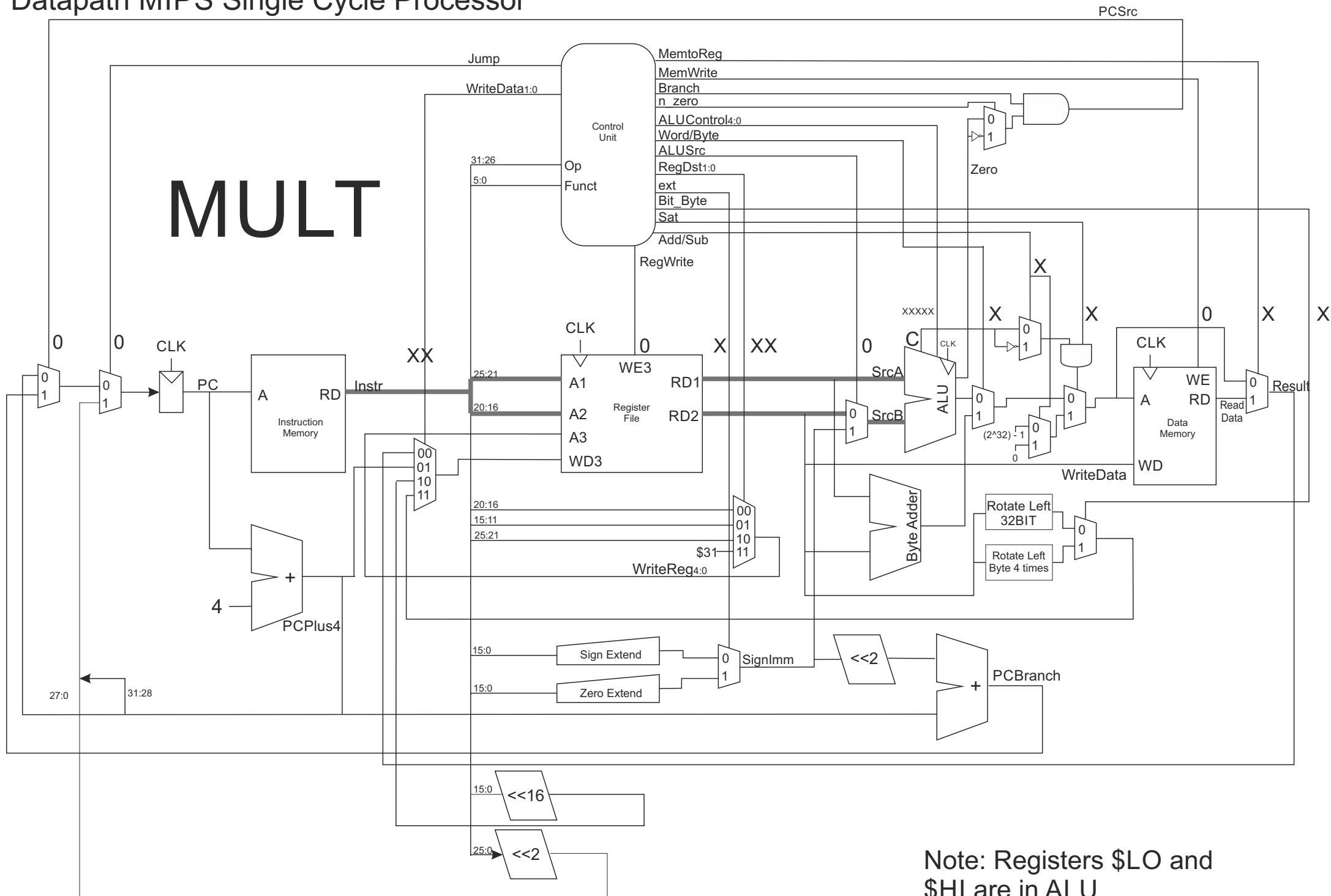


Datapath MIPS Single Cycle Processor



Note: Registers \$LO and \$HI are in ALU