

Incident description

The Github.com website experienced a data breach. Over 265 thousand email addresses were exposed, together with credential, personal, location and employment data. Leaked records include username, name, location, company name, Parent email address and bio. The validity of the data exposed couldn't be verified. Yet we're still informing you about a potential data breach - but keep in mind there's a chance of it being a false positive.

Incident date

No. of exposed accounts

265,160

Exposed data

- Location
- Name
- Username
- Email
- Company name
- Bio
- Alternate email

Powered by

Safety warning: This website has been breached or has been part of a data leak.



[Skip to content](#)

Navigation Menu

TomMajor / SmartHome

- [TomMajor](#) /
- [SmartHome](#)

Q

Q Type / to search

Search code, repositories, users, issues, pull requests...

+ ▾

🔍

📧

📁

- <> [Code](#)
- 🔍 [Issues 1](#)
- 📄 [Pull requests](#)
- ⚙️ [Actions](#)
- 📦 [Projects](#)
- 🛡️ [Security](#)
- 📊 [Insights](#)

[Open in github.dev](#) [Open in a new github.dev tab](#) [Open in codespace](#)

← Files

📁

🔍

master

▾

Breadcrumbs

1. [SmartHome](#)
2. [/Info](#)

Bootloader

/

Directory actions

Q

Go to file

t

Add file

Add file ▾

More options



Directory actions

More options



Latest commit

TomMajor

[Update bootloader 328P fuse image](#)

Jun 11, 2021

[163bb48](#) · Jun 11, 2021

History

History



← Files

master

▾

Breadcrumbs

1. [SmartHome](#)
2. [/Info](#)

/

Bootloader

/

↑ Top

Folders and files

Name		Name	Last commit message	Last commit date
parent directory				
	..			
	Images		Update bootloader documentation	Jan 25, 2021
	mega1284_RC-Osc_or_Quarz		Info/Bootloader, update documentation	Feb 23, 2020
	mega328_RC-Osc_or_Quarz		Update bootloader 328P fuse image	Jun 11, 2021
	mega328_RC-Osc_with_Calibration		Bootloader, update documentation	May 11, 2019
	README.md		Update bootloader documentation	Jan 25, 2021
<div>View all files</div>				



Bootloader/Fuses/Flashen

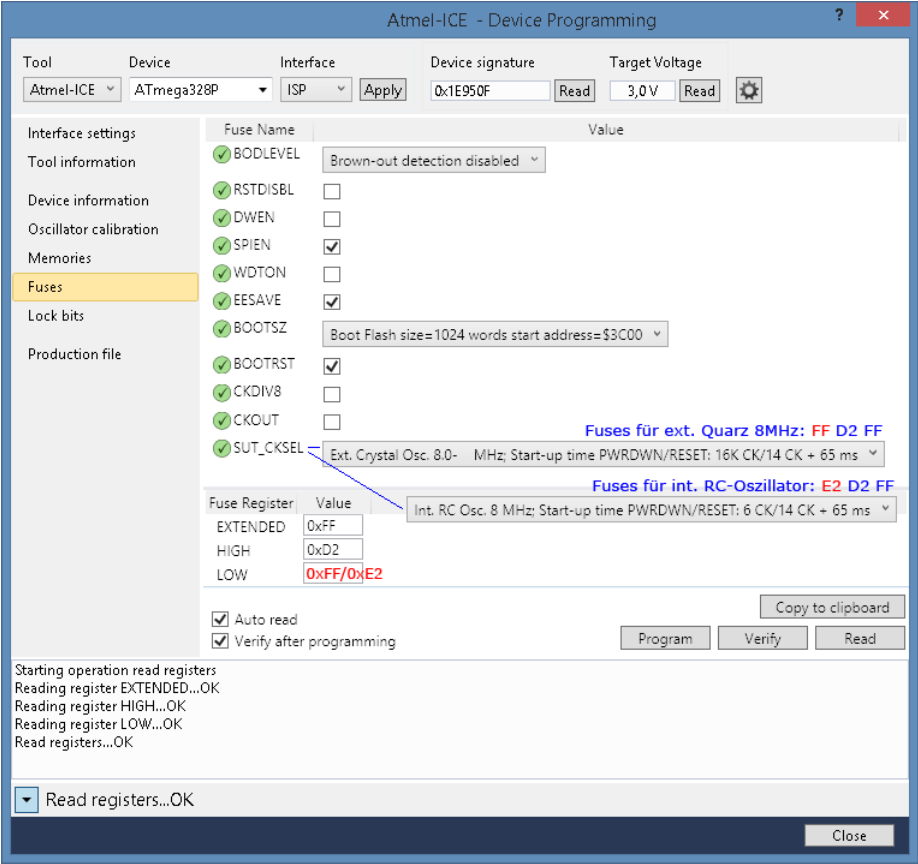


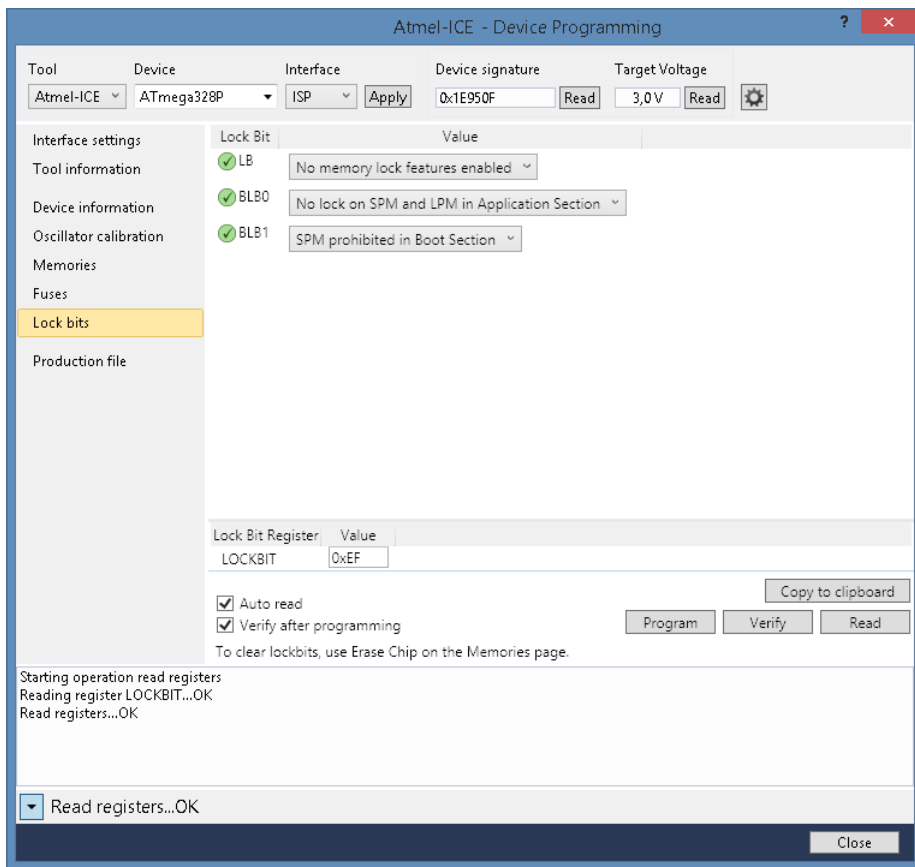
Standard Bootloader ATmega328P RC-Oszillator oder Quarz 8MHz



- für RC-Oszillator oder Quarz 8MHz verwendbar, der Unterschied zwischen RC-Oszillator und Quarz steckt nur in der Fuse SUT_CKSEL, siehe Bild unten
- Standard ATmega328P Bootloader (ATmegaBOOT), 8MHz:
ATmegaBOOT_168_atmega328_pro_8MHz.hex
- mit früher Watchdog-Abschaltung, damit kann man in eigenen Sketchen den WD-Reset nutzen:
ATmegaBOOT_168_atmega328_pro_8MHz_wdt.hex
- in einem ProMini 8MHz aus China enthaltener Bootloader:
ProMiniChina8MHz_OriginalBootloader.hex

-> [ATmega328P Bootloader \(RC-Oszillator oder Quarz 8MHz\)](#)





Bootloader ATmega328P RC-Oszillator mit Kalibrierung



- Kalibrierung der RC-Oszillatorfrequenz für erhöhte Genauigkeitsanforderungen ohne Verwendung eines Quarzes
- Bootloader für RC-Oszillator mit Auslesen der Frequenzkalibrierung aus dem EEPROM
- der Wert für die Frequenzkalibrierung kann vorher mit dem Sketch OSCCAL ermittelt und in den EEPROM geschrieben werden

-> [ATmega328P Bootloader \(RC-Oszillator mit Kalibrierung\)](#)

Bootloader ATmega1284P RC-Oszillator oder Quarz 8MHz

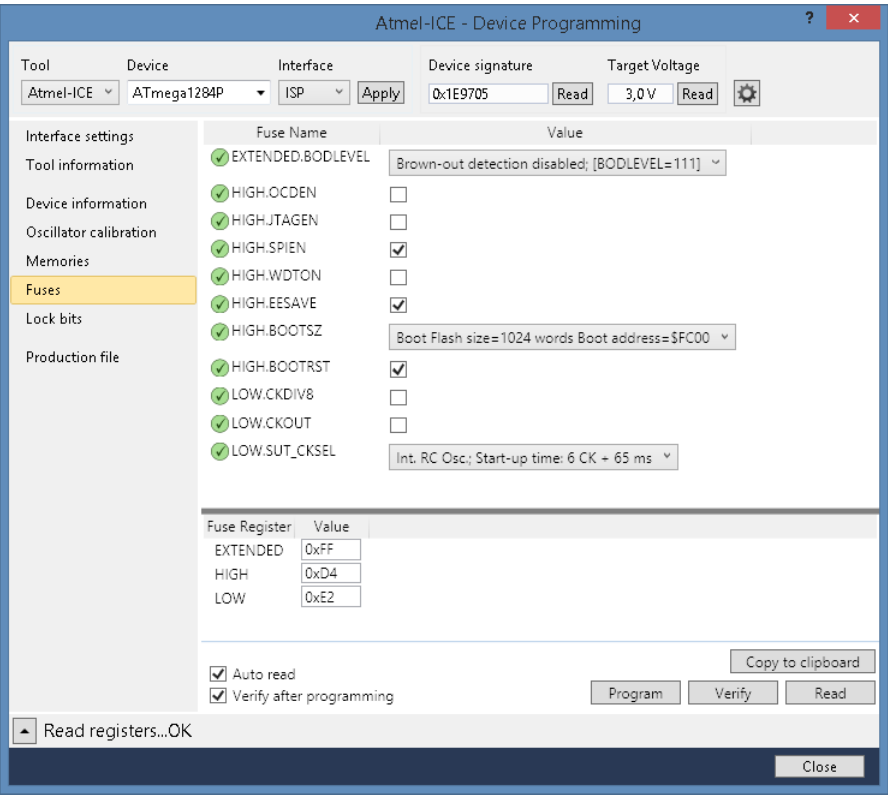


- Standard ATmega1284P Bootloader, 8MHz, z.B. für die HB-DIS-EP-42BW und HB-RC-12-EP Projekte
- Die MightyCore Lib gibt unterschiedliche Baudraten für 8MHz RC-Oszillator bzw. 8MHz Quarz vor, ich empfehle dies zu berücksichtigen um das Flash-Erlebnis angenehmer zu gestalten 😊

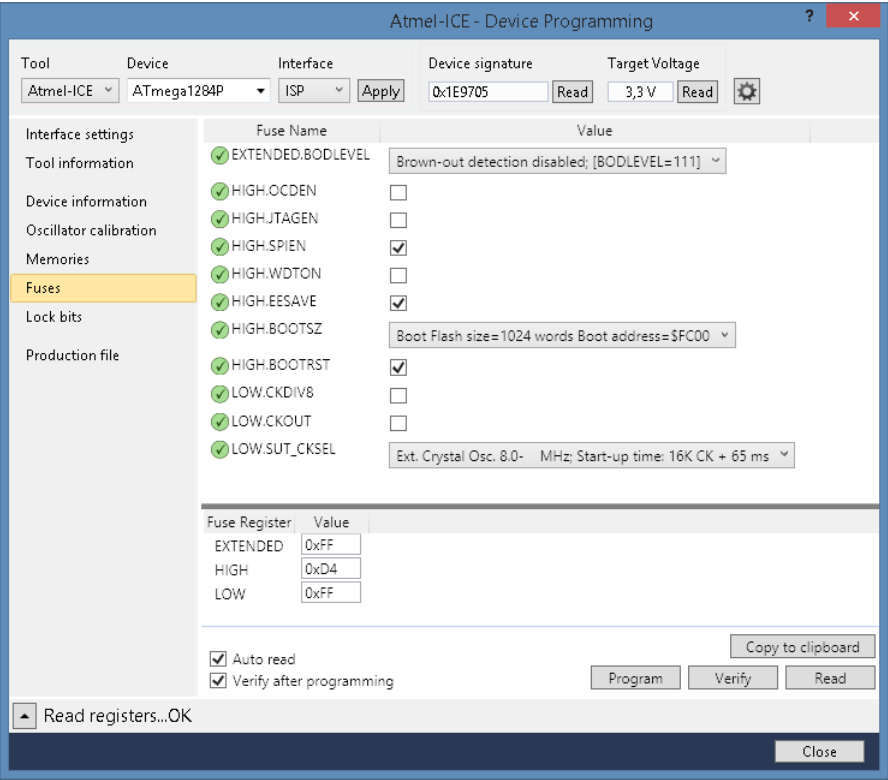
-> [ATmega1284P Bootloader \(RC-Oszillator oder Quarz 8MHz\)](#)

Fuses für 8MHz RC-Oszillator - Bootloader Version mit 38400 Baud benutzen





Fuses für 8MHz Quarz - Bootloader Version mit 57600 Baud benutzen



Atmel AVR Fuse Calculator



[Engbedded Atmel AVR® Fuse Calculator](#)

Beispiele für avrdude Kommandos



Fuses lesen



```
avrdude -c stk500 -p m328p -P COM5 -b 115200 -U lfuse:r:lowfuse.hex:h -U hfuse:r:highfuse.hex:h -U efuse:r:extfuse.hex:h
```



Fuses schreiben



```
avrdude -c stk500 -p m328p -P COM5 -b 115200 -U lfuse:w:0xE2:m -U hfuse:w:0xD2:m -U efuse:w:0xFF:m
```



Flashspeicher lesen



```
avrdude -c stk500 -p m328p -P COM5 -b 115200 -U flash:r:readtest.hex:i
```



Bootloader/Flashspeicher schreiben



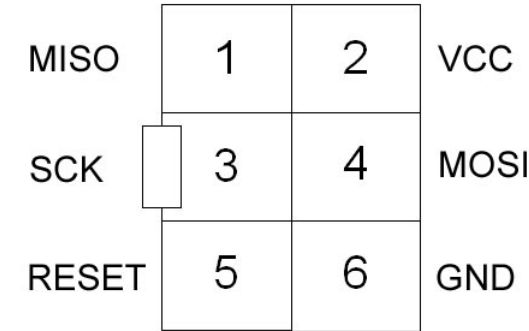
```
avrdude -c stk500 -p m328p -P COM5 -b 115200 -e -U flash:w:C:\temp\ATmegaBOOT_168_atmega328_pro_8MHz.hex:i
```



```
C:\Windows\system32\cmd.exe

c:\Program\AURDUDESS>avrdude -c stk500 -p m328p -P COM5 -b 115200 -e -U flash:w:C:\temp\ATmegaBOOT_168_atmega328_pro_8MHz.hex:i
avrdude: AVR device initialized and ready to accept instructions
Reading : ##### : 100% 0.06s
avrdude: Device signature = 0x1e950f (probably m328p)
avrdude: erasing chip
avrdude: reading input file "C:\temp\ATmegaBOOT_168_atmega328_pro_8MHz.hex"
avrdude: writing flash (32652 bytes):
Writing : ##### : 100% 0.24s
avrdude: 32652 bytes of flash written
avrdude: verifying flash memory against C:\temp\ATmegaBOOT_168_atmega328_pro_8MHz.hex:
avrdude: load data flash data from input file C:\temp\ATmegaBOOT_168_atmega328_pro_8MHz.hex:
avrdude: input file C:\temp\ATmegaBOOT_168_atmega328_pro_8MHz.hex contains 32652 bytes
avrdude: reading on-chip flash data:
Reading : ##### : 100% 0.06s
avrdude: verifying ...
avrdude: 32652 bytes of flash verified
avrdude: safemode: Fuses OK (E:FF, H:D2, L:E2)
avrdude done. Thank you.
```

Pinbelegung AVR ISP 6-polig



Pinbelegung Atmel-ICE AVR Port



Table 3-6. Atmel-ICE SPI Pin Mapping

Atmel-ICE AVR port pins	Target pins	SPI pinout
Pin 1 (TCK)	SCK	3
Pin 2 (GND)	GND	6
Pin 3 (TDO)	MISO	1
Pin 4 (VTG)	VTG	2
Pin 5 (TMS)		
Pin 6 (nSRST)	/RESET	5
Pin 7 (not connected)		
Pin 8 (nTRST)		
Pin 9 (TDI)	MOSI	4
Pin 10 (GND)		