Week	Lecture (Mon, 2pm) – SR1 (COM1-206)	Tutorial (Mon-Thu)	Lab (Mon-Thu)	Deadlines
1	L0: Course Admin L1: Introduction			
2	L2: Processes, Thread & Synchronization			
3	L3: Processor & Memory Organization		L1: Processes & Threads	
4	L4: Parallel Programming Models – I	T1: Parallel Computer Architecture		Wed, 2pm – Lab 1 due
5	L5: Performance of Parallel Systems		L2: Shared-memory Programming, Performance Instrumentation	Sun, 8pm – Quiz 1 due
6	L6: GPU Programming	T2: Performance of Parallel Systems		Wed, 2pm – Lab 2 due
Recess				Wed, 2pm – Assignment 1
7	L7: Cache Coherence & Memory Consistency		L3: CUDA programming	
8	L8: Performance Instrumentation	T3: Memory Consistency & CUDA Programming		Sun, 8pm – Quiz 2 due
9	L9: Parallel Programming Models – II	T4: Shared-memory Problems		
10	L10: Message-passing Programming		L4: Introduction to Distributed-memory Programming	Mon, 2pm - Assignment 2 Sun, 8pm – Quiz 3 due
11	L11: Interconnection Networks		L5: Message-passing Programming with MPI	Wed, 2pm – Lab 4 due
12	L12: Energy-efficient Computing and Cloud Computing L13: Summary & Recap	T5: MPI		Sun, 8pm – Quiz 4 due
13	PH: No lecture		L6: Benchmarks & performance	Fri, 2pm – Assignment 3 (optional) Sun, 2pm – Lab 6 due
Exam	29 Nov, 9am - Exam In-person, open book			Wed, 29 Nov, 9am - Exam

Grading policy:

- 6% - Lab (3 lab sheets)
- 4% - Tutorial attendance and participation
- 35% - Programming assignments (C/C++, OpenMP, MPI, CUDA)
- 15% - 4 quizzes due on Sun, 8pm in weeks 5, 8, 10, and 12
- 40% - Final exam (29 Nov, 9am, in-person, open book)