

1					2					3					4				
HISTROY REVIEW																			
A	NO.	Version	Date	Board	Content	Reason	NO.	Version	Date	Board	Content	Reason	A						
	1	VC1.0	2020/8/14	Power/Main	Original version		30												
	2	VP1.0	2020/10/23	Power/Main	VP(部分物料料号更改, POE磁珠封装改为0402)		31												
	3						32												
	4						33												
B	5						34						B						
	6						35												
	7						36												
	8						37												
	9						38												
	10						39												
	11						40												
	12						41												
	13						42												
	14						43												
C	15						44						C						
	16						45												
	17						46												
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	19						48												
	20						49												
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	26						55												
	27						56												
	28						57												
	29						58												
<div><div>汉 桑(南京)科技有限公司</div><div>HANSONG (NANJING) TECHNOLOGY LTD.<div>(No.HS/QR-RD-25)</div></div></div> <div><div>THE INFORMATION CONTAINED IN THIS DRAWING IS THE SOLE PROPERTY OF HANSONG TECHNOLOGY CO.,LTD. ANY REPRODUCTION IN PART OF WHOLE WITHOUT THE WRITTEN PERMISSION OF HANSONG TECHNOLOGY CO.,LTD. IS PROHIBITED</div><div><div>Project Name: HS40-1177-MAIN BOARD-VP1_0.PrjPCB</div><div>File Name: HS40-1177-B09-History Review.SchDoc</div><div>Drawn by: Richard</div></div><div><div>Sheet: 1 of 3</div><div>Version: VP1_0</div><div>Approved by: GLName</div></div><div><div>Date: 2020/12/09</div><div>Date: -----</div></div></div>																			

A

B

C

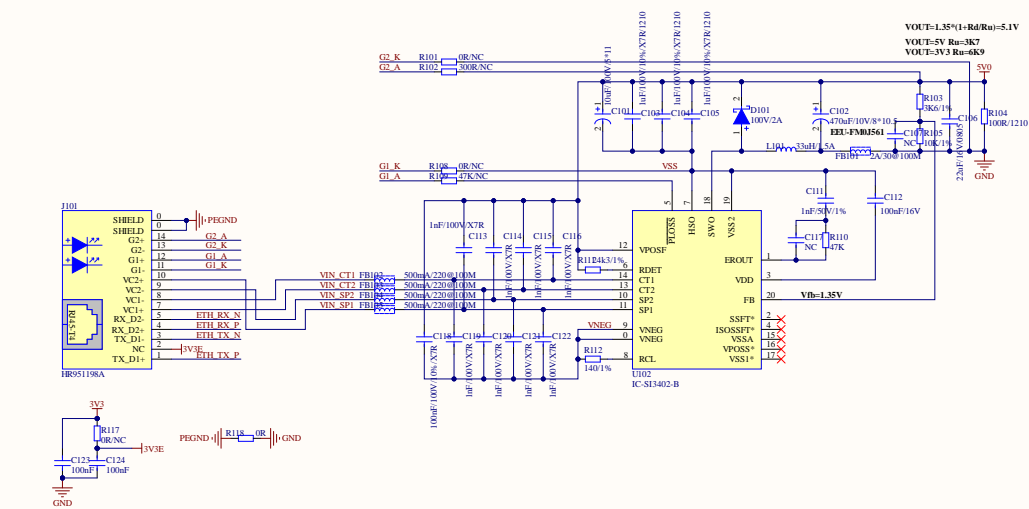
D

A

B

C

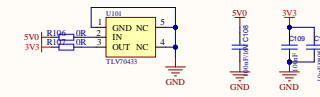
D



- 1.Detection 2.8~10V Ilimit=5mA
- 2.Classification 15.5~20.5V Ilimit=100mA

RCL	CLASS	PSE POWER	RLCASS	I _{max}
0		15.4W/10.36	FLOAT	4mA
1		4.0W/2.95	140Ω	12mA
2		7W/4.84W	75Ω	20mA
3		15.4W	48.7Ω	28mA

- 3.Power UP 44~57V
- 4.MPS(Maintain power)
- 4.Turn OFF



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E:\Documents\设计规范文件\PCB\PCB层叠1.2mm
impedance.png

PCB差分线阻抗计算器

线宽(W): 5 mil

线距(S): 10 mil

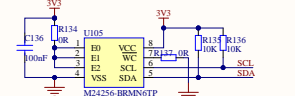
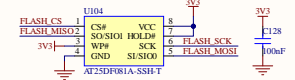
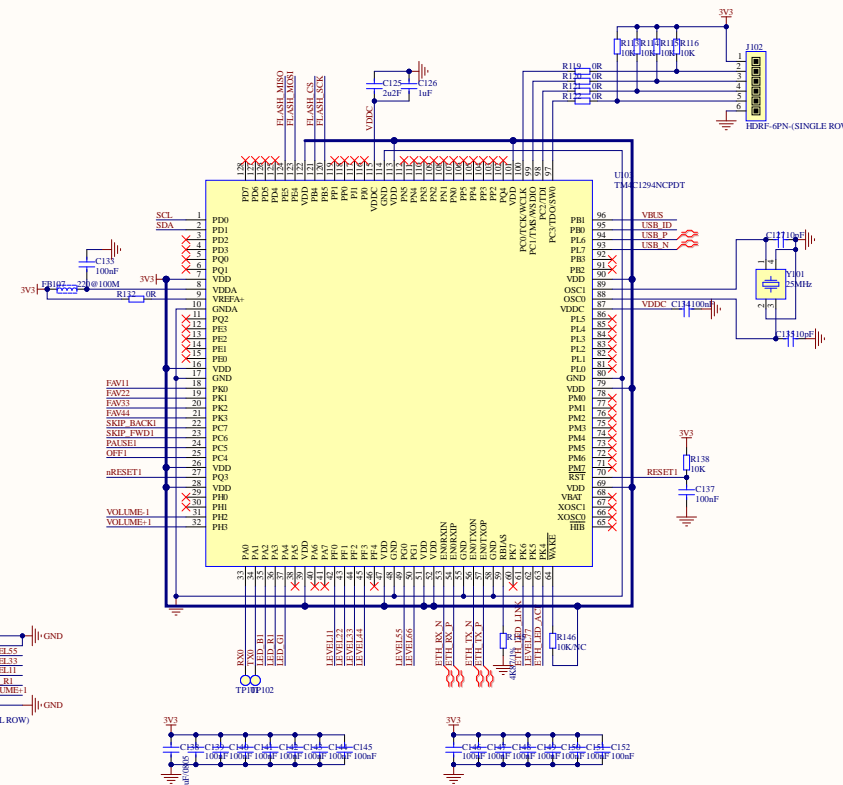
铜厚(T): 1 mil

介电常数(Dr): 4.3

介电厚度(Ht): 0.05 mil

开始计算(Z0)

说明: 以上计算结果仅供参考



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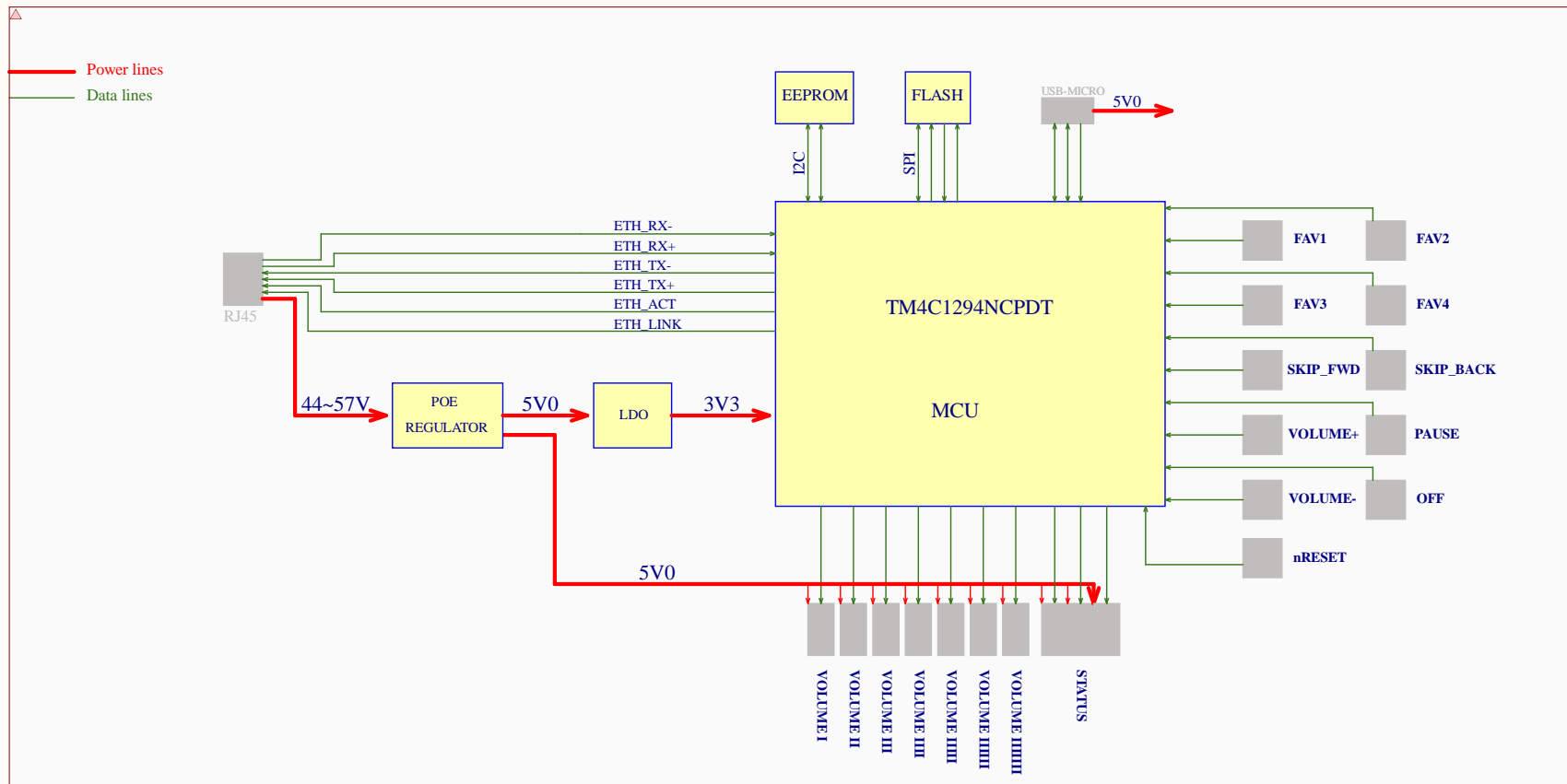
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Project Name: HS40-1177-MAIN BOARD-VP1_0.PrjPCB Sheet: 2 of 3

File Name: HS40-1177-B09-MAIN BOARD.SchDoc Version: VP1_0

Drawn by: Richard Date: 2020/12/09 Approved by: GLName Date:



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Project Name: HS40-1177-MAIN BOARD-VP1_0.PrjPCB			Sheet: 2 of 3
File Name: HS40-1177-B05-BLOCK DIAGRAM.SchDoc			Version: VP1_0
Drawn by: Richard	Date: 2020/12/09	Approved by: GLName	Date: