MC 206 COA TOPICS FOR MID SEM

UNIT	TOPICS	BOOK TITLE	CHAPTERS
I	Basic Circuits Of Adders, Subtractors, Encoders, Decoders, Multiplexers, Demultiplexers, Flip Flops.	Digital Logic and Computer Design by Morris Mano	Chapters: 4 (4.3, 4.4), 5 (5.2, 5.4, 5.5, 5.6, 5.7), and 6 (6.1, 6.2)
	Amdahl's Law, Clock Speed and MIPS Rate of CPU, Computer Components, BUS Interconnections,	Computer Organization and Architecture by William Stallings	Chapters: 2 (2.3, 2.4), 3(3.2, 3.3, 3.4,)
	Register Transfer and other Micro-operations	Computer System Architecture by Morris Mano	Chapters: 4 (entire chapter except 4.7)
II	Binary Numbers Conversion, Signed Unsigned Numbers, Addition , Subtraction, Floating Point Representation	Computer System Architecture by Morris Mano	Chapters: 3 (3.1, 3.2, 3.3, 3.4)
	Multiplication Booths Algorithm, Division Algorithms	Computer System Architecture by Morris Mano	Chapters: 10 (10.3, 10.4)
	Processor Organization: General Register and Stack, Addressing Modes	Computer System Architecture by Morris Mano	Chapters: 8 (8.2, 8.3, 8.5)