

The left diagram shows the input stage of the power supply. It features a 5V regulator (U2) with input pins VIN1, VIN2, VIN3, and VIN4. The output of the regulator is connected to the module's GND. The 24V input (J6) is connected to the module's GND. The output of the 24V regulator (U8) is connected to the module's GND. The output of the 24V regulator is also connected to the module's GND.

The right diagram shows the output stage of the power supply. It features a 24V regulator (U8) with input pins IN, EN, and FB. The output of the regulator is connected to the module's GND. The output of the 24V regulator is also connected to the module's GND. The output of the 24V regulator is also connected to the module's GND.

PA3: RXD
PB13: SWCLK
PB14: SWDIO
PB0: NRST

[illegible]

The diagram illustrates the hardware connection for the SPI flash memory. It shows the SPI0_CS signal path from the microcontroller, through a 1kΩ resistor (R41) and a 33kΩ resistor (R43), to the FLASH_CS pin of the W5201Q28 chip. A switch (SW1) labeled "海电" allows for manual selection between different configurations. Power supply pins are connected to +3V3 and GND, with a 100nF capacitor (C36) used for decoupling.