

**IBM**

*Personal Computer*

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## **IBM Enhanced Graphics Adapter**

IBM ENHANCED GRAPHICS ADAPTER



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# Description

The IBM Enhanced Graphics Adapter (EGA) is a graphics controller that supports both color and monochrome direct drive displays in a variety of modes. In addition to the direct drive port, a light pen interface is provided. Advanced features on the adapter include bit-mapped graphics in four planes and a RAM (Random Access Memory) loadable character generator. Design features in the hardware substantially reduce the software overhead for many graphics functions.

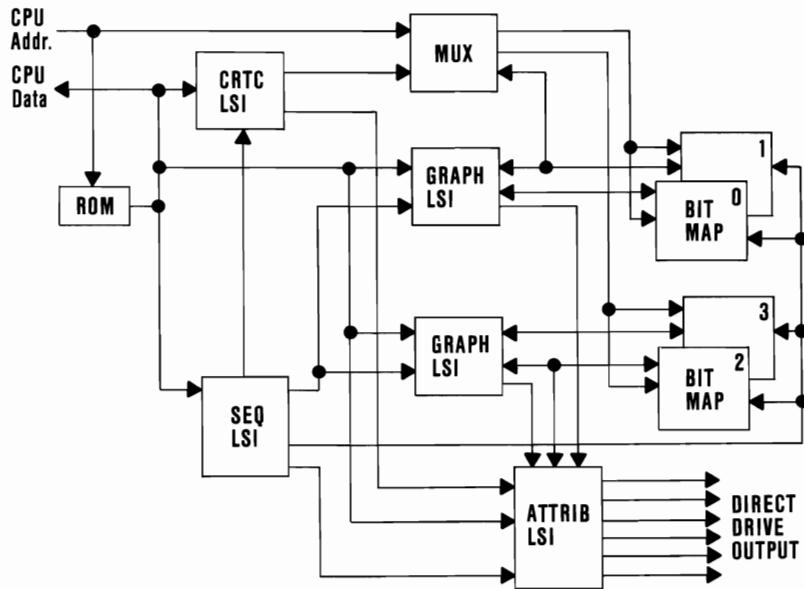
The Enhanced Graphics Adapter provides Basic Input Output System (BIOS) support for both alphanumeric (A/N) modes and all-points-addressable (APA) graphics modes, including all modes supported by the Monochrome Display Adapter and the Color/Graphics Monitor Adapter. Other modes provide APA 640x350 pel graphics support for the IBM Monochrome Display, full 16 color support in both 320x200 pel and 640x200 pel resolutions for the IBM Color Display, and both A/N and APA support with resolution of 640x350 for the IBM Enhanced Color Display. In alphanumeric modes, characters are formed from one of two ROM (Read Only Memory) character generators on the adapter. One character generator defines 7x9 characters in a 9x14 character box. For Enhanced Color Display support, the 9x14 character set is modified to provide an 8x14 character set. The second character generator defines 7x7 characters in an 8x8 character box. These generators contain dot patterns for 256 different characters. The character sets are identical to those provided by the IBM Monochrome Display Adapter and the IBM Color/Graphics Monitor Adapter.

The adapter contains 64K bytes of storage configured as four 16K byte bit planes. Memory expansion options are available to expand the adapter memory to 128K bytes or 256K bytes.

The adapter is packaged on a single 13-1/8 inch (333.50 mm) card. The direct drive port is a right-angle mounted connector at the rear of the adapter and extends through the rear panel of the system unit. Also on the card are five large scale integration (LSI) modules custom designed for this controller.

Located on the adapter is a feature connector that provides access to internal functions through a 32-pin berg connector. A separate 64-pin connector provides an interface for graphics memory expansion.

The following is a block diagram of the Enhanced Graphics Adapter:



**Enhanced Graphics Adapter Block Diagram**

# **Major Components**

## **CRT Controller**

The CRT (Cathode Ray Tube) Controller (CRTC) generates horizontal and vertical synchronous timings, addressing for the regenerative buffer, cursor and underline timings, and refresh addressing for the dynamic RAMs.

## **Sequencer**

The Sequencer generates basic memory timings for the dynamic RAMs and the character clock for controlling regenerative memory fetches. It allows the processor to access memory during active display intervals by inserting dedicated processor memory cycles periodically between the display memory cycles. Map mask registers are available to protect entire memory maps from being changed.

## **Graphics Controller**

The Graphics Controller directs the data from the memory to the attribute controller and the processor. In graphics modes, memory data is sent in serialized form to the attribute chip. In alpha modes the memory data is sent in parallel form, bypassing the graphics controller. The graphics controller formats the data for compatible modes and provides color comparators for use in color painting modes. Other hardware facilities allow the processor to write 32 bits in a single memory cycle, (8 bits per plane) for quick color presetting of the display areas, and additional logic allows the processor to write data to the display on non-byte boundaries.

## **Attribute Controller**

The Attribute Controller provides a color palette of 16 colors, each of which may be specified separately. Six color outputs are

available for driving a display. Blinking and underlining are controlled by this chip. This chip takes data from the display memory and formats it for display on the CRT screen.

## Display Buffer

The display buffer on the adapter consists of 64K bytes of dynamic read/write memory configured as four 16K byte video bit planes. Two options are available for expanding the graphics memory. The Graphics Memory Expansion Card plugs into the memory expansion connector on the adapter, and adds one bank of 16K to each of the four bit planes, increasing the graphics memory to 128K bytes. The expansion card also provides DIP sockets for further memory expansion. Populating the DIP sockets with the Graphics Memory Module Kit adds two additional 16K banks to each bit plane, bringing the graphics memory to its maximum of 256K bytes.

The address of the display buffer can be changed to remain compatible with other video cards and application software. Four locations are provided. The buffer can be configured at segment address hex A0000 for a length of 128K bytes, at hex A0000 for a length of 64K bytes, at hex B0000 for a length of 32K bytes, or at hex B8000 for a length of 32K bytes.

## BIOS

A read-only memory (ROM) Basic Input Output System (BIOS) module on the adapter is linked to the system BIOS. This ROM BIOS contains character generators and control code and is mapped into the processor address at hex C0000 for a length of 16K bytes.

## Support Logic

The logic on the card surrounding the LSI modules supports the modules and creates latch buses for the CRT controller, the

processor, and character generator. Two clock sources (14 MHz and 16 MHz) provide the dot rate. The clock is multiplexed under processor I/O control. Four I/O registers also resident on the card are not part of the LSI devices.

## Modes of Operation

### IBM Color Display

The following table describes the modes supported by BIOS on the IBM Color Display:

MODE #	TYPE	COLORS	ALPHA FORMAT	BUFFER START	BOX SIZE	MAX. PAGES	RESOLUTION
0	A/N	16	40x25	B8000	8x8	8	320x200
1	A/N	16	40x25	B8000	8x8	8	320x200
2	A/N	16	80x25	B8000	8x8	8	640x200
3	A/N	16	80x25	B8000	8x8	8	640x200
4	APA	4	40x25	B8000	8x8	1	320x200
5	APA	4	40x25	B8000	8x8	1	320x200
6	APA	2	80x25	B8000	8x8	1	640x200
D	APA	16	40x25	A0000	8x8	2/4/8	320x200
E	APA	16	80x25	A0000	8x8	1/2/4	640x200

Modes 0 through 6 emulate the support provided by the IBM Color/Graphics monitor Adapter.

Modes 0,2 and 5 are identical to modes 1,3 and 4 respectively at the adapter's direct drive interface.

The Maximum Pages fields for modes D and E indicate the number of pages supported when 64K, 128K or 256K bytes of graphics memory is installed, respectively.

## IBM Monochrome Display

The following table describes the modes supported by BIOS on the IBM Monochrome Display.

MODE #	TYPE	COLORS	ALPHA FORMAT	BUFFER START	BOX SIZE	MAX. PAGES	RESOLUTION
7	A/N	4	80x25	B0000	9x14	8	720x350
F	APA	4	80x25	A0000	8x14	1/2	640x350

Mode 7 emulates the support provided by the IBM Monochrome Display Adapter.

## IBM Enhanced Color Display

The Enhanced Graphics Adapter supports attachment of the IBM Enhanced Color Display. The IBM Enhanced Color Display is capable of running at the standard television frequency of 15.75 KHz as well as running 21.85 KHz. The table below summarizes the characteristics of the IBM Enhanced Color Display:

PARAMETER	TV FREQUENCY	HIGH RESOLUTION
Horiz Scan Rate	15.75 KHz.	21.85 KHz.
Vertical Scan Rate	60 Hz.	60 Hz.
Video Bandwidth	14.318 MHz.	16.257 MHz.
Displayable Colors	16 Maximum	16 or 64
Character Size	7 by 7 Pels	7 by 9 Pels
Character Box Size	8 by 8 Pels	8 by 14 Pels
Maximum Resolution	640x200 Pels	640 by 350 Pels
Alphanumeric Modes	0,1,2,3	0,1,2,3
Graphics Modes	4,5,6,D,E	10

In the television frequency mode, the IBM Enhanced Color Display displays information identical in color and resolution to the IBM Color Display.

In the high resolution mode, the adapter provides enhanced alphanumeric character support. This enhanced alphanumeric support consists of transforming the 8 by 8 character box into an 8 by 14 character box, and providing 16 colors out of a palette of

64 possible display colors. Display colors are changed by altering the programming of the color palette registers in the Attribute Controller. In alphanumeric modes, any 16 of 64 colors are displayable. the screen resolution is 320x350 for modes 0 and 1, and 640x350 for modes 2 and 3.

The resolution displayed on the IBM Enhanced Color Display is selected by the switch settings on the Enhanced Graphics Adapter.

The Enhanced Color Display is compatible with all modes listed for the IBM Color Display. the following table describes additional modes supported by BIOS for the IBM Enhanced Color Display:

MODE #	TYPE	COLORS	ALPHA FORMAT	BUFFER START	BOX SIZE	MAX. PAGES	RESOLUTION
0*	A/N	16/64	40x25	B8000	8x14	8	320x350
1*	A/N	16/64	40x25	B8000	8x14	8	320x350
2*	A/N	16/64	80x25	B8000	8x14	8	640x350
3*	A/N	16/64	80x25	B8000	8x14	8	640x350
10*	APA	4/16 16/64	80x25	A0000	8x14	1/2	640x350

\* Note that modes 0, 1, 2, and 3, are also listed for IBM Color Display support. BIOS provides enhanced support for these modes when an Enhanced Color Display is attached.

The values in the "COLORS" field indicate 16 colors of a 64 color palette or 4 colors of a sixteen color palette.

In mode 10, The dual values for the "COLORS" field and the "MAX. PAGES" field indicate the support provided when 64K or when greater than 64K of graphics memory is installed, respectively.

# **Basic Operations**

## **Alphanumeric Modes**

The data format for alphanumeric modes on the Enhanced Graphics Adapter is the same as the data format on the IBM Color/Graphics Monitor Adapter and the IBM Monochrome Display Adapter. As an added function, bit three of the attribute byte may be redefined by the Character Map Select register to act as a switch between character sets. This gives the programmer access to 512 characters at one time. This function is valid only when memory has been expanded to 128K bytes or more.

When an alphanumeric mode is selected, the BIOS transfers character patterns from the ROM to bit plane 2. The processor stores the character data in bit plane 0, and the attribute data in bit plane 1. The programmer can view bit planes 0 and 1 as a single buffer in alphanumeric modes. The CRTC generates sequential addresses, and fetches one character code byte and one attribute byte at a time. The character code and row scan count address bit plane 2, which contains the character generators. The appropriate dot patterns are then sent to the palette in the attribute chip, where color is assigned according to the attribute data.

## **Graphics Modes**

### **320x200 Two and Four Color Graphics (Modes 4 and 5)**

Addressing, mapping and data format are the same as the 320x200 pel mode of the Color/Graphics Monitor Adapter. The display buffer is configured at hex B8000. Bit image data is stored in bit planes 0 and 1.

### **640x200 Two Color Graphics (Mode 6)**

Addressing, mapping and data format are the same as the 640x200 pel black and white mode of the Color/Graphics

Monitor Adapter. The display buffer is configured at hex B8000. Bit image data is stored in bit plane 0.

## 640x350 Monochrome Graphics (Mode F )

This mode supports graphics on the IBM Monochrome Display with the following attributes: black, video, blinking video, and intensified video. Resolution of 640x350 requires 56K bytes to support four attributes. By chaining maps 0 and 1, then maps 2 and 3 together, two 32K bit planes can be formed. This chaining is done only when necessary (less than 128K of graphics memory). The first map is the video bit plane, and the second map is the intensity bit plane. Both planes reside at hex address A0000.

Two bits, one from each bit plane, define one picture element (pel) on the screen. The bit definitions for the pels are given in the following table. The video bit plane is denoted by C0 and the Intensity Bit Plane is denoted by C2.

C2	C0	Pixel Color	Valid Attributes
0	0	Black	0
0	1	Video	3
1	0	Blinking Video	C
1	1	Intensified Video	F

The byte organization in memory is sequential. The first eight pels on the screen are defined by the contents of memory in location A000:0H, the second eight pels by location A000:1H, and so on. The first pel within any one byte is defined by bit 7 in the byte. The last pel within the byte is defined by bit 0 in the byte.

Monochrome graphics works in odd/even mode, which means that even CPU addresses go into even bit planes and odd CPU addresses go into odd bit planes. Since both bit planes reside at address A0000, the user must select which plane or planes he desires to update. This is accomplished by the map mask register of the sequencer. (See the table above for valid attributes).

## 16/64 Color Graphics Modes (Mode 10)

These modes support graphics in 16 colors on either a medium or high resolution monitor. The memory in these modes consists of using all four bit planes. Each bit plane represents a color as shown below. The bit planes are denoted as C0,C1,C2 and C3 respectively.

C0 = Blue Pels

C1 = Green Pels

C2 = Red Pels

C3 = Intensified Pels

Four bits (one from each plane) define one pel on the screen. The color combinations are illustrated in the following table:

I	R	G	B	Color
0	0	0	0	Black
0	0	0	1	Blue
0	0	1	0	Green
0	0	1	1	Cyan
0	1	0	0	Red
0	1	0	1	Magenta
0	1	1	0	Brown
0	1	1	1	White
1	0	0	0	Dark Gray
1	0	0	1	Light Blue
1	0	1	0	Light Green
1	0	1	1	Light Cyan
1	1	0	0	Light Red
1	1	0	1	Light Magenta
1	1	1	0	Yellow
1	1	1	1	Intensified White

The display buffer resides at address A0000. The map mask register of the sequencer is used to select any or all of the bit planes to be updated when a memory write to the display buffer is executed by the CPU.

### Color Mapping

The Enhanced Graphics Adapter supports 640x350 Graphics for both the IBM Monochrome and the IBM Enhanced Color

Displays. Four color capability is supported on the EGA without the Graphics Memory Expansion Card (base 64 KB), and sixteen colors are supported when the Graphics Memory Expansion Card is installed on the adapter (128 KB or above). This section describes the differences in the colors displayed depending upon the graphics memory available. Note that colors 0H, 1H, 4H, and 7H map directly regardless of the graphics memory available.

Character Attribute	Monochrome	Mode 10H 64KB	Mode 10H >64KB
00H*	Black	Black	Black
01H*	Video	Blue	Blue
02H	Black	Black	Green
03H	Video	Blue	Cyan
04H*	Blinking	Red	Red
05H	Intensified	White	Magenta
06H	Blinking	Red	Brown
07H*	Intensified	White	White
08H	Black	Black	Dark Gray
09H	Video	Blue	Light Blue
0AH	Black	Black	Light Green
0BH	Video	Blue	Light Cyan
0CH	Blinking	Red	Light Red
0DH	Intensified	White	Light Magenta
0EH	Blinking	Red	Yellow
0FH	Intensified	White	Intensified White

\* Graphics character attributes which map directly regardless of the graphics memory available.

# Registers

## External Registers

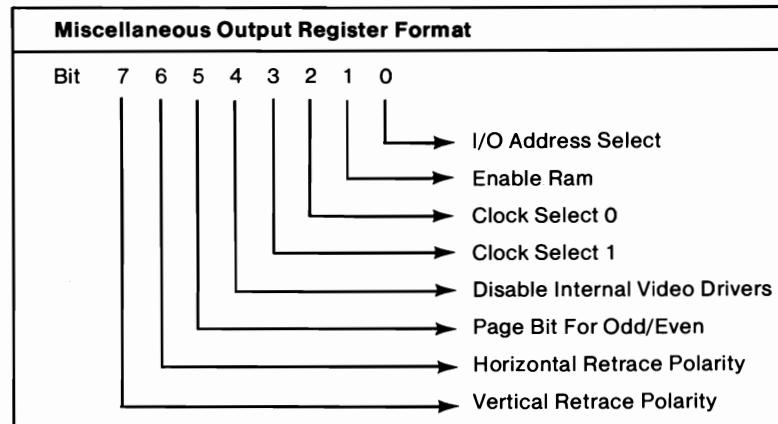
This section contains descriptions of the registers of the Enhanced Graphics Adapter that are not contained in an LSI device.

Name	Port	Index
Miscellaneous Output Register	3C2	-
Feature Control Register	3?A	-
Input Status Register 0	3C2	-
Input Status Register 1	3?2	-

? = B in Monochrome Modes      ? = D in Color Modes

### Miscellaneous Output Register

This is a write-only register. The processor output port address is hex 3C2. A hardware reset causes all bits to reset to zero.



#### Bit 0

3BX/3DX CRTC I/O Address—This bit maps the CRTC I/O addresses for IBM Monochrome or Color/Graphics Monitor Adapter emulation. A logical 0 sets CRTC addresses to 3BX and Input Status Register 1's address to 3BA for Monochrome emulation. A logical 1 sets CRTC

addresses to 3DX and Input Status Register 1's address to 3DA for Color/Graphics Monitor Adapter emulation.

**Bit 1** Enable RAM—A logical 0 disables RAM from the processor; a logical 1 enables RAM to respond at addresses designated by the Control Data Select value programmed into the Graphics Controllers.

**Bit 2-Bit 3** Clock Select—These two bits select the clock source according to the following table:

Bits	
3 2	
<b>0 0-</b>	Selects 14 MHz clock from the processor I/O channel
<b>0 1-</b>	Selects 16 MHz clock on-board oscillator
<b>1 0-</b>	Selects external clock source from the feature connector.
<b>1 1-</b>	Not used

**Bit 4** Disable Internal Video Drivers—A logical 0 activates internal video drivers; a logical 1 disables internal video drivers. When the internal video drivers are disabled, the source of the direct drive color output becomes the feature connector direct drive outputs.

**Bit 5** Page Bit For Odd/Even—Selects between two 64K pages of memory when in the Odd/Even modes (0,1,2,3,7). A logical 0 selects the low page of memory; a logical 1 selects the high page of memory.

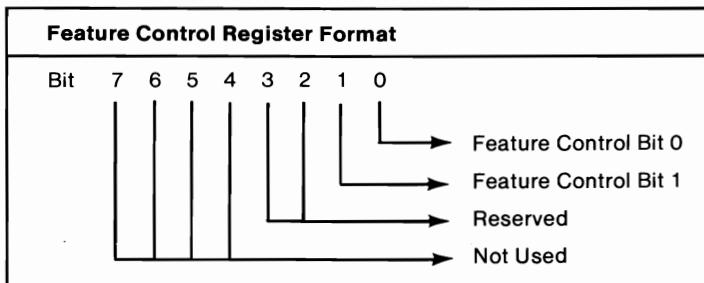
**Bit 6** Horizontal Retrace Polarity—A logical 0 selects positive horizontal retrace; a logical 1 selects negative horizontal retrace.

**Bit 7** Vertical Retrace Polarity—A logical 0 selects positive vertical retrace; a logical 1 selects

negative vertical retrace. The IBM Monochrome display requires a negative vertical retrace polarity.

## Feature Control Register

This is a write-only register. The processor output register is hex 3BA or 3DA.

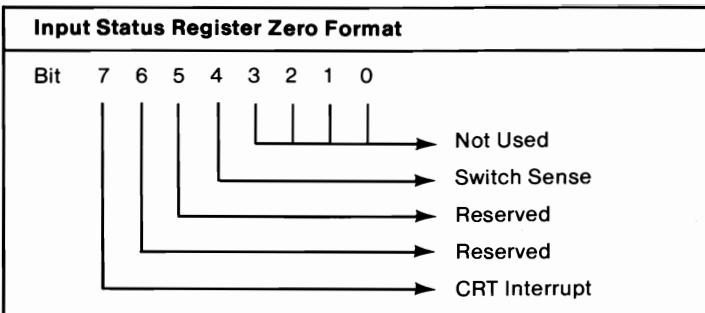


### Bits 0 and 1

Feature Control Bits—These bits are used to convey information to the feature connector. The output of these bits goes to the FEAT 0 (pin 19) and FEAT 1 (pin 17) of the feature connector.

## Input Status Register Zero

This is a read-only register. The processor input port address is hex 3C2.



**Bit 4**      **Switch Sense**—When set to 1, this bit allows the processor to read the four configuration switches on the board. The setting of the CLKSEL field determines which switch is being read. The switch configuration can be determined by reading byte 40:88H in RAM.

Bit 3: Switch 4 ; Logical 0 = switch closed

Bit 2: Switch 3 ; Logical 0 = switch closed

Bit 1: Switch 2 ; Logical 0 = switch closed

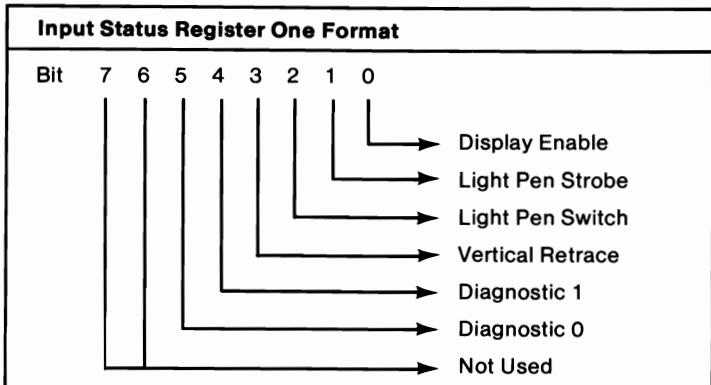
Bit 0: Switch 1 ; Logical 0 = switch closed

**Bits 5 and 6**      **Feature Code**—These bits are input from the Feat (0) and Feat (1) pins on the feature connector.

**Bit 7**      **CRT Interrupt**—A logical 1 indicates video is being displayed on the CRT screen; a logical 0 indicates that vertical retrace is occurring.

## **Input Status Register One**

This is a read-only register. The processor port address is hex 3BA or hex 3DA.



- Bit 0**      **Display Enable**—Logical 0 indicates the CRT raster is in a horizontal or vertical retrace interval. This bit is the real time status of the display enable signal. Some programs use this status bit to restrict screen updates to inactive display intervals. The Enhanced Graphics Adapter does not require the CPU to update the screen buffer during inactive display intervals to avoid glitches in the display image.
- Bit 1**      **Light Pen Strobe**—A logical 0 indicates that the light pen trigger has not been set; a logical 1 indicates that the light pen trigger has been set.
- Bit 2**      **Light Pen Switch**—A logical 0 indicates that the light pen switch is closed; a logical 1 indicates that the light pen switch is open.
- Bit 3**      **Vertical Retrace**—A logical 0 indicates that video information is being displayed on the CRT screen; a logical 1 indicates the CRT is in a vertical retrace interval. This bit can be programmed to interrupt the processor on interrupt level 2 at the start of the vertical retrace. This is done through bits 4 and 5 of the Vertical Retrace End Register of the CRTC.
- Bits 4 and 5**      **Diagnostic Usage**—These bits are selectively connected to two of the six color outputs of the

**Attribute Controller.** The Color Plane Enable register controls the multiplexer for the video wiring. The following table illustrates the combinations available and the color output wiring.

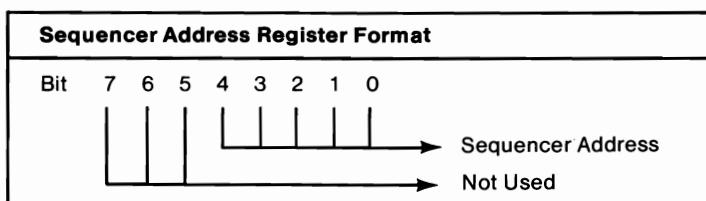
Color Plane Register		Input Status Register One	
Bit 5	Bit 4	Bit 5	Bit 4
0	0	Red	Blue
0	1	Secondary Blue	Green
1	0	Secondary Red	Secondary Green
1	1	Not Used	Not Used

## **Sequencer Registers**

Name	Port	Index
Address	3C4	-
Reset	3C5	00
Clocking Mode	3C5	01
Map Mask	3C5	02
Character Map Select	3C5	03
Memory Mode	3C5	04

## **Sequencer Address Register**

The Address Register is a pointer register located at address hex 3C4. This register is loaded with a binary value that points to the sequencer data register where data is to be written. This value is referred to as "Index" in the table above.



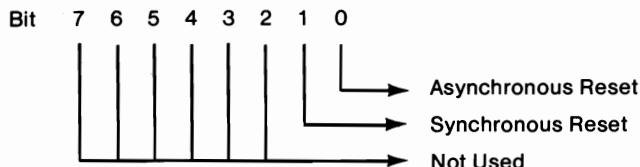
### **Bit 0-Bit 3**

**Sequencer Address Bits**—A binary value pointing to the register where data is to be written.

## **Reset Register**

This is a write-only register pointed to when the value in the address register is hex 00. The output port address for this register is hex 3C5.

### Reset Register Format

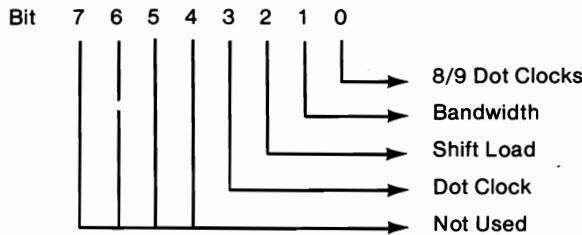


- Bit 0** Asynchronous Reset—A logical 0 commands the sequencer to asynchronous clear and halt. All outputs are placed in the high impedance state when this bit is a 0. A logical 1 commands the sequencer to run unless bit 1 is set to zero. Resetting the sequencer with this bit can cause data loss in the dynamic RAMs.
- Bit 1** Synchronous Reset—A logical 0 commands the sequencer to synchronous clear and halt. Bits 1 and 0 must both be ones to allow the sequencer to operate. Reset the sequencer with this bit before changing the Clocking Mode Register, if memory contents are to be preserved.

### Clocking Mode Register

This is a write-only register pointed to when the value in the address register is hex 01. The output port address for this register is hex 3C5.

### Clocking Mode Register Format

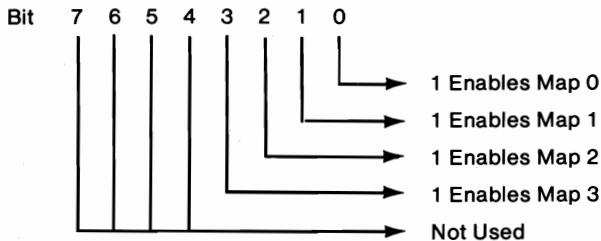


- Bit 0** 8/9 Dot Clocks—A logical 0 directs the sequencer to generate character clocks 9 dots wide; a logical 1 directs the sequencer to generate character clocks 8 dots wide. Monochrome alphanumeric mode (07H) is the only mode that uses character clocks 9 dots wide. All other modes must use 8 dots per character clock.
- Bit 1** Bandwidth—A logical 0 makes CRT memory cycles occur on 4 out of 5 available memory cycles; a logical 1 makes CRT memory cycles occur on 2 out of 5 available memory cycles. Medium resolution modes require less data to be fetched from the display buffer during the horizontal scan time. This allows the CPU greater access time to the display buffer. All high resolution modes must provide the CRTC with 4 out of 5 memory cycles in order to refresh the display image.
- Bit 2** Shift Load—When set to 0, the video serializers are reloaded every character clock; when set to 1, the video serializers are loaded every other character clock. This mode is useful when 16 bits are fetched per cycle and chained together in the shift registers.
- Bit 3** Dot Clock—A logical 0 selects normal dot clocks derived from the sequencer master clock input. When this bit is set to 1, the master clock will be divided by 2 to generate the dot clock. All the other timings will be stretched since they are derived from the dot clock. Dot clock divided by two is used for 320x200 modes (0, 1, 4, 5) to provide a pixel rate of 7 MHz, (9 MHz for mode D).

### Map Mask Register

This is a write-only register pointed to when the value in the address register is hex 02. The output port address for this register is hex 3C5.

**Map Mask Register Format**



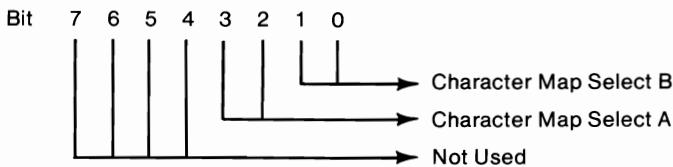
#### **Bit 0–Bit 3**

**Map Mask**—A logical 1 in bits 3 through 0 enables the processor to write to the corresponding maps 3 through 0. If this register is programmed with a value of OFH, the CPU can perform a 32-bit write operation with only one memory cycle. This substantially reduces the overhead on the CPU during display update cycles in graphics modes. Data scrolling operations are also enhanced by setting this register to a value of OFH and writing the display buffer address with the data stored in the CPU data latches. This is a read-modify-write operation. When odd/even modes are selected, maps 0 and 1 and maps 2 and 3 should have the same map mask value.

#### **Character Map Select Register**

This is a write-only register pointed to when the value in the address register is hex 03. The output port address for this register is 3C5.

### Character Map Select Register Format



**Bit 0-Bit 1**      Character Map Select B—Selects the map used to generate alpha characters when attribute bit 3 is a 0, according to the following table:

Bits 1      0	Map Selected	Table Location
Value		
0      0	0	1st 8K of Plane 2 Bank 0
0      1	1	2nd 8K of Plane 2 Bank 1
1      0	2	3rd 8K of Plane 2 Bank 2
1      1	3	4th 8K of Plane 2 Bank 3

**Bit 2-Bit 3**      Character Map Select A—Selects the map used to generate alpha characters when attribute bit 3 is a 1, according to the following table:

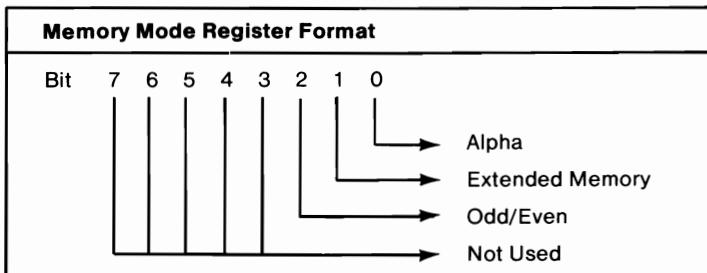
Bits 3      2	Map Selected	Table Location
Value		
0      0	0	1st 8K of Plane 2 Bank 0
0      1	1	2nd 8K of Plane 2 Bank 1
1      0	2	3rd 8K of Plane 2 Bank 2
1      1	3	4th 8K of Plane 2 Bank 3

In alphanumeric modes, bit 3 of the attribute byte normally has the function of turning the foreground intensity on or off. This bit however may be redefined as a switch between character sets. This function is enabled when there is a difference between the value in Character Map Select A and the value in Character Map Select B. Whenever these two values are the same, the character select function is disabled. The memory mode register bit 1 must be a 1 (indicates the memory extension card is installed in the unit) to enable this function; otherwise, bank 0 is always selected.

128K of graphics memory is required to support two character sets. 256K supports four character sets. Asynchronous reset clears this register to 0. This should be done only when the sequencer is reset.

## Memory Mode Register

This is a write-only register pointed to when the value in the address register is hex 04. The processor output port address for this register is 3C5.



- Bit 0**      Alpha—A logical 0 indicates that a non-alpha mode is active. A logical 1 indicates that alpha mode is active and enables the character generator map select function.
- Bit 1**      Extended Memory—A logical 0 indicates that the memory expansion card is not installed. A logical 1 indicates that the memory expansion card is installed and enables access to the extended memory through address bits 14 and 15.
- Bit 2**      Odd/Even—A logical 0 directs even processor addresses to access maps 0 and 2, while odd processor addresses access maps 1 and 3. A logical 1 causes processor addresses to sequentially access data within a bit map. The maps are accessed according to the value in the map mask register.

## CRT Controller Registers

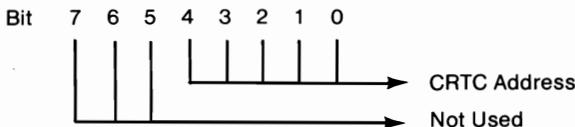
Name	Port	Index
Address Register	3?4	-
Horizontal Total	3?5	00
Horizontal Display End	3?5	01
Start Horizontal Blank	3?5	02
End Horizontal Blank	3?5	03
Start Horizontal Retrace	3?5	04
End Horizontal Retrace	3?5	05
Vertical Total	3?5	06
Overflow	3?5	07
Preset Row Scan	3?5	08
Max Scan Line	3?5	09
Cursor Start	3?5	0A
Cursor End	3?5	0B
Start Address High	3?5	0C
Start Address Low	3?5	0D
Cursor Location High	3?5	0E
Cursor Location Low	3?5	0F
Vertical Retrace Start	3?5	10
Light Pen High	3?5	10
Vertical Retrace End	3?5	11
Light Pen Low	3?5	11
Vertical Display End	3?5	12
Offset	3?5	13
Underline Location	3?5	14
Start Vertical Blank	3?5	15
End Vertical Blank	3?5	16
Mode Control	3?5	17
Line Compare	3?5	18

? = B in Monochrome Modes and D in Color Modes

### CRT Controller Address Register

The Address register is a pointer register located at hex 3B4 or hex 3D4. If an IBM Monochrome Display is attached to the adapter, address 3B4 is used. If a color display is attached to the adapter, address 3D4 is used. This register is loaded with a binary value that points to the CRT Controller data register where data is to be written. This value is referred to as "Index" in the table above.

### CRT Controller Address Register Format



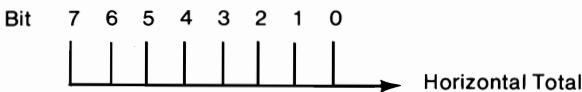
### Bit 0-Bit 4

CRT Controller Address Bits—A binary value pointing to the CRT Controller register where data is to be written.

### Horizontal Total Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 00. The processor output port address for this register is hex 3B5 or hex 3D5.

### Horizontal Total Register Format



This register defines the total number of characters in the horizontal scan interval including the retrace time. The value directly controls the period of the horizontal retrace output signal. An internal horizontal character counter counts character clock inputs to the CRT Controller, and all horizontal and vertical timings are based upon the horizontal register. Comparators are used to compare register values with horizontal character values to provide horizontal timings.

### Bit 0-Bit 7

Horizontal Total—The total number of characters less 2.

## Horizontal Display Enable End Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 01. The processor output port address for this register is hex 3B5 or hex 3D5.

**Horizontal Display Enable End Register Format**

Bit 7 6 5 4 3 2 1 0



Horizontal Display Enable End

This register defines the length of the horizontal display enable signal. It determines the number of displayed character positions per horizontal line.

**Bit 0-Bit 7**      Horizontal display enable end —A value one less than the total number of displayed characters.

## Start Horizontal Blanking Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 02. The processor output port address for this register is hex 3B5 or hex 3D5.

**Start Horizontal Blanking Register Format**

Bit 7 6 5 4 3 2 1 0



Start Vertical Blanking

This register determines when the horizontal blanking output signal becomes active. The row scan address and underline scan line decode outputs are multiplexed on the memory address outputs and cursor outputs respectively during the blanking interval. These outputs are latched external to the CRT Controller with the falling edge of the BLANK output signal. The row scan address and underline signals remain on the output signals for one character count beyond the end of the blanking signal.

### Bit 0-Bit 7

Start Horizontal Blanking—The horizontal blanking signal becomes active when the horizontal character counter reaches this value.

### End Horizontal Blanking Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 03. The processor output port address for this register is hex 3B5 or hex 3D5.

End Horizontal Blanking Register Format								
Bit	7	6	5	4	3	2	1	0
								

This register determines when the horizontal blanking output signal becomes inactive. The row scan address and underline scan line decode outputs are multiplexed on the memory address outputs and the cursor outputs respectively during the blanking interval. These outputs are latched external to the CRT Controller with the falling edge of the BLANK output signal. The row scan address and underline signals remain on the output signals for one character count beyond the end of the blanking signal.

### Bit 0-Bit 4

End Horizontal Blanking—A value equal to the five least significant bits of the horizontal character counter value at which time the horizontal blanking signal becomes inactive (logical 0). To obtain a blanking signal of width W, the following algorithm is used: Value of Start Blanking Register + Width of Blanking signal in character clock units = 5-bit result to be programmed into the End Horizontal Blanking Register.

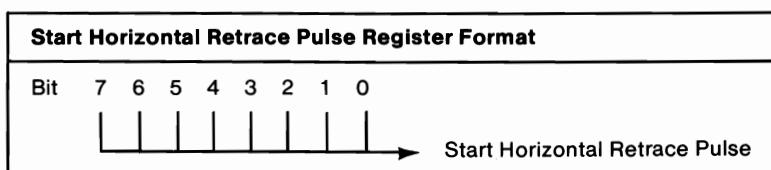
**Bit 5-Bit 6**

Display Enable Skew Control—These two bits determine the amount of display enable skew. Display enable skew control is required to provide sufficient time for the CRT Controller to access the display buffer to obtain a character and attribute code, access the character generator font, and then go through the Horizontal Pel Panning Register in the Attribute Controller. Each access requires the display enable signal to be skewed one character clock unit so that the video output is in synchronization with the horizontal and vertical retrace signals. The bit values and amount of skew are shown in the following table:

Bits	
6 5	
0 0	Zero character clock skew
0 1	One character clock skew
1 0	Two character clock skew
1 1	Three character clock skew

**Start Horizontal Retrace Pulse Register**

This is a write-only register pointed to when the value in the CRT Controller address register is hex 04. The processor output port address for this register is hex 3B5 or hex 3D5.

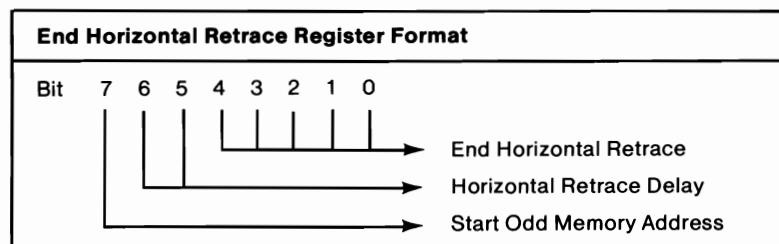


This register is used to center the screen horizontally, and to specify the character position at which the Horizontal Retrace Pulse becomes active.

**Bit 0-Bit 7** Start Horizontal Retrace Pulse—The value programmed is a binary count of the character position number at which the signal becomes active.

### End Horizontal Retrace Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 05. The processor output port address for this register is hex 3B5 or hex 3D5.



This register specifies the character position at which the Horizontal Retrace Pulse becomes inactive (logical 0).

**Bit 0-Bit 4** End Horizontal Retrace—A value equal to the five least significant bits of the horizontal character counter value at which time the horizontal retrace signal becomes inactive (logical 0). To obtain a retrace signal of width W, the following algorithm is used: Value of Start Retrace Register + width of horizontal retrace signal in character clock units = 5-bit result to be programmed into the End Horizontal Retrace Register.

**Bit 5-Bit 6** Horizontal Retrace Delay—These bits control the skew of the horizontal retrace signal. Binary 00 equals no Horizontal Retrace Delay. For some modes, it is necessary to provide a horizontal retrace signal that takes up the entire blanking interval. Some internal timings are generated by the falling edge of the horizontal retrace signal. To guarantee the signals are

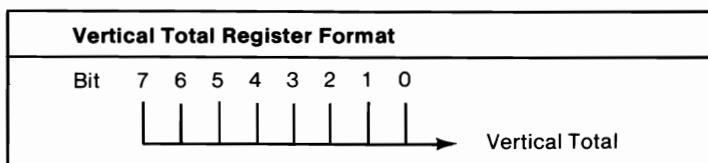
latched properly, the retrace signal is started before the end of the display enable signal, and then skewed several character clock times to provide the proper screen centering.

#### Bit 7

Start Odd/Even Memory Address—This bit controls whether the first CRT memory address output after a horizontal retrace begins with an even or an odd address. A logical 0 selects even addresses; a logical 1 selects odd addresses. This bit is used for horizontal pel panning applications. Generally, this bit should be set to a logical 0.

### Vertical Total Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 06. The processor output port address for this register is hex 3B5 or 3D5.



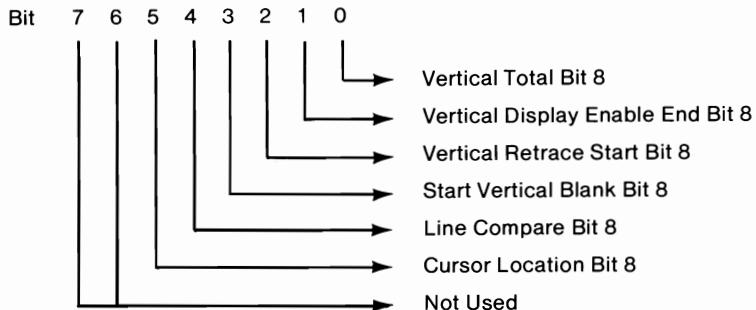
#### Bit 0-Bit 7

Vertical Total—This is the low-order eight bits of a nine-bit register. The binary value represents the number of horizontal raster scans on the CRT screen, including vertical retrace. The value in this register determines the period of the vertical retrace signal. Bit 8 of this register is contained in the CRT Controller Overflow Register hex 07 bit 0.

### CRT Controller Overflow Register

This is a write-only register pointed to when the value in the CRT Controller Address Register is hex 07. The processor output port address for this register is hex 3B5 or hex 3D5.

### CRTC Overflow Register Format



**Bit 0**      Vertical Total—Bit 8 of the Vertical Total register (index hex 06).

**Bit 1**      Vertical Display Enable End—Bit 8 of the Vertical Display Enable End register (index hex 12).

**Bit 2**      Vertical Retrace Start—Bit 8 of the Vertical Retrace Start register (index hex 10).

**Bit 3**      Start Vertical Blank—Bit 8 of the Start Vertical Blank register (index hex 15).

**Bit 4**      Line Compare—Bit 8 of the Line Compare register (index hex 18).

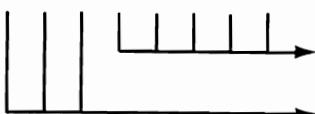
**Bit 5**      Cursor Location—Bit 8 of the Cursor Location register (index hex 0A).

### Preset Row Scan Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 08. The processor output port address for this register is hex 3B5 or hex 3D5.

### Preset Row Scan Register Format

Bit 7 6 5 4 3 2 1 0



Starting Row Scan Count after  
a Vertical Retrace  
Not Used

This register is used for pel scrolling.

#### Bit 0-Bit 4

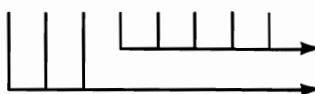
Preset Row Scan (Pel Scrolling)—This register specifies the starting row scan count after a vertical retrace. The row scan counter increments each horizontal retrace time until a maximum row scan occurs. At maximum row scan compare time the row scan is cleared (not preset).

### Maximum Scan Line Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 09. The processor output port address for this register is hex 3B5 or hex 3D5.

### Maximum Scan Line Register Format

Bit 7 6 5 4 3 2 1 0



Maximum Scan Line  
Not Used

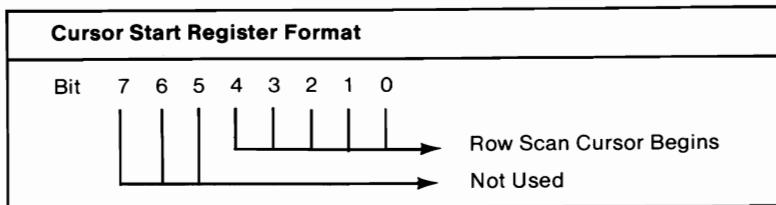
#### Bit 0-Bit 4

Maximum Scan Line—This register specifies the number of scan lines per character row. The number to be programmed is the maximum row scan number minus one.

### Cursor Start Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 0A. The processor output port

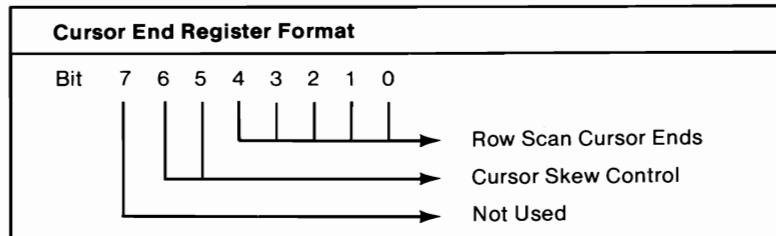
address for this register is hex 3B5 or hex 3D5.



- Bit 0-Bit 4** Cursor Start—This register specifies the row scan of a character line where the cursor is to begin. The number programmed should be one less than the starting cursor row scan.

### Cursor End Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 0B. The processor output port address for this register is hex 3B5 or hex 3D5.



- Bit 0-Bit 4** Cursor End—These bits specify the row scan where the cursor is to end.

- Bit 5-Bit 6** Cursor Skew—These bits control the skew of the cursor signal.

**Bits****6 5**

- 
- 0 0** Zero character clock skew
  - 0 1** One character clock skew
  - 1 0** Two character clock skew
  - 1 1** Three character clock skew

## Start Address High Register

This is a read/write register pointed to when the value in the CRT Controller address register is hex 0C. The processor input/output port address for this register is hex 3B5 or hex 3D5.

**Start Address High Register Format**

Bit    7    6    5    4    3    2    1    0



High Order Start Address

**Bit 0-Bit 7**

**Start Address High**—These are the high-order eight bits of the start address. The 16-bit value, from the high-order and low-order start address registers, is the first address after the vertical retrace on each screen refresh.

## Start Address Low Register

This is a read/write register pointed to when the value in the CRT Controller address register is hex 0D. The processor input/output port address for this register is hex 3B5 or hex 3D5.

**Start Address Low Register Format**

Bit    7    6    5    4    3    2    1    0

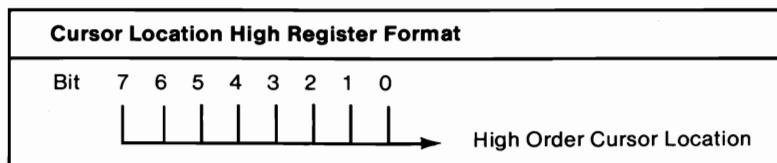


Low Order Start Address

**Bit 0-Bit 7** Start Address Low—These are the low-order 8 bits of the start address.

## Cursor Location High Register

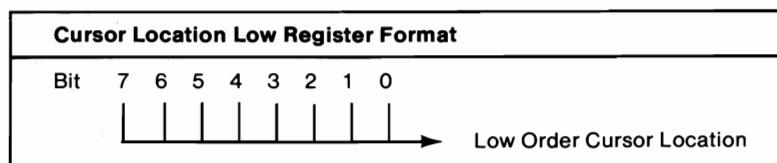
This is a read/write register pointed to when the value in the CRT Controller address register is hex 0E. The processor input/output port address for this register is hex 3B5 or hex 3D5.



**Bit 0-Bit 7** Cursor Location High—These are the high-order 8 bits of the cursor location.

## Cursor Location Low Register

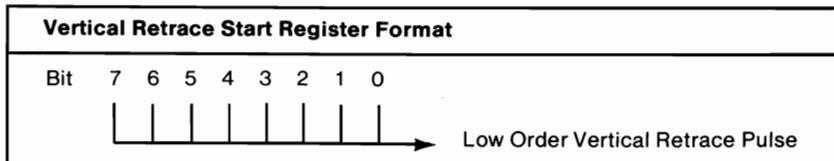
This is a read/write register pointed to when the value in the CRT Controller address register is hex 0F. The processor input/output port address for this register is hex 3B5 or Hex 3D5.



**Bit 0-Bit 7** Cursor Location Low— These are the low-order 8 bits of the cursor location.

## Vertical Retrace Start Register

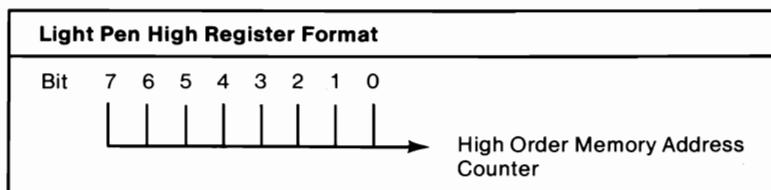
This is a write-only register pointed to when the value in the CRT Controller address register is hex 10. The processor output port address for this register is hex 3B5 or hex 3D5.



**Bit 0-Bit 7** Vertical Retrace Start—This is the low-order 8 bits of the vertical retrace pulse start position programmed in horizontal scan lines. Bit 8 is in the overflow register location hex 07.

## Light Pen High Register

This is a read-only register pointed to when the value in the CRT Controller address register is hex 10. The processor input port address for this register is hex 3B5 or hex 3D5.

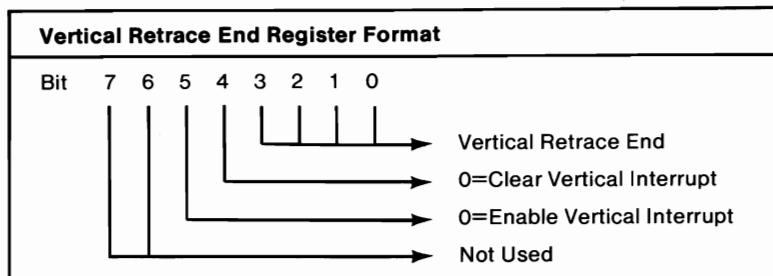


**Bit 0-Bit 7** Light Pen High—This is the high order 8 bits of the memory address counter at the time the light pen was triggered.

## Vertical Retrace End Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 11. The processor output port

address for this register is hex 3B5 or hex 3D5.



**Bit 0-Bit 3** Vertical Retrace End—These bits determine the horizontal scan count value when the vertical retrace output signal becomes inactive. The register is programmed in units of horizontal scan lines. To obtain a vertical retrace signal of width W, the following algorithm is used: Value of Start Vertical Retrace Register + width of vertical retrace signal in horizontal scan units = 4-bit result to be programmed into the End Horizontal Retrace Register.

**Bit 4** Clear Vertical Interrupt—A logical 0 will clear a vertical interrupt.

**Bit 5** Enable Vertical Interrupt—A logical 0 will enable vertical interrupt.

### Light Pen Low Register

This is a read-only register pointed to when the value in the CRT Controller address register is hex 11. The processor input port address for this register is hex 3B5 or 3D5.

### Light Pen Low Register Format

Bit 7 6 5 4 3 2 1 0



Low Order Memory Address Counter

### Bit 0-Bit 7

**Light Pen Low**—This is the low-order 8 bits of the memory address counter at the time the light pen was triggered.

### Vertical Display Enable End Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 12. The processor output port address for this register is hex 3B5 or hex 3D5.

### Vertical Display Enable End Register Format

Bit 7 6 5 4 3 2 1 0



Low Order Vertical Display Enable End

### Bit 0-Bit 7

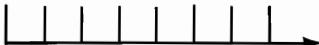
**Vertical Display Enable End**—These are the low-order 8 bits of the vertical display enable end position. This address specifies which scan line ends the active video area of the screen. Bit 8 is in the overflow register location hex 07.

### Offset Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 13. The processor output port address for this register is hex 3B5 or hex 3D5.

### Offset Register Format

Bit 7 6 5 4 3 2 1 0



Logical line width of the screen

#### Bit 0-Bit 7

**Offset**—This register specifies the logical line width of the screen. The starting memory address for the next character row is larger than the current character row by this amount. The Offset Register is programmed with a word address. Depending upon the method of clocking the CRT Controller, this word address is either a word or double word address.

### Underline Location Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 14. The processor output port address for this register is hex 3B5 or hex 3D5.

### Underline Location Register Format

Bit 7 6 5 4 3 2 1 0



Horizontal row scan where  
underline will occur

Not Used

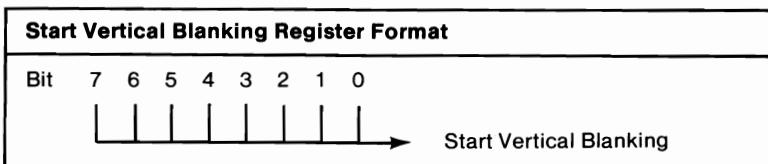
#### Bit 0-Bit 4

**Underline Location**—This register specifies the horizontal row scan on which underline will occur. The value programmed is one less than the scan line number desired.

### Start Vertical Blanking Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 15. The processor output port

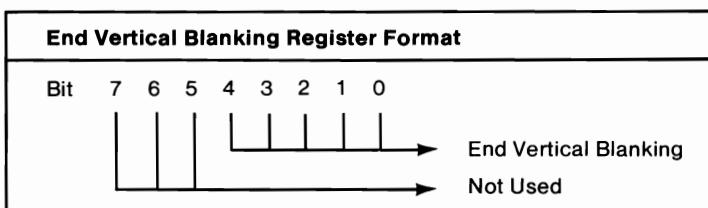
address for this register is hex 3B5 or hex 3D5.



**Bit 0-Bit 7** Start Vertical Blank—These are the low 8 bits of the horizontal scan line count, at which the vertical blanking signal becomes active. Bit 8 bit is in the overflow register hex 07.

### End Vertical Blanking Register

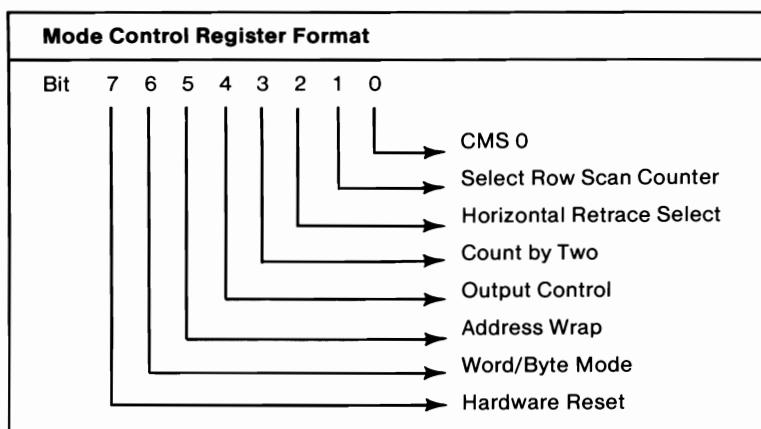
This is a write-only register pointed to when the value in the CRT Controller address register is hex 16. The processor output port address for this register is hex 3B5 or hex 3D5.



**Bit 0-Bit 4** End Vertical Blank—This register specifies the horizontal scan count value when the vertical blank output signal becomes inactive. The register is programmed in units of horizontal scan lines. To obtain a vertical blank signal of width W, the following algorithm is used: Value of Start Vertical Blank Register + width of vertical blank signal in horizontal scan units = 5-bit result to be programmed into the End Vertical Blank Register.

## Mode Control Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 17. The processor output port address for this register is hex 3B5 or hex 3D5.



### Bit 0

**Compatibility Mode Support**— When this bit is a logical 0, the row scan address bit 0 is substituted for memory address bit 13 during active display time. A logical 1 enables memory address bit 13 to appear on the memory address output bit 13 signal of the CRT Controller. The CRT Controller used on the IBM Color/Graphics Monitor Adapter is the 6845. The 6845 has 128 horizontal scan line address capability. To obtain 640 by 200 graphics resolution, the CRTC was programmed for 100 horizontal scan lines with 2 row scan addresses per character row. Row scan address bit 0 became the most significant address bit to the display buffer. Successive scan lines of the display image were displaced in memory by 8K bytes. This bit allows compatibility with the 6845 and Color Graphics APA modes of operation.

- Bit 1** Select Row Scan Counter—A logical 0 selects row scan counter bit 1 on MA 14 output pin. A logical 1 selects MA 14 counter bit on MA 14 output pin.
- Bit 2** Horizontal Retrace Select—This bit selects Horizontal Retrace or Horizontal Retrace divided by 2 as the clock that controls the vertical timing counter. This bit can be used to effectively double the vertical resolution capability of the CRT Controller. The vertical counter has a maximum resolution of 512 scan lines due to the 9-bit wide Vertical Total Register. If the vertical counter is clocked with the horizontal retrace divided by 2 clock, then the vertical resolution is doubled to 1024 horizontal scan lines. A logical 0 selects HRTC and a logical 1 selects HRTC divided by 2.
- Bit 3** Count By Two— When this bit is set to 0, the memory address counter is clocked with the character clock input. A logical 1 clocks the memory address counter with the character clock input divided by 2. This bit is used to create either a byte or word refresh address for the display buffer.
- Bit 4** Output Control—A logical 0 enables the module output drivers. A logical 1 forces all outputs into high impedance state.
- Bit 5** Address Wrap—This bit selects Memory Address counter bit MA 13 or bit MA 15, and it appears on the MA 0 output pin in the word address mode. If you are not in the word address mode, MA 0 counter output appears on the MA 0 output pin. A logical 1 selects MA 15. In odd/even mode, bit MA 13 should be selected when the 64K memory is installed on the board. Bit MA 15 should be selected when greater than 64K memory is installed. This function is used to implement Color Graphics Monitor Adapter compatibility.

### Bit 6

Word Mode or Byte Mode—When this bit is a logical 0, the Word Mode shifts all memory address counter bits down one bit, and the most significant bit of the counter appears on the least significant bit of the memory address outputs. See table below for address output details. A logical 1 selects the Byte Address mode.

Internal Memory Address Counter Wiring to the Output Multiplexer		
CRTC Out Pin	Byte Address Mode	Word Address Mode
MA 0/RFA 0	MA 0	MA 15 or MA 13
MA 1/RFA 1	MA 1	MA 0
MA 2/RFA 2	MA 2	MA 1
MA 3/RFA 3	MA 3	MA 2
*	*	*
*	*	*
*	*	*
MA 14/RS 3	MA 14	MA 13
MA 15/RS 4	MA 15	MA 14

### Bit 7

Hardware Reset—A logical 0 forces horizontal and vertical retrace to clear. A logical 1 forces horizontal and vertical retrace to be enabled.

## Line Compare Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 18. The processor output port address for this register is hex 3B5 or hex 3D5.

Line Compare Register Format									
Bit	7	6	5	4	3	2	1	0	
									Line Compare Target

### Bit 0-Bit 7

Line Compare—This register is the low-order 8 bits of the compare target. When the vertical

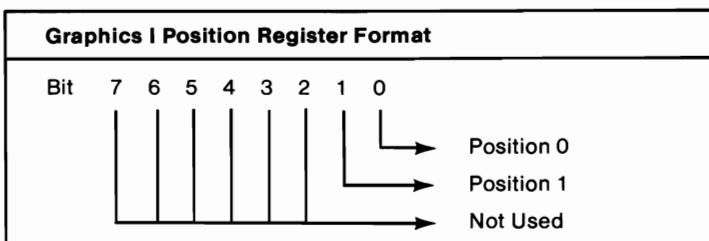
counter reaches this value, the internal start of the line counter is cleared. This allows an area of the screen to be immune to scrolling. Bit 8 of this register is in the overflow register hex 07.

# Graphics Controller Registers

Name	Port	Index
Graphics 1 Position	3CC	-
Graphics 2 Position	3CA	-
Graphics 1 & 2 Address	3CE	-
Set/Reset	3CF	00
Enable Set/Reset	3CF	01
Color Compare	3CF	02
Data Rotate	3CF	03
Read Map Select	3CF	04
Mode Register	3CF	05
Miscellaneous	3CF	06
Color Don't Care	3CF	07
Bit Mask	3CF	08

## Graphics 1 Position Register

This is a write-only register. The processor output port address for this register is hex 3CC.

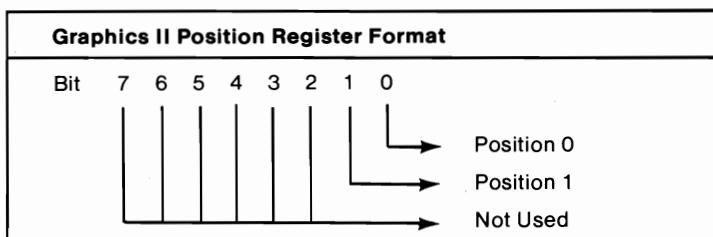


### Bit 0-Bit 1

**Position**—These 2 bits are binary encoded hierarchy bits for the graphics chips. The position register controls which 2 bits of the processor data bus each chip responds to. Graphics 1 must be programmed with a position register value of 0 for this card.

## Graphics 2 Position Register

This is a write-only register. The processor output port address for this register is hex 3CA.

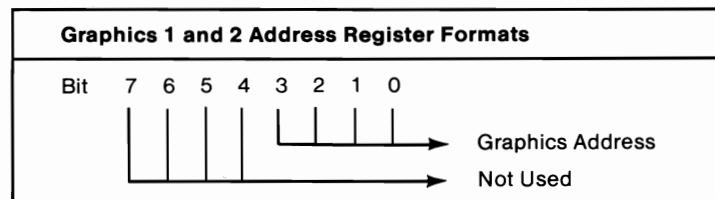


### Bit 0-Bit 1

**Position**—These 2 bits are binary encoded hierarchy bits for the graphics chips. The position register controls which 2 bits of the processor data bus to which each chip responds. Graphics 2 must be programmed with a position register value of 1 for this card.

## Graphics 1 and 2 Address Register

This is a write-only register and the processor output port address for this register is hex 3CE.

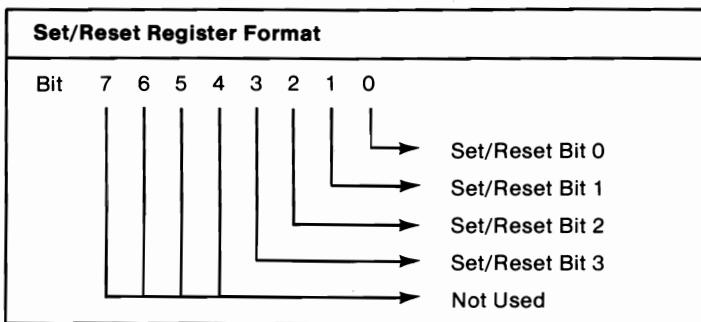


### Bit 0-Bit 3

**Graphics 1 and 2 Address Bits**—This output loads the address register in both graphics chips simultaneously. This register points to the data register of the graphics chips.

## **Set/Reset Register**

This is a write-only register pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 00 before writing can take place. The processor output port address for this register is hex 3CF.



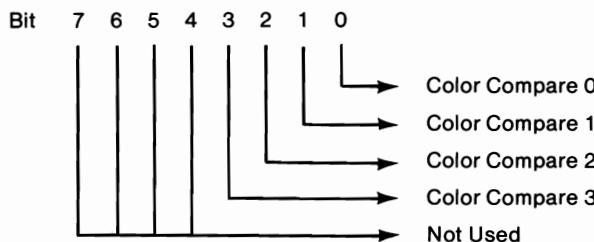
### **Bit 0-Bit 3**

**Set/Reset**—These bits represent the value written to the respective memory planes when the processor does a memory write with write mode 0 selected and set/reset mode is enabled.  
Set/Reset can be enabled on a plane by plane basis with separate OUT commands to the Set/Reset register.

## **Enable Set/Reset Register**

This is a write-only register and is pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 01 before writing can take place. The processor output port for this register is hex 3CF.

### Color Compare Register Format



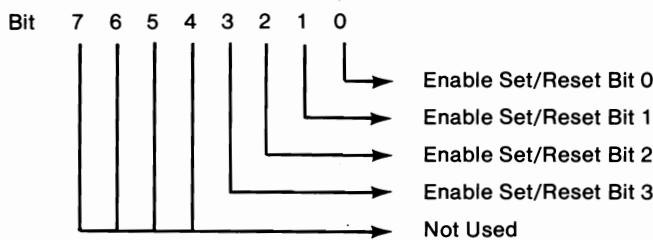
### Bit 0-Bit 3

**Enable Set/Reset**—These bits enable the set/reset function. The respective memory plane is written with the value of the Set/Reset register provided the write mode is 0. When write mode is 0 and Set/Reset is not enabled on a plane, that plane is written with the value of the processor data.

### Color Compare Register

This is a write-only register pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 02 before writing can take place. The processor output port address for this register is hex 3CF.

### Enable Set/Reset Register Format



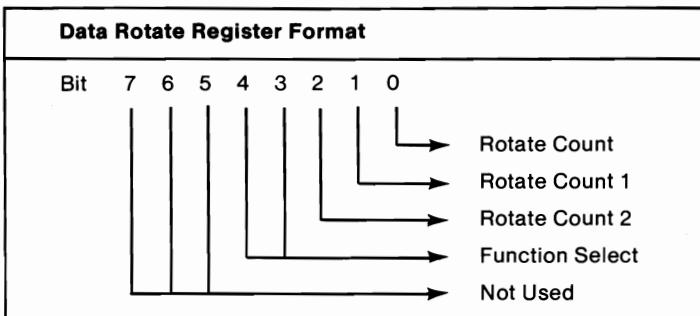
### Bit 0-Bit 3

**Color Compare**—These bits represent a 4 bit color value to be compared. If the processor sets

read mode 1 on the graphics chips, and does a memory read, the data returned from the memory cycle will be a 1 in each bit position where the 4 bit planes equal the color compare register.

## Data Rotate Register

This is a write-only register pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 03 before writing can take place. The processor output port address for this register is hex 3CF.



- Bit 0-Bit 2**      Rotate Count—These bits represent a binary encoded value of the number of positions to rotate the processor data bus during processor memory writes. This operation is done when the write mode is 0. To write unrotated data the processor must select a count of 0.
- Bit 3-Bit 4**      Function Select—Data written to memory can operate logically with data already in the processor latches. The bit functions are defined in the following table.

**Bits**

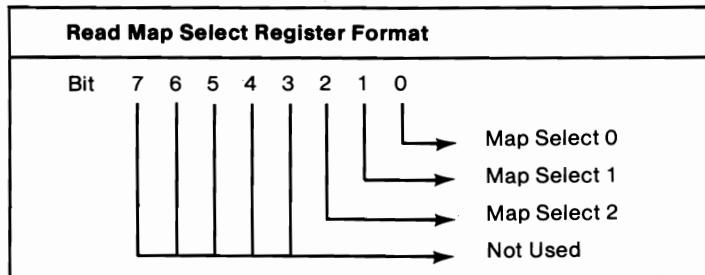
**4 3**

- 
- |            |                                |
|------------|--------------------------------|
| <b>0 0</b> | Data unmodified.               |
| <b>0 1</b> | Data AND'ed with latched data. |
| <b>1 0</b> | Data OR'ed with latched data.  |
| <b>1 1</b> | Data XOR'ed with latched data. |

Data may be any of the choices selected by the Write Mode Register except processor latches. If rotated data is selected, the rotate applies before the logical function.

### **Read Map Select Register**

This is a write-only register pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 04 before writing can take place. The processor output port address for this register is hex 3CF.

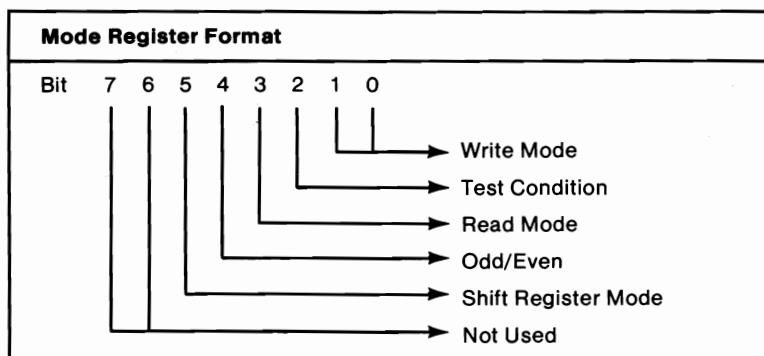


**Bit 0-Bit 2**      Map Select—These bits represent a binary encoded value of the memory plane number from which the processor reads data. This register has no effect on the color compare read mode described elsewhere in this section.

### **Mode Register**

This is a write-only register pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 05

before writing can take place. The processor output port address for this register is 3CF.



### Bit 0-Bit 1      Write Mode

**Bits**  
**1 0**

- |            |  |
|------------|--|
| <b>0 0</b> | Each memory plane is written with the processor data rotated by the number of counts in the rotate register, unless Set/Reset is enabled for the plane. Planes for which Set/Reset is enabled are written with 8 bits of the value contained in the Set/Reset register for that plane. |
| <b>0 1</b> | Each memory plane is written with the contents of the processor latches. These latches are loaded by a processor read operation.   |
| <b>1 0</b> | Memory plane $n$ (0 through 3) is filled with 8 bits of the value of data bit $n$ .  |
| <b>1 1</b> | Not Valid  |

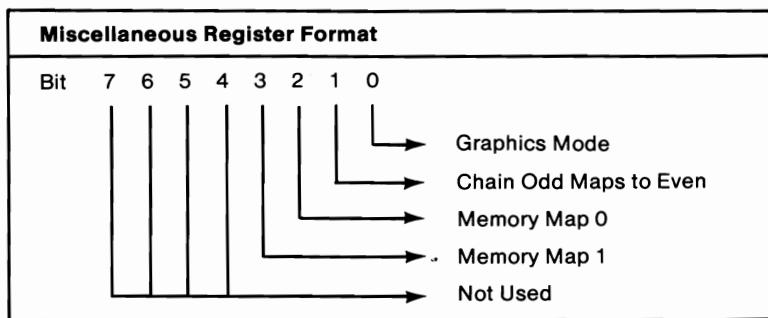
The logic function specified by the function select register also applies.

### Bit 2      Test Condition—A logical 1 directs graphics controller outputs to be placed in high impedance state for testing.

- |              |   |
|--------------|---|
| <b>Bit 3</b> | Read Mode—When this bit is a logical 0, the processor reads data from the memory plane selected by the read map select register. When this bit is a logical 1, the processor reads the results of the comparison of the 4 memory planes and the color compare register. |
| <b>Bit 4</b> | Odd/Even—A logical 1 selects the odd/even addressing mode, which is useful for emulation of the Color Graphics Monitor Adapter compatible modes. Normally the value here follows the value of the Memory Mode Register bit 3 of the Sequencer.                          |
| <b>Bit 5</b> | Shift Register—A logical 1 directs the shift registers on each graphics chip to format the serial data stream with even numbered bits on the even numbered maps and odd numbered bits on the odd maps.  |

## Miscellaneous Register

This is a write-only register pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 06 before writing can take place. The processor output port for this register is hex 3CF.



**Bit 0**      Graphics Mode—This bit controls alpha-mode addressing. A logical 1 selects graphics mode. When set to graphics mode, the character generator address latches are disabled.

**Bit 1**      Chain Odd Maps To Even Maps—When set to 1, this bit directs the processor address bit 0 to be replaced by a higher order bit and odd/even maps to be selected with odd/even values of the processor A0 bit, respectively.

**Bit 2-Bit 3**      Memory Map—These bits control the mapping of the regenerative buffer into the processor address space.

**Bits**

**3 2**

**0 0**      Hex A000 for 128K bytes.

**0 1**      Hex A000 for 64K bytes.

**1 0**      Hex B000 for 32K bytes

**1 1**      Hex B800 for 32K bytes.

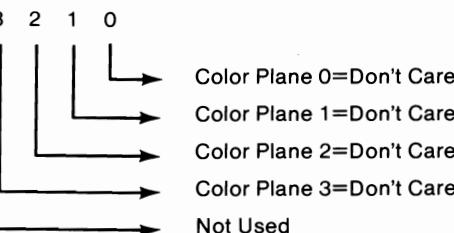
If the display adapter is mapped at address hex A000 for 128K bytes, no other adapter can be installed in the system.

### **Color Don't Care Register**

This is a write-only register and is pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 07 before writing can take place. The processor output port for this register is hex 3CF.

### Color Don't Care Register Format

Bit 7 6 5 4 3 2 1 0



- Bit 0**      Color Don't Care—Color plane 0=don't care when reading color compare when this bit is set to 1.
- Bit 1**      Color Don't Care—Color plane 1=don't care when reading color compare when this bit is set to 1.
- Bit 2**      Color Don't Care—Color plane 2=don't care when reading color compare when this bit is set to 1.
- Bit 3**      Color Don't Care—Color plane 3=don't care when reading color compare when this bit is set to 1.

### Bit Mask Register

This is a write-only register and is pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 08 before writing can take place. The processor output port for this register is hex 3CF.

### Bit Mask Register Format

Bit 7 6 5 4 3 2 1 0



0-Immune to change  
1-Unimpeded Writes

### **Bit 0-Bit 7**

**Bit Mask**—Any bit programmed to  $n$  causes the corresponding bit  $n$  in each bit plane to be immune to change provided that the location being written was the last location read by the processor. Bits programmed to a 1 allow unimpeded writes to the corresponding bits in the bit planes.

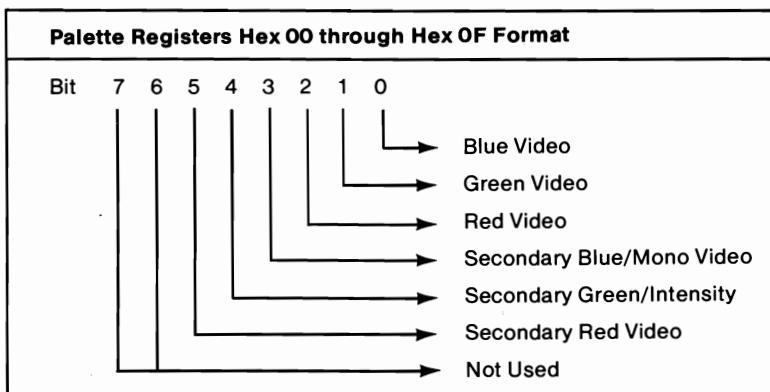
The bit mask applies to any data written by the processor (rotate, AND'ed, OR'ed, XOR'ed, DX, and S/R). To preserve bits using the bit mask, data must be latched internally by reading the location. When data is written to preserve the bits, the most current data in latches is written in those positions. The bit mask applies to all bit planes simultaneously.

## Attribute Controller Registers

Name	Port	Index
Address Register	3C0	-
Palette Registers	3C0	00-0F
Mode Control Register	3C0	10
Overscan Color Register	3C0	11
Color Plane Enable Register	3C0	12
Horizontal Pel Panning Register	3C0	13

### Attribute Address Register

This is a write-only register. The processor output port is hex 3C0.



#### Bit 0-Bit 4

**Attribute Address Bits**—The Address Register is a pointer register located at hex 3C0. This register is loaded with a binary value that points to the attribute data register where data is to be written. The Attribute Controller does not have an address bit input to control selection of the address and data registers. An internal address flip-flop controls selection of either the address or data registers. To initialize the flip-flop, an IOR instruction is issued to the Attribute Controller at address 3BA or 3DA. This clears the flip-flop, and selects the Address Register. After the Address Register has been loaded, the

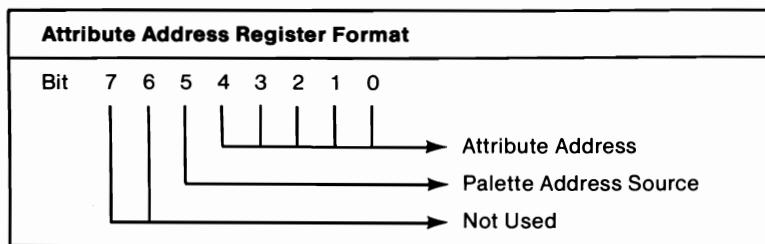
next OUT instruction loads the data register. The flip-flop toggles each time an OUT is issued to the Attribute Controller.

### Bit 5

Palette Address Source—When loading the color palette registers, bit 5 must be cleared to 0. To enable the memory data to access the color palette, bit 5 must be set to 1.

## Palette Register Hex 00 through Hex 0F

This is a write-only register. The processor output port is hex 3C0.

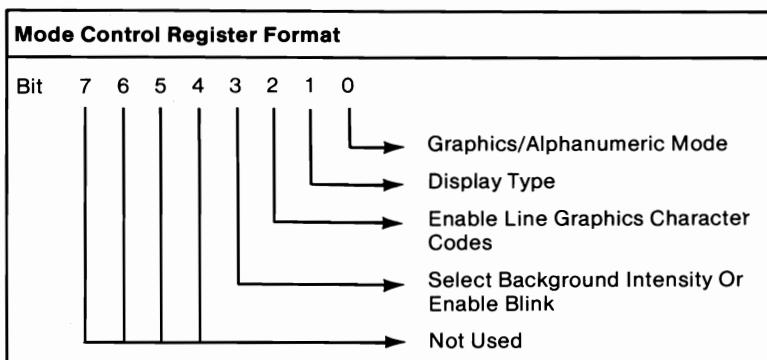


### Bit 0-Bit 5

Palette—These 6-bit registers allow a dynamic mapping between the text attribute or graphic color input value and the display color in the CRT screen. A logical 1 selects the appropriate color. A logical 0 de-selects. The color palette register should be modified only during the vertical retrace interval to avoid glitches in the displayed image. Note that some color monitors do not have an intensity input and only a maximum of eight colors are available. Monitors with four color inputs display sixteen colors, and monitors with six color inputs display 64 colors.

## Mode Control Register

This is a write-only register pointed to by the value in the Attribute address register. This value must be hex 10 before writing can take place. The processor output port address for this register is hex 3C0.



**Bit 0**      Graphics/Alphanumeric Mode—A logical 0 selects alphanumeric mode. A logical 1 selects graphics mode.

**Bit 1**      Monochrome Display/Color Display—A logical 0 selects color display attributes. A logical 1 selects IBM Monochrome Display attributes.

**Bit 2**      Enable Line Graphics Character Codes—When this bit is set to 0, the ninth dot will be the same as the background. A logical 1 enables the special line graphics character codes for the IBM Monochrome Display adapter. This bit when enabled forces the ninth dot of a line graphic character to be identical to the eighth dot of the character. The line graphics character codes for the Monochrome Display Adapter are Hex C0 through Hex DF.

For character fonts that do not utilize the line graphics character codes in the range of Hex C0

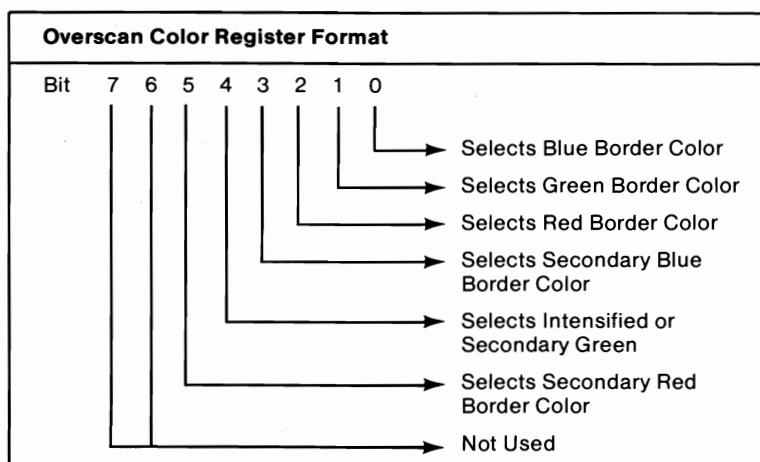
through Hex DF, bit 2 of this register should be a logical 0. Otherwise unwanted video information will be displayed on the CRT screen.

#### Bit 3

Enable Blink>Select Background Intensity—A logical 0 selects the background intensity of the attribute input. This mode was available on the Monochrome and Color Graphics adapters. A logical 1 enables the blink attribute in alphanumeric modes. This bit must also be set to 1 for blinking graphics modes.

### Overscan Color Register

This is a write-only register pointed to by the value in the Attribute address register. This value must be hex 11 before writing can take place. The processor output port address for this register is hex 3C0.

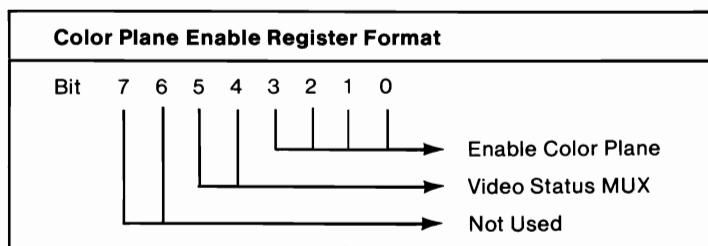


#### Bit 0-Bit 5

**Overscan Color**—This 6-bit register determines the overscan (border) color displayed on the CRT screen. For monochrome display this register should be set to a value of 0. A logical 1 selects the appropriate color.

## Color Plane Enable Register

This is a write-only register pointed to by the value in the Attribute address register. This value must be hex 12 before writing can take place. The processor output port address for this register is 3C0.



**Bit 0-Bit 3** Enable Color Plane—Writing a logical 1 in any of bits 0 through 3 enables the respective display memory color plane.

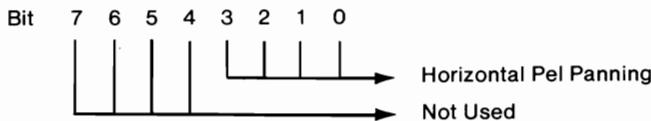
**Bit 4-Bit 5** Video Status MUX—Selects two of the six color outputs to be available on the status port. The following table illustrates the combinations available and the color output wiring.

COLOR PLANE ENABLE REGISTER		INPUT STATUS REGISTER ONE	
Bit 5	Bit 4	Bit 5	Bit 4
0	0	Red	Blue
0	1	Secondary Blue	Green
1	0	Secondary Red	Secondary Green
1	1	Not Used	Not Used

## Horizontal Pel Panning Register

This is a write-only register pointed to by the value in the Attribute address register. This value must be hex 12 before writing can take place. The processor output port address for this register is hex 3C0.

### Horizontal Pel Panning Register Format



#### Bit 0-Bit 3

**Horizontal Pel Panning**—This 4 bit register selects the number of picture elements (pels) to shift the video data horizontally to the left. Pel panning is available in both A/N and APA modes. In Monochrome A/N mode, the image can be shifted a maximum of 9 pels. In all other A/N and APA modes, the image can be shifted a maximum of 8 pels. The sequence for shifting the image is given below:

9 pels/character : 8, 0, 1, 2, 3, 4, 5, 6, 7  
(Monochrome A/N mode only)

8 pels/character : 0, 1, 2, 3, 4, 5, 6, 7 (All other Modes)

# **Programming Considerations**

## **Programming the Registers**

Each of the LSI devices has an address register and a number of data registers. The address register serves as a pointer to the other registers on the LSI device. It is a write-only register that is loaded by the processor by executing an 'OUT' instruction to its I/O address with the index of the selected data register.

The data registers on each LSI device are accessed through a common I/O address. They are distinguished by the pointer (index) in the address register. To write to a data register, the address register is loaded with the index of the appropriate data register, then the selected data register is loaded by executing an 'OUT' instruction to the common I/O address.

The external registers that are not part of an LSI device and the Graphics I and II registers are not accessed through an address register; they are written to directly.

The following tables define the values that are loaded into the registers by BIOS to support the different modes of operation supported by this adapter.

Register			Mode of Operation																	
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F*	10*	0*	1*	2*	3*
Miscellaneous	3C2	-	23	23	23	23	23	23	23	A6	23	23	A2	A7	A2	A7	A7	A7	A7	A7
Feature Cntrl	3?A	-	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
Input Stat 0	3C2	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
Input Stat 1	3?2	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-

? = B in monochrome modes      ? = D in color modes

\*Values for these modes when the IBM Enhanced Color Display is attached

#Values for these modes when greater than 64K Graphics Memory is installed

## External Registers

Register			Mode of Operation																	
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F*	10*	0*	1*	2*	3*
Seq Address	3C4	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
Reset	3C5	00	03	03	03	03	03	03	03	03	03	03	03	03	03	03	03	03	03	03
Clock Mode	3C5	01	0B	0B	01	01	0B	0B	01	00	0B	01	05	05	01	01	0B	0B	01	01
Map Mask	3C5	02	03	03	03	03	03	03	01	03	0F	0F	0F	0F	0F	0F	03	03	03	03
Char Gen Sel	3C5	03	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
Memory Mode	3C5	04	03	03	03	03	02	02	06	03	06	06	00	00	06	06	03	03	03	03

\*Values for these modes when the IBM Enhanced Color Display is attached

#Values for these modes when greater than 64K Graphics Memory is installed

## Sequencer Registers

Register			Mode of Operation																	
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F*	10*	0*	1*	2*	3*
Address Reg	3?4	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
Horiz Total	3?5	00	37	37	70	70	37	37	70	60	37	70	60	5B	60	5B	2D	2D	5B	5B
Hrz Disp End	3?5	01	27	27	4F	4F	27	27	4F	4F	27	4F	4F	4F	4F	4F	27	27	4F	4F
Strt Hzr Blk	3?5	02	2D	2D	5C	5C	2D	2D	59	56	2D	56	56	53	56	53	2B	2B	53	53
End Hzr Blk	3?5	03	37	37	2F	2F	37	37	2D	3A	37	2D	1A	17	3A	37	2D	2D	37	37
Strt Hzr Retr	3?5	04	31	31	5F	5F	30	30	5E	51	30	5E	50	50	50	52	2B	2B	51	51
End Hzr Retr	3?5	05	15	15	07	07	14	14	06	60	14	06	E0	BA	60	00	6D	6D	5B	5B
Vert Total	3?5	06	04	04	04	04	04	04	04	70	04	04	70	6C	70	6C	6C	6C	6C	6C
Overflow	3?5	07	11	11	11	11	11	11	11	1F	11	11	1F	1F	1F	1F	1F	1F	1F	1F
Preset Row SC	3?5	08	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
Max Scan Line	3?5	09	07	07	07	07	01	01	01	0D	00	00	00	00	00	00	0D	0D	0D	0D
Cursor Start	3?5	0A	06	06	06	06	00	00	00	0B	00	00	00	00	00	00	0B	0B	0B	0B
Cursor End	3?5	0B	07	07	07	07	00	00	00	0C	00	00	00	00	00	00	0C	0C	0C	0C
Strt Addr Hi	3?5	0C	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
Strt Addr Lo	3?5	0D	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-

? = B in monochrome modes      ? = D in color modes

\*Values for these modes when the IBM Enhanced Color Display is attached

:Values for these modes when greater than 64K Graphics Memory is installed

## CRT Controller Registers (1 of 2)

Register			Mode of Operation																	
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F*	10*	0*	1*	2*	3*
Cursor LC Hi	3?5	0E	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
Cursor LC Low	3?5	0F	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
Vrt Retr Strt	3?5	10	E1	E1	E1	E1	E1	E1	E0	5E	E1	E0	5E	5E	5E	5E	5E	5E	5E	5E
Light Pen Hi	3?5	10	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
Vert Retr End	3?5	11	24	24	24	24	24	24	23	2E	24	23	2E	2B	2E	2B	2B	2B	2B	2B
Light Pen Low	3?5	11	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
Vrt Disp End	3?5	12	C7	C7	C7	C7	C7	C7	C7	5D	C7	C7	5D	5D	5D	5D	5D	5D	5D	5D
Offset	3?5	13	14	14	28	28	14	14	28	28	14	28	14	14	28	28	14	14	28	28
Underline Loc	3?5	14	08	08	08	08	00	00	00	0D	00	00	0D	0F	0D	0F	0F	0F	0F	0F
Strt Vert Blk	3?5	15	E0	E0	E0	E0	E0	E0	DF	5E	E0	DF	5E	5F	5E	5F	5E	5E	5E	5E
End Vert Blk	3?5	16	F0	F0	F0	F0	F0	F0	EF	6E	F0	EF	6E	0A	6E	0A	0A	0A	0A	0A
Mode Control	3?5	17	A3	A3	A3	A3	A2	A2	C2	A3	E3	E3	8B	8B	E3	E3	A3	A3	A3	A3
Line Compare	3?5	18	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
?= B in monochrome modes      ?= D in color modes																				
*Values for these modes when the IBM Enhanced Color Display is attached																				
:Values for these modes when greater than 64K Graphics Memory is installed																				

## CRT Controller Registers (2 of 2)

Register			Mode of Operation																	
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F%	10%	0*	1*	2*	3*
Grphx I Pos	3CC	-	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
Grphx II Pos	3CA	-	01	01	01	01	01	01	01	01	01	01	01	01	01	01	01	01	01	
Grphx I II AD	3CE	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	
Set Reset	3CF	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
Enable S/R	3CF	01	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
Color Compare	3CF	02	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
Data Rotate	3CF	03	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
Read Map Sel	3CF	04	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
Mode Register	3CF	05	10	10	10	10	30	30	00	10	00	00	10	10	00	00	10	10	10	
Miscellaneous	3CF	06	0E	0E	0E	0E	0F	0F	0D	0A	05	05	07	07	05	05	0E	0E	0E	
Color No Care	3CF	07	00	00	00	00	00	00	00	00	0F	0F	0F	0F	0F	0F	00	00	00	
Bit Mask	3CF	08	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	
*Values for these modes when the IBM Enhanced Color Display is attached																				
:Values for these modes when greater than 64K Graphics Memory is installed																				

## Graphics SI Registers

Register			Mode of Operation																		
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F#	10#	0*	1*	2*	3*	
Address	3?A	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	
Palette	3C0	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
Palette	3C0	01	01	01	01	01	13	13	17	08	01	01	08	01	08	01	01	01	01	01	
Palette	3C0	02	02	02	02	02	15	15	17	08	02	02	00	00	00	02	02	02	02	02	
Palette	3C0	03	03	03	03	03	17	17	17	08	03	03	00	00	00	03	03	03	03	03	
Palette	3C0	04	04	04	04	04	04	02	02	17	08	04	04	18	04	18	04	04	04	04	
Palette	3C0	05	05	05	05	05	05	04	04	17	08	05	05	18	07	18	05	05	05	05	
Palette	3C0	06	06	06	06	06	06	06	06	17	08	06	06	00	00	00	06	14	14	14	
Palette	3C0	07	07	07	07	07	07	07	07	17	08	07	07	00	00	00	07	07	07	07	
Palette	3C0	08	10	10	10	10	10	10	17	10	10	10	00	00	00	38	38	38	38	38	
Palette	3C0	09	11	11	11	11	11	11	11	17	18	11	11	08	01	08	39	39	39	39	
Palette	3C0	0A	12	12	12	12	12	12	12	17	18	12	12	00	00	00	3A	3A	3A	3A	
Palette	3C0	0B	13	13	13	13	13	13	13	17	18	13	13	00	00	00	3B	3B	3B	3B	

? = B in monochrome modes      ? = D in color modes

\*Values for these modes when the IBM Enhanced Color Display is attached

:Values for these modes when greater than 64K Graphics Memory is installed

## Attribute Registers (1 of 2)

Register			Mode of Operation																	
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F*	10*	0*	1*	2*	3*
Palette	3C0	0C	14	14	14	14	14	14	17	18	14	14	00	04	00	3C	3C	3C	3C	3C
Palette	3C0	0D	15	15	15	15	15	15	17	18	15	15	18	07	18	3D	3D	3D	3D	3D
Palette	3C0	0E	16	16	16	16	16	16	17	18	16	16	00	00	00	3E	3E	3E	3E	3E
Palette	3C0	0F	17	17	17	17	17	17	18	17	17	00	00	00	3F	3F	3F	3F	3F	
Mode Control	3C0	10	08	08	08	08	01	01	01	0E	01	01	0B	0B	0B	01	08	08	08	08
Overscan	3C0	11	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
Color Plane	3C0	12	0F	0F	0F	0F	03	03	01	0F	0F	0F	05	05	05	0F	0F	0F	0F	0F
Hz Panning	3C0	13	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00

\*Values for these modes when the IBM Enhanced Color Display is attached

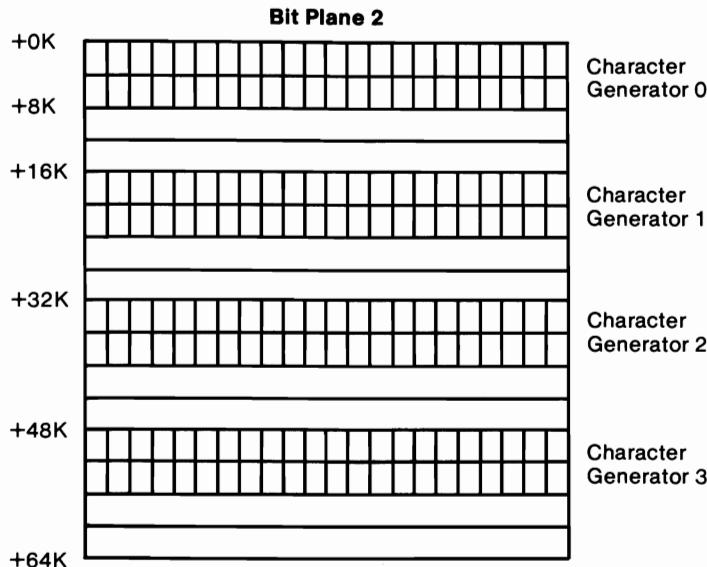
:Values for these modes when greater than 64K Graphics Memory is installed

## Attribute Registers (2 of 2)

## RAM Loadable Character Generator

The character generator on the adapter is RAM loadable and can support characters up to 32 scan lines high. Two character generators are stored within the BIOS and one is automatically loaded into the RAM by the BIOS when an alphanumeric mode is selected. The Character Map Select Register can be programmed to define the function of bit 3 of the attribute byte to be a character generator switch. This allows the user to select between any two character sets residing in bit plane 2. This effectively gives the user access to 512 characters instead of 256. character tables may be loaded off line. The adapter must have 128K bytes of storage to support this function. Up to four tables can be loaded with 256K of graphics memory installed.

The structure of the character tables is described in the following figure. The character generator is in bit plane 2 and must be protected using the map mask function.



The following figure illustrates the structure of each character pattern. If the CRT controller is programmed to generate  $n$  row

scans, then  $n$  bytes must be filled in for each character in the character generator. The example assumes eight row scans per character.

Address	Byte Image	Data
CC * 32 + 0		18H
1		3EH
2		66H
3		66H
4		7EH
5		66H
6		66H
7		66H

CC = Value of the character code. For example, 41H in the case of an ASCII "A".

## Creating a 512 Character Set

This section describes how to create a 512 character set on the IBM Color Display. Note that only 256 characters can be printed on the printer. This is a special application which the Enhanced Graphics Adapter will support. The 9 by 14 characters will be displayed when attribute bit 3 is a logical 0, and the IBM Color/Graphics Monitor Adapter 8 by 8 characters will be displayed when the attribute bit 3 is a logical 1. This example is for demonstrative purposes only. The assembly language routine for creating 512 characters is given below. Debug 2.0 was used for this example. The starting assembly address is 100 and the character string is stored in location 200. This function requires 128K or more of graphics memory.

```

a100
mov ax,1102 ;load 8x8 character font in character
mov bl,02 ;generator number 2
int 10

mov ax,1103 ;select 512 character operation
mov bl,08 ;if attribute bit 3=1 use 8x8 font
int 10 ;if attribute bit 3=0 use 9x14 font

mov ax,1000 ;set color plane enable to 7H to disable
mov bx,0712 ;attribute bit 3 in the color palette
int 10 ;lookup table

mov ax,1301 ;write char. string with attribute bit 3=1
mov bx,000F ;cx = character string length
mov cx,003A ;write character on line 22 of display
mov dx,1600 ;pointer to character string location
mov bp,0200
push cs
pop es
int 10

mov ax ,1301 ;write char. string with attribute bit 3=0
mov bx,0007 ;cx = character string length
mov cx,003A ;write character on line 23 of display
mov dx,1700 ;pointer to character string location
mov bp,0200
push cs
pop es
int 10
int 3

a200 db "This character string is used to show 512
characters"

```

## Creating an 80 by 43 Alphanumeric Mode

The following examples show how to create 80 column by 43 row, both alphanumeric and graphics, images on the IBM Monochrome Display. The BIOS Interface supports an 80 column by *n* row display by using the character generator load routine call. The print screen routine must be revectored to

handle the additional character rows on the screen. The assembly language required for both an alphanumeric and a graphics screen is shown below.

```
mov al,7          ;Monochrome alphanumeric mode
int 10           ;video interrupt call
mov ax,1112       ;character generator BIOS routine
mov bl,0          ;load 8 by 8 double dot character font
int 10           ;video interrupt call
mov ax,1200       ;alternate screen routine
move bl,20        ;select alternate print screen routine
int 10           ;video interrupt call
int 3

mov ax,f          ;Monochrome graphic mode
int 10           ;video interrupt call
mov ax,1123       ;character generator BIOS routine
mov bl,0          ;load 8 by 8 double dot character font
mov dl,2B         ;43 character rows
int 10           ;video interrupt call
mov ax,1200       ;alternate screen routine
mov bl,20         ;alternate print screen routine
int 10           ;video interrupt call
int 3
```

## Vertical Interrupt Feature

The Enhanced Graphics Adapter can be programmed to create an interrupt each time the vertical display refresh time has ended.

An interrupt handler routine must be written by the application to take advantage of this feature. The CRT Vertical interrupt is on IRQ2. The CPU can poll the Enhanced Graphics Adapter Input Status Register 0 (bit 7) to determine whether the CRTC caused the interrupt to occur.

The Vertical Retrace End Register (11H) in the CRT controller contains two bits which are used to control the interrupt circuitry. The remaining bits must be output as per the value in the mode table.

**Bit 5** Enable Vertical Interrupt—A logical 0 will enable vertical interrupt.

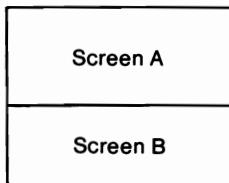
**Bit 4** Clear Vertical Interrupt—A logical 0 will clear a vertical interrupt.

The sequence of events which occur in an interrupt handler are outlined below.

1. Clear IRQ latch and enable driver
2. Enable IRQ latch
3. Wait for vertical interrupt
4. Poll Interrupt Status Register 0 to determine if CRTC has caused the interrupt
5. If CRTC interrupt, then clear IRQ latch; if not, then branch to next interrupt handler.
6. Enable IRQ latch
7. Update Enhanced Graphics Adapter during vertical blanking interval
8. Wait for next vertical interrupt

## Creating a Split Screen

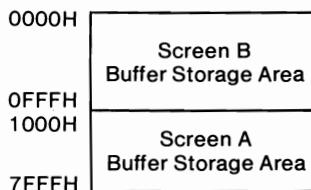
The Enhanced Graphics Adapter hardware supports an alphanumeric mode dual screen display. The top portion of the screen is designated as screen A, and the bottom portion of the screen is designated as screen B as per the following figure.



**Dual Screen Definition**

The following figure shows the screen mapping for a system containing a 32K byte alphanumeric storage buffer. Note that the Enhanced Graphics Adapter has a 32K byte storage buffer in alphanumeric mode. Information displayed on screen A is

defined by the start address high and low registers (0CH and 0DH) of the CRTC. Information displayed on screen B always begins at address 0000H.



### **Screen Mapping Within the Display Buffer Address Space**

The Line Compare Register (18H) of the CRT Controller is utilized to perform the split screen function. The CRTC has an internal horizontal scan counter, and logic which compares the horizontal scan counter value to the Line Compare Register value and clears the memory address generator when a compare occurs. The linear address generator then sequentially addresses the display buffer starting at location zero, and each subsequent row address is determined by the 16 bit addition of the start of line latch and the offset register.

Screen B can be smoothly scrolled onto the CRT screen by updating the Line compare in synchronization with the vertical retrace signal. The information on screen B is immune from scrolling operations which utilize the Start Address High and Low registers to scroll through the Screen A address map.

## **Compatibility Issues**

The CRT Controller on the IBM Enhanced Graphics Adapter is a custom design, and is different than the 6845 controller used on the IBM Monochrome Monitor Adapter and the IBM Color/Graphics Monitor Adapter. It should be noted that several CRTC register addresses differ between the adapters. The following figure illustrates the registers which do not map directly across the two controllers.

<b>Register</b>	<b>6485 Function</b>	<b>EGA CRTC Function</b>
02H	Start Horiz. Retrace	Start Horiz. Blanking
03H	End Horiz. Retrace	End Horiz. Blanking
04H	Vertical Total	Start Horiz. Retrace
05H	Vertical Total Adjust	End Horiz. Retrace
06H	Vertical Displayed	Vertical Total
07H	Vertical Sync Position	Overflow
08H	Interlace Mode and Skew	Preset Row Scan

Existing applications which utilize the BIOS interface will generally be compatible with the Enhanced Graphics Adapter.

Horizontal screen centering was required on the IBM Color/Graphics Monitor Adapter in order to center the screen when generating composite video. This was done through the Horizontal Sync Position Register. Since the Enhanced Graphics Adapter does not support a composite video monitor, programs which do screen centering may cause loss of the screen image if centering is attempted.

The Enhanced Graphics Adapter offers a wider variety of displayable monochrome character attributes than the IBM Monochrome Display Adapter. Some attribute values may display differently between the two Adapters. The values listed in the table below, in any combinations with the blink and intensity attributes, will display identically.

<b>Background R G B</b>	<b>Foreground R G B</b>	<b>Function</b>
0 0 0	0 0 0	Non-Display
0 0 0	0 0 1	Underline
0 0 0	1 1 1	White Character/Black Background
1 1 1	0 0 0	Reverse Video

Software which explicitly addresses 3D8 (Mode Select Register) or 3D9 (Color Select Register) on the Color Graphics Monitor Adapter may produce different results on the Enhanced Graphics Adapter. For example, blinking which is disabled by writing to 3D8 on the Color Graphics Adapter will not be disabled on the Enhanced Graphics Adapter.

# Interface

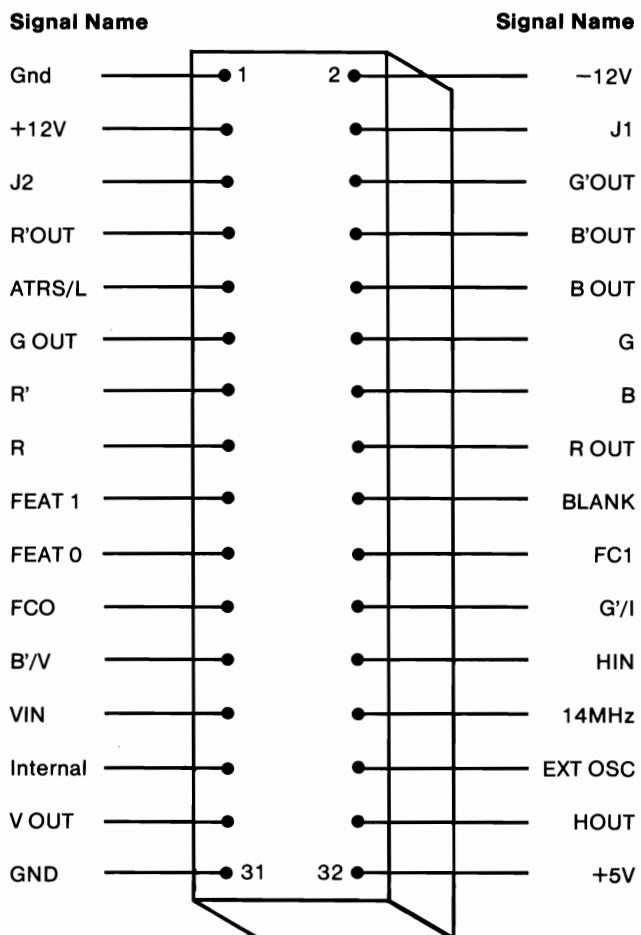
## Feature Connector

The following is a description of the Enhanced Graphics Adapter feature connector. Note that signals coming from the Enhanced Graphics Adapter are labeled "inputs" and the signals coming to the Enhanced Graphics Adapter through the feature connector are labeled "outputs".

Signal	Description
J2	This pin is connected to auxiliary jack 2 on the rear panel of the adapter.
R'OUT	Secondary red output
ATRS/L	Attribute shift load. This signal controls the serialization of the video information. The shift register parallel loads at the dot clock leading edge when this signal is low.
G OUT	Primary green output
R'	Secondary red input
R	Primary red input
FC1	This signal is input from bit 1 (Feature Control Bit 1) of the Feature Control Register.
FC0	This signal is input from bit 0 (Feature Control Bit 0) of the Feature control Register.
FEAT 0	This signal is output to bit 5 (Feature Code 0) of Input Status Register 0.
B'/V	Secondary blue input/Monochrome video
VIN	Vertical retrace input

<b>Internal</b>	This signal is output to bit 4 (Disable Internal Video Drivers) of the Miscellaneous Output Register.
<b>V OUT</b>	Vertical retrace output
<b>J1</b>	This pin is connected to auxiliary jack 1 on the rear panel of the adapter.
<b>G'OUT</b>	Secondary green output
<b>B'OUT</b>	Secondary blue output
<b>B OUT</b>	Blue output
<b>G</b>	Green input
<b>B</b>	Blue input
<b>R OUT</b>	Red output
<b>BLANK</b>	This is a composite horizontal and vertical blanking signal from the CRTC.
<b>FEAT 1</b>	This signal is output to bit 6 (Feature Code 1) of Input Status Register 0.
<b>G'/I</b>	Secondary green/Intensity input
<b>HIN</b>	Horizontal retrace input from the CRTC
<b>14MHZ</b>	14 MHz signal from the system board
<b>EXT OSC</b>	External dot clock output
<b>HOUT</b>	Horizontal retrace output

The following figure shows the layout and pin numbering of the feature connector.

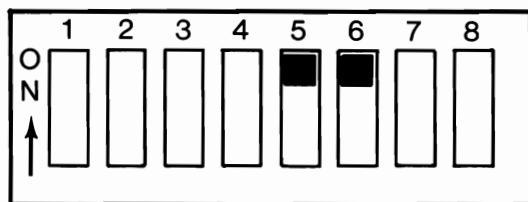


**Feature Connector Diagram**

# Specifications

## System Board Switches

The following figure shows the proper system board DIP switch settings for the IBM Enhanced Graphics Adapter when used with the Personal Computer and the Personal Computer XT. The switch block locations are illustrated in the Technical Reference Manual "System Board Component Diagram". The Personal Computer has two DIP switch blocks; the switch settings shown pertain to DIP Switch Block 1. The Personal Computer XT has one DIP switch block.

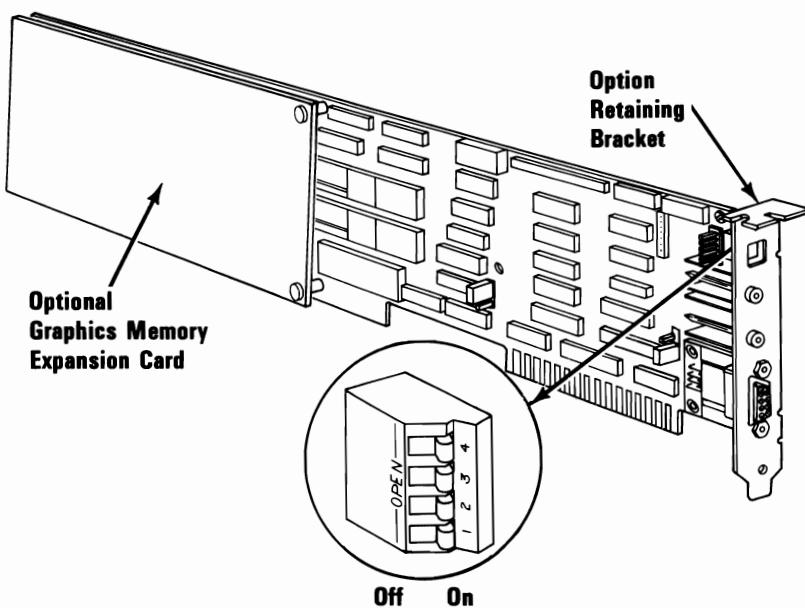


**Switch Block (1)**

**Note:** The DIP switches must be set as shown whenever the IBM Enhanced Graphics Adapter is installed, regardless of display type. This is true even when a second display adapter is installed in the system.

## Configuration Switches

The following diagram shows the location and orientation of the configuration switches on the Enhanced Graphics Adapter.



## Configuration Switch Settings

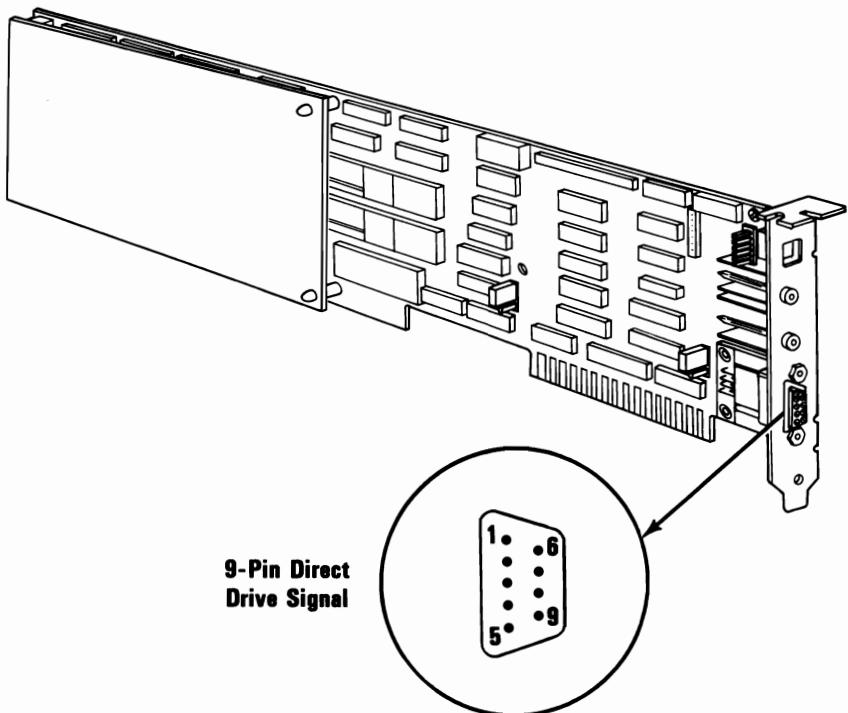
The configuration switches on the Enhanced Graphics Adapter determine the type of display support the adapter provides, as follows:

Switch Settings for Enhanced Graphics Adapter as Primary Display Adapter					Configuration		
SW1	SW2	SW3	SW4		Enhanced Adapter	Monochrome Adapter	Color/Graphics Adapter
On	Off	Off	On	Color Display 40x25	Secondary	—	
Off	Off	Off	On	Color Display 80x25	Secondary	—	
On	On	On	Off	Enhanced Display Emulation Mode	Secondary	—	
Off	On	On	Off	Enhanced Display Hi Res Mode	Secondary	—	
On	Off	On	Off	Monochrome	—	Secondary 40x25	
Off	Off	On	Off	Monochrome	—	Secondary 80x25	

**Switch Settings for Enhanced Graphics Adapter  
as Secondary Display Adapter**

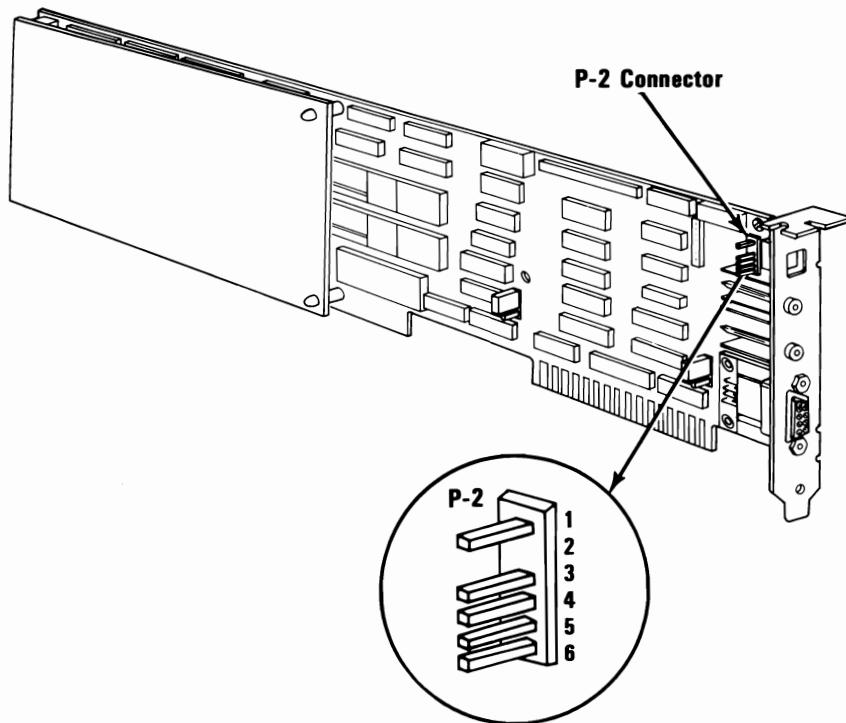
SW1	SW2	SW3	SW4	Configuration		
				Enhanced Adapter	Monochrome Adapter	Color/Graphics Adapter
On	On	On	On	Color Display 40x25	Primary	-
Off	On	On	On	Color Display 80x25	Primary	-
On	Off	On	On	Enhanced Display Emulation Mode	Primary	-
Off	Off	On	On	Enhanced Display Hi Res Mode	Primary	-
On	On	Off	On	Monochrome	-	Primary 40x25
Off	On	Off	On	Monochrome	-	Primary 80x25

# Direct Drive Connector



	Signal Name - Description	Pin	
Direct Drive Display	Ground	1	Enhanced Graphics Adapter
	Secondary Red	2	
	Primary Red	3	
	Primary Green	4	
	Primary Blue	5	
	Secondary Green/Intensity	6	
	Secondary Blue/Mono Video	7	
	Horizontal Retrace	8	
	Vertical Retrace	9	

# Light Pen Interface



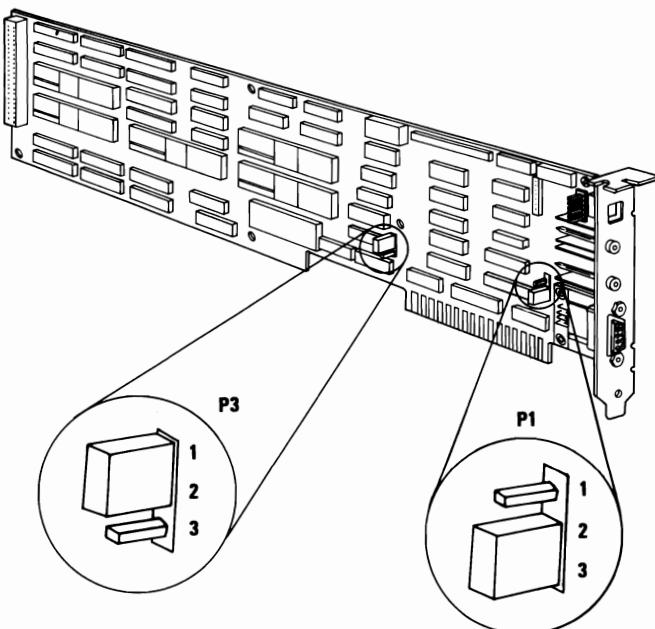
	P-2 Connector	Pin	
Light Pen Attachment	+Light Pen Input	1	Enhanced Graphics Adapter
	Not used	2	
	+Light Pen Switch	3	
	Ground	4	
	+5 Volts	5	
	12 Volts	6	

## Jumper Descriptions

Located on the adapter are two jumpers designated P1 and P3. Jumper P1 changes the function of pin 2 on the direct drive interface. When placed on pins 2 and 3, jumper P1 selects ground as the function of direct drive interface, pin 2. This selection is for displays that support five color outputs, such as the IBM Color Display. When P1 is placed on pins 1 and 2, red prime output is placed on pin 2 of the direct drive interface connector. This supports the IBM Enhanced Color Display, which utilizes six color outputs on the direct drive interface.

Jumper P3 changes the I/O address port of the Enhanced Graphics Adapter within the system. In its normal position, (pins 1 and 2), all Enhanced Graphics Adapter addresses are in the range 3XX. Moving jumper P3 to pins 2 and 3 changes the addresses to 2XX. Operation of the adapter in the 2XX mode is not supported in BIOS.

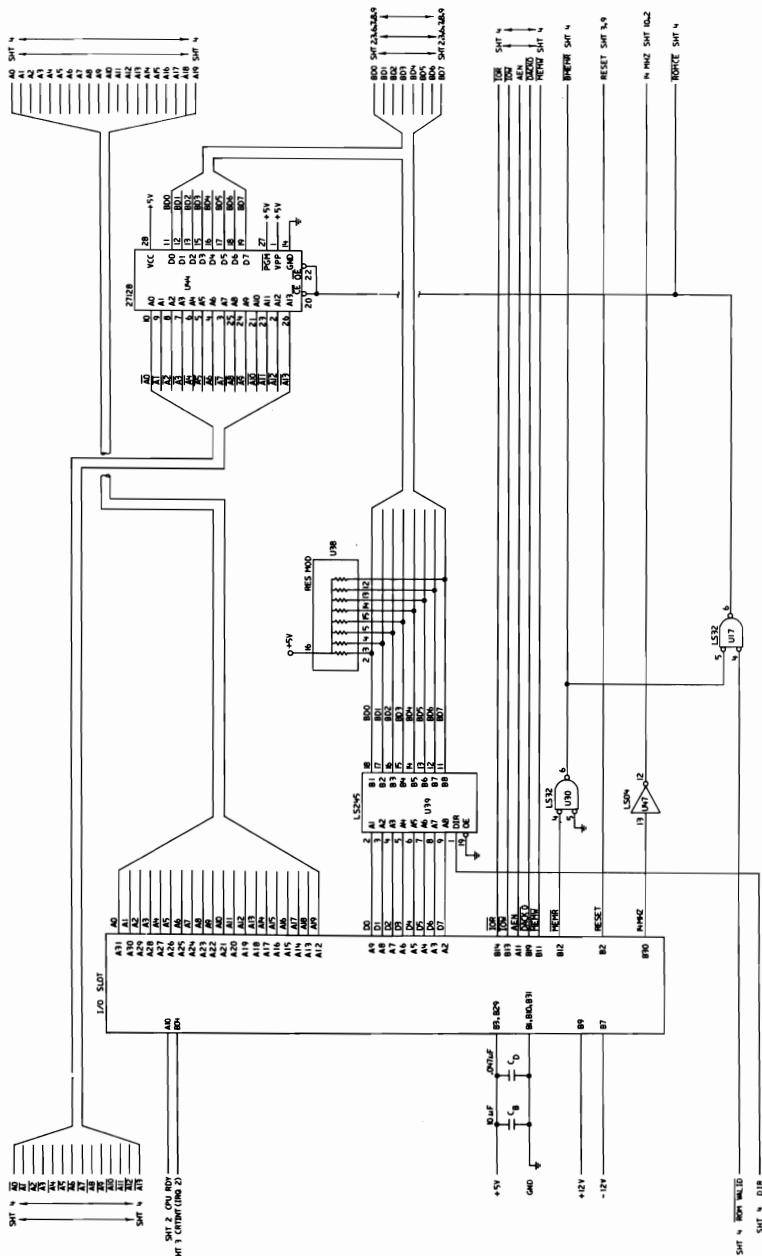
The following figure shows the location of the jumpers and numbering of the connectors.





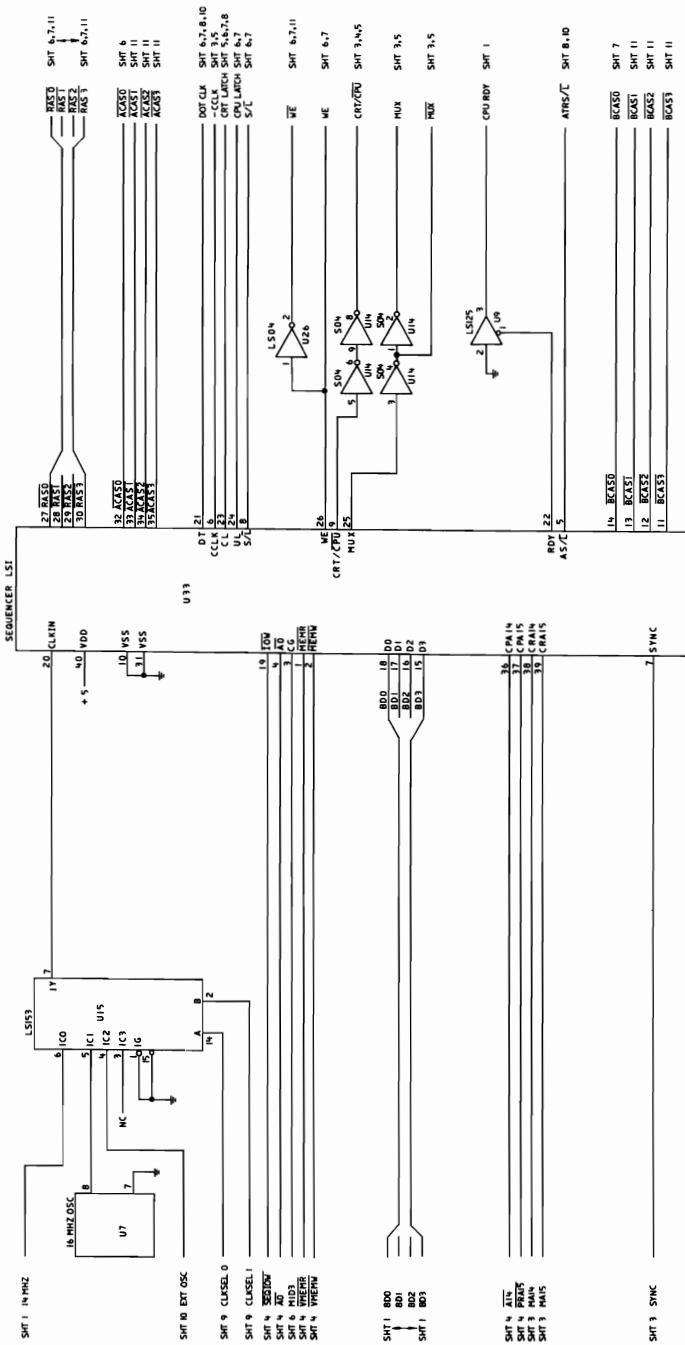
# Logic Diagrams

## ENHANCED GRAPHICS ADAPTER



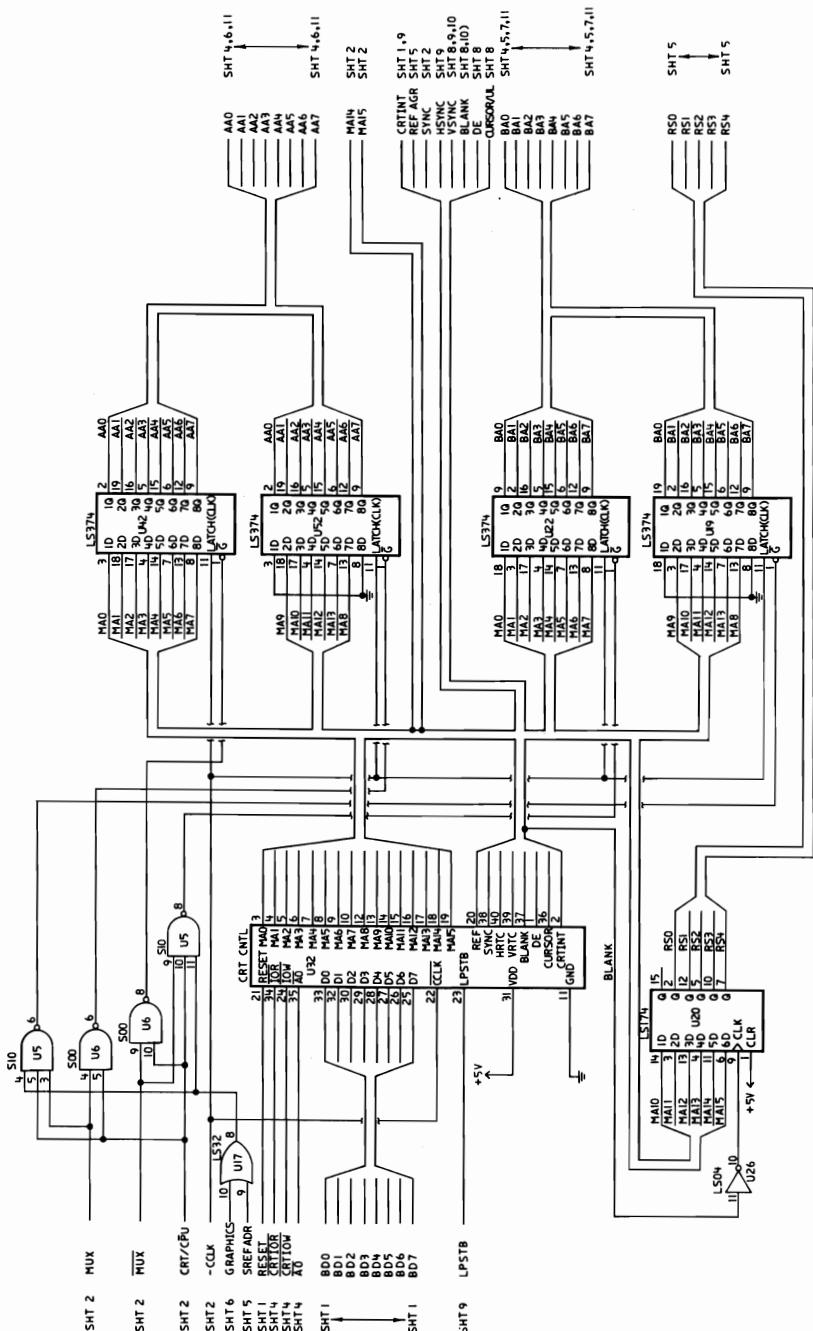
Enhanced Graphics Adapter Sheet 1 of 11

## **ENHANCED GRAPHICS ADAPTER**



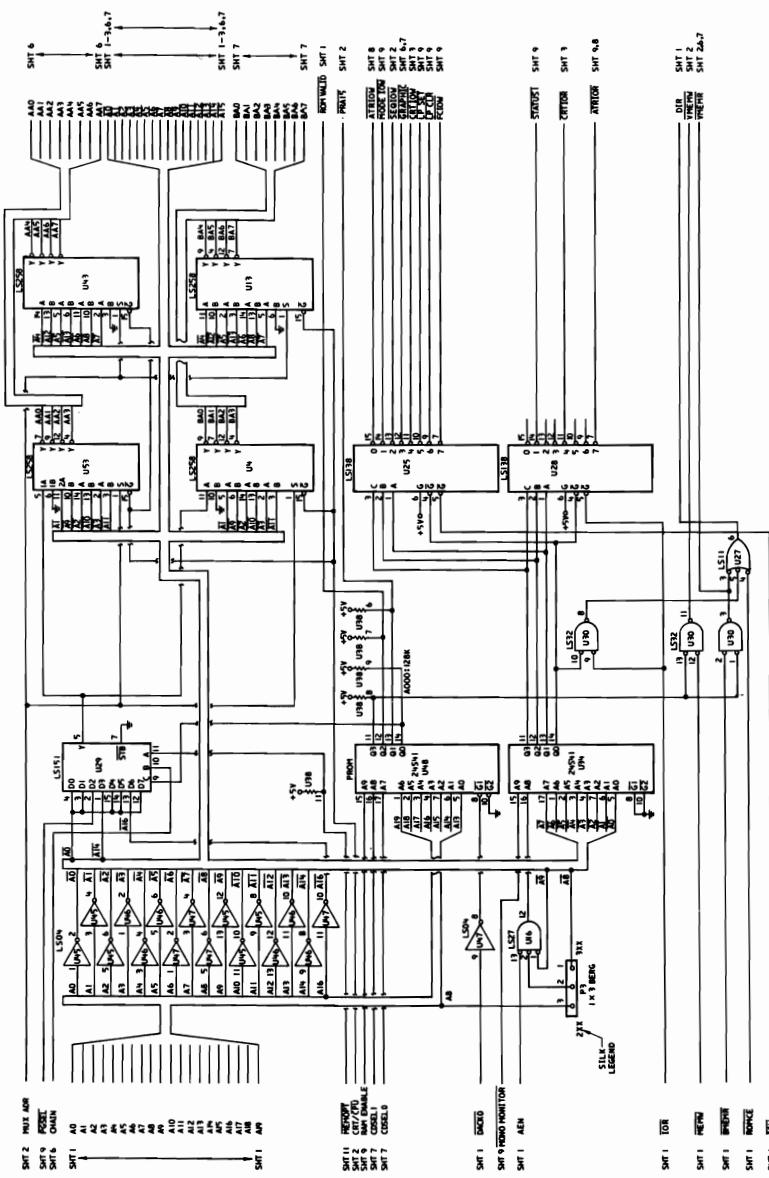
Enhanced Graphics Adapter Sheet 2 of 11

# ENHANCED GRAPHICS ADAPTER



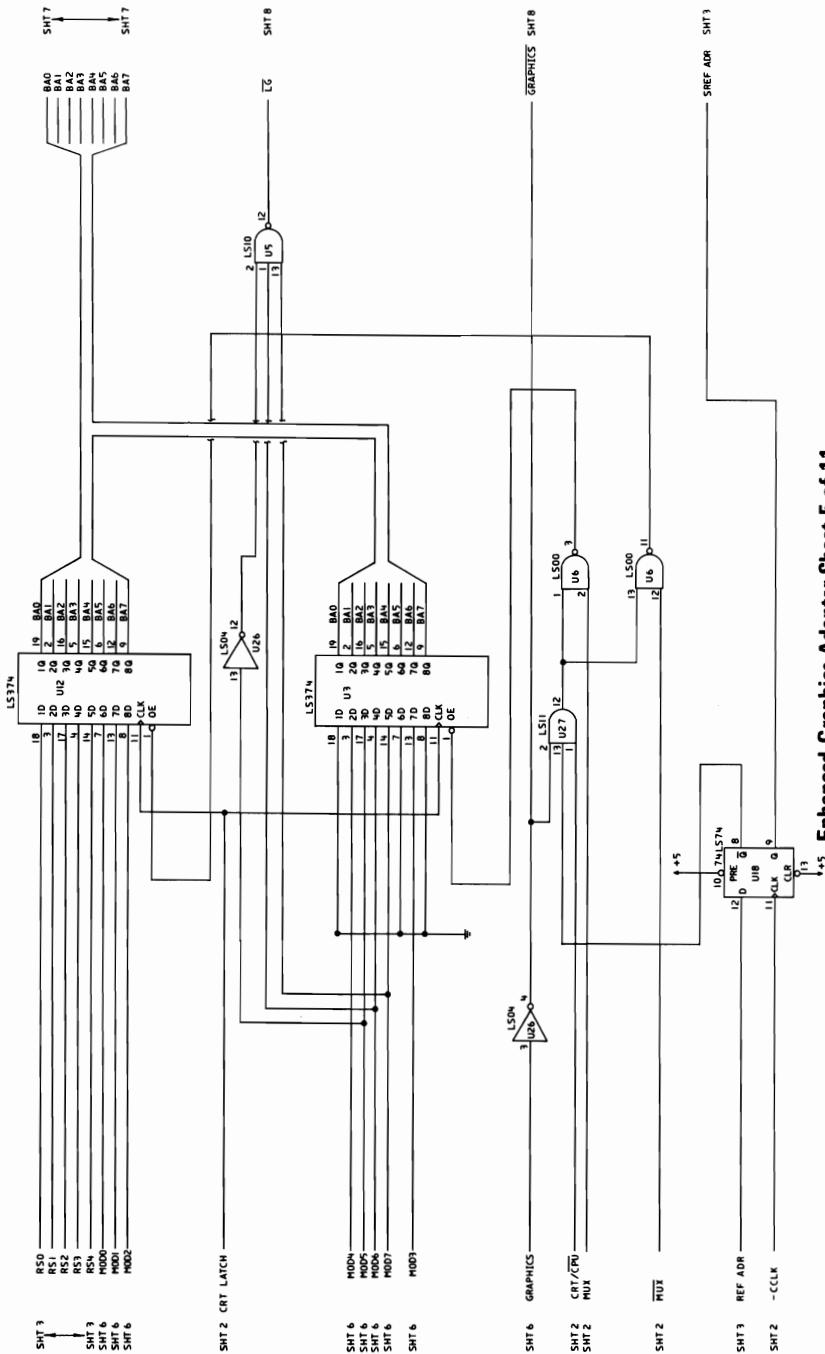
Enhanced Graphics Adapter Sheet 3 of 11

## **ENHANCED GRAPHICS ADAPTER**



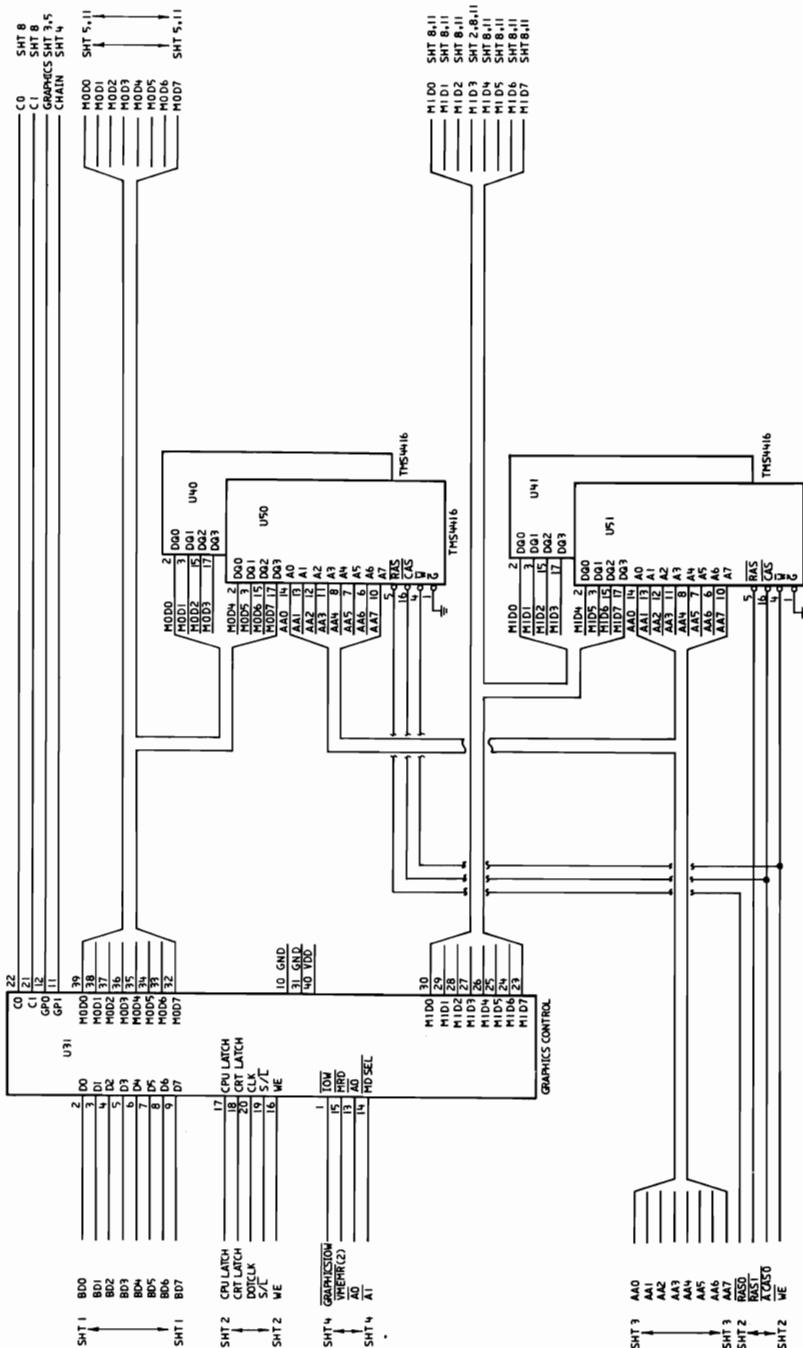
Enhanced Graphics Adapter Sheet 4 of 11

# ENHANCED GRAPHICS ADAPTER



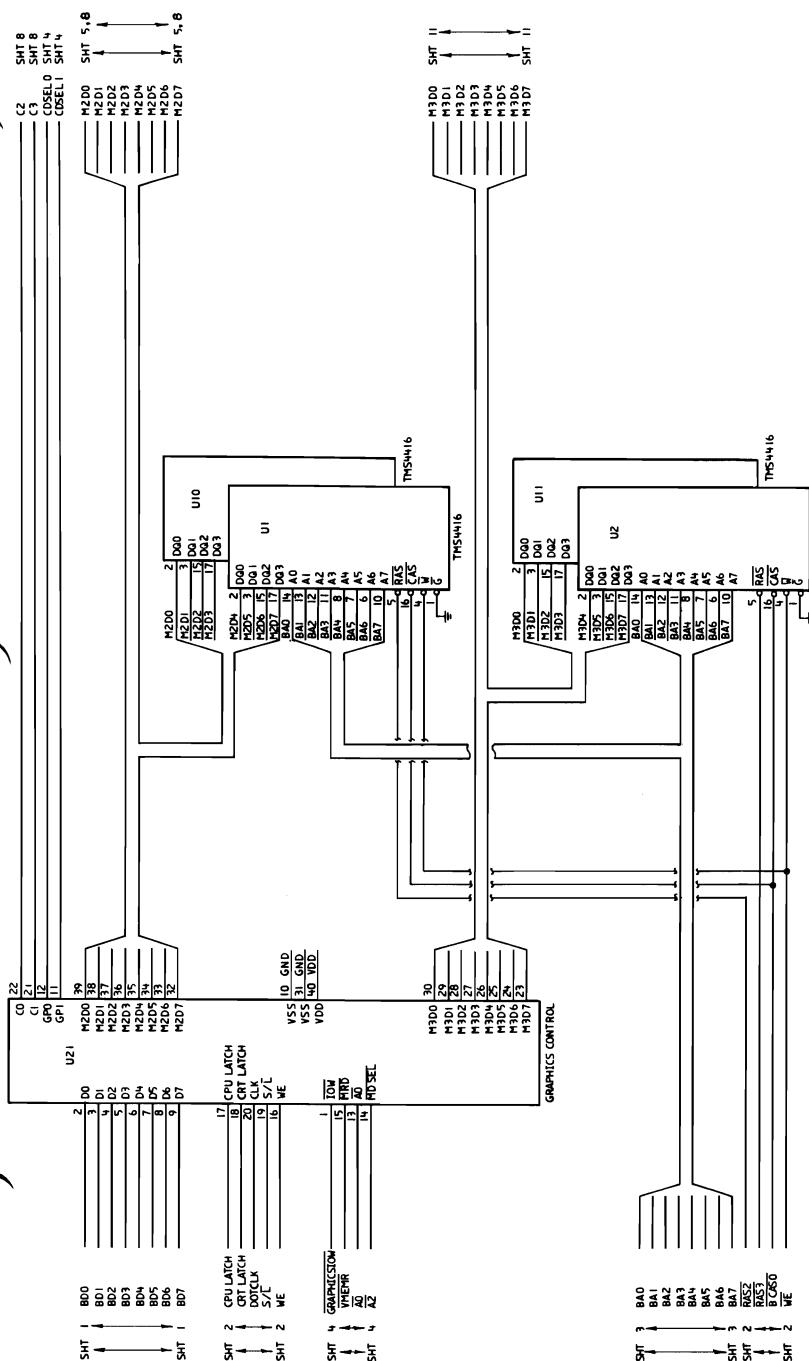
Enhanced Graphics Adapter Sheet 5 of 11

## ENHANCED GRAPHICS ADAPTER



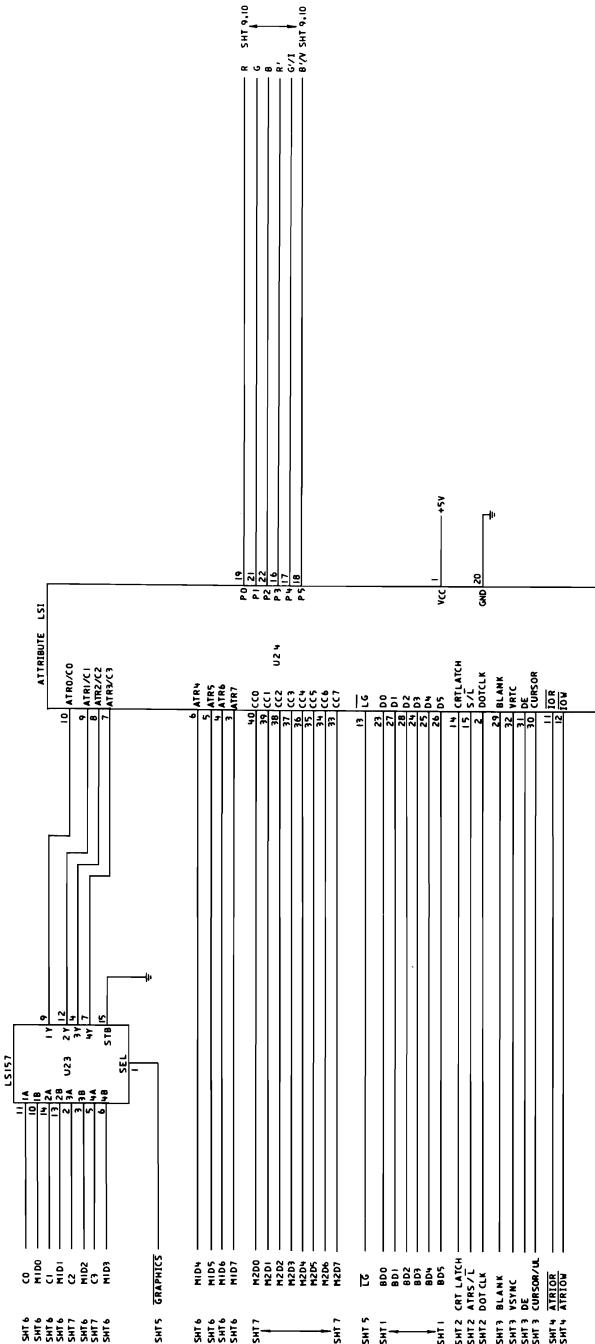
Enhanced Graphics Adapter Sheet 6 of 11

## ENHANCED GRAPHICS ADAPTER



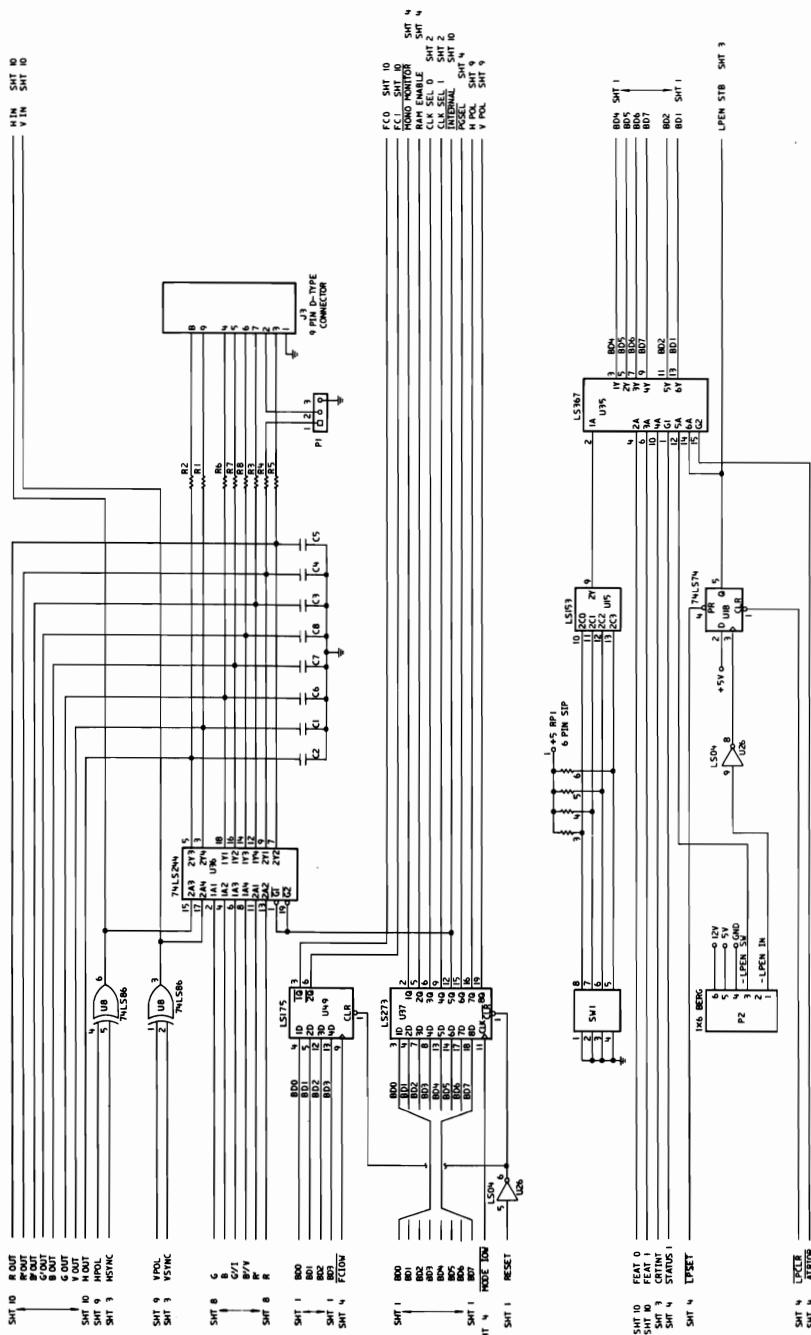
Enhanced Graphics Adapter Sheet 7 of 11

## ENHANCED GRAPHICS ADAPTER



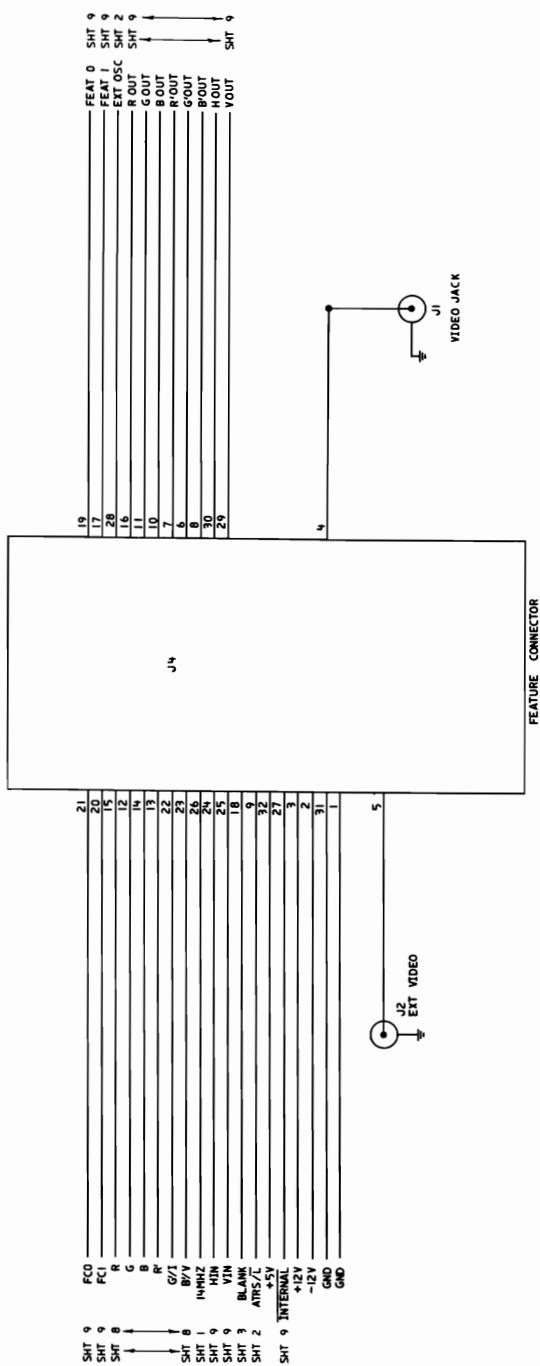
Enhanced Graphics Adapter Sheet 8 of 11

# ENHANCED GRAPHICS ADAPTER



Enhanced Graphics Adapter Sheet 9 of 11

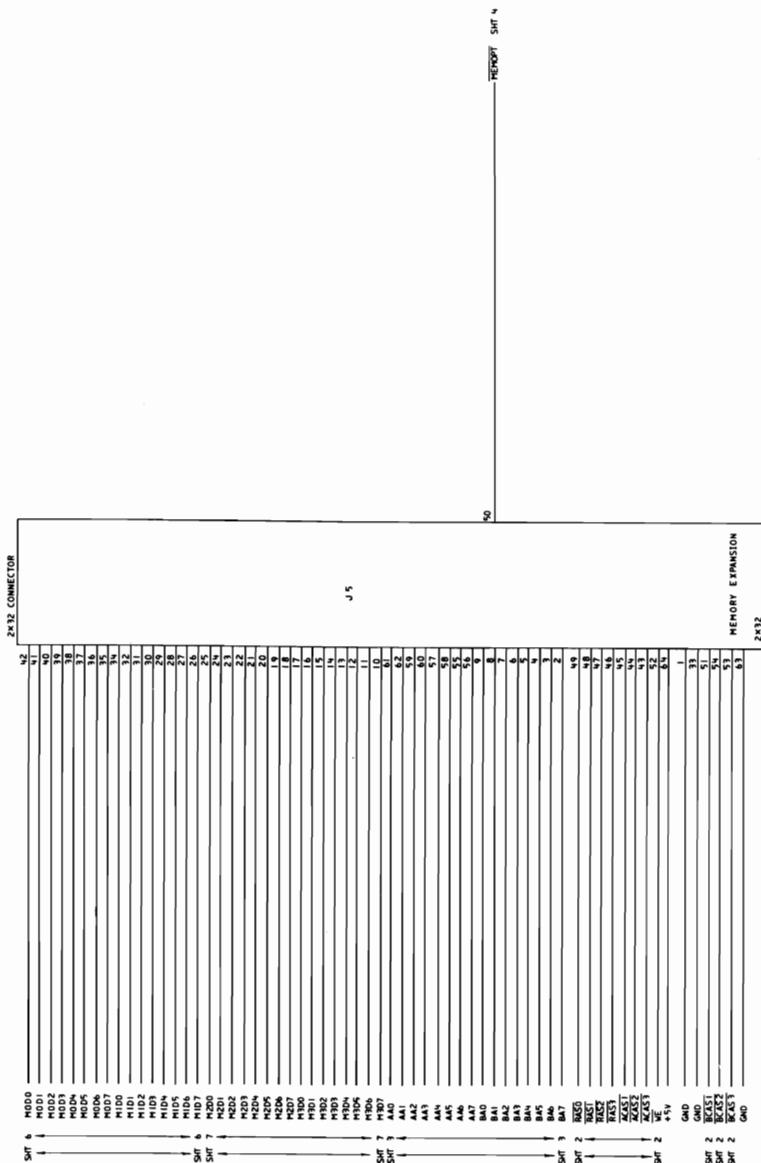
## ENHANCED GRAPHICS ADAPTER



NOTE :  
1 GROUNDS - ONE AT EACH END OF CONNECTOR.

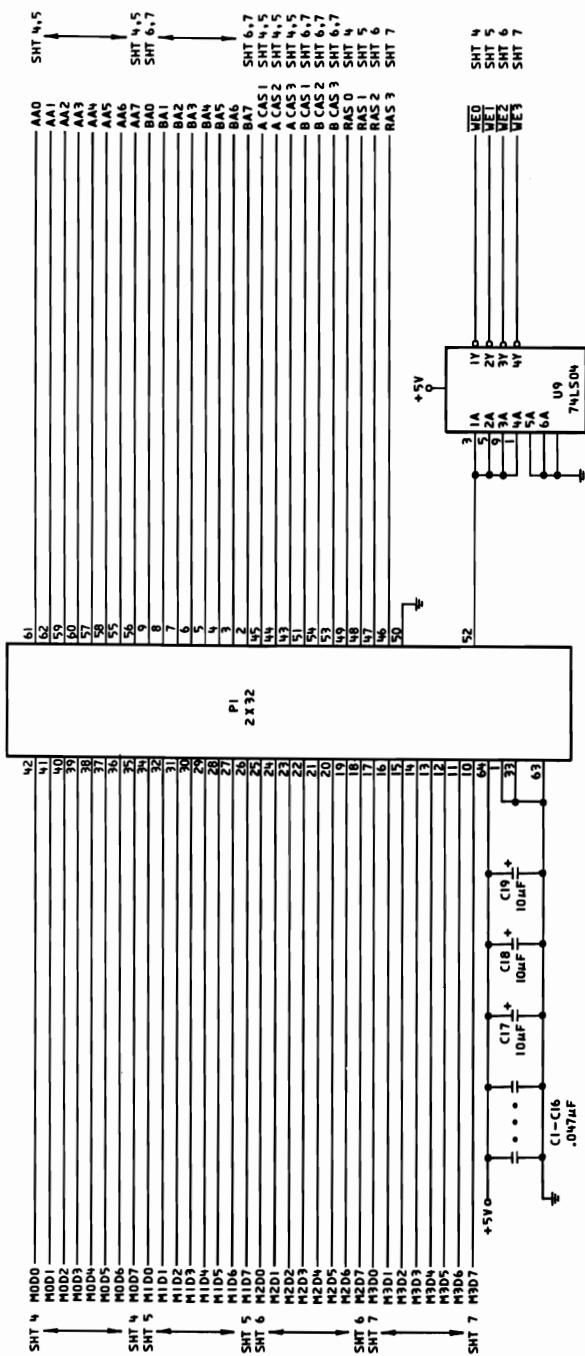
Enhanced Graphics Adapter Sheet 10 of 11

## ENHANCED GRAPHICS ADAPTER



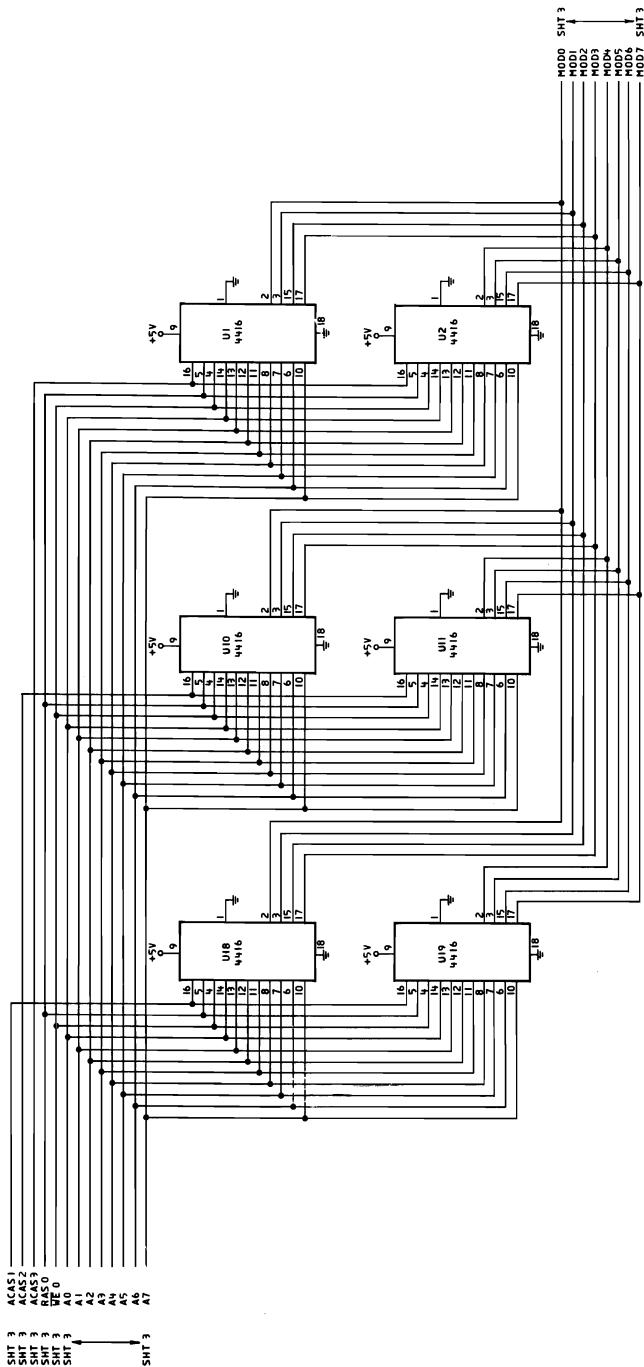
Enhanced Graphics Adapter Sheet 11 of 11

## ENHANCED GRAPHICS ADAPTER



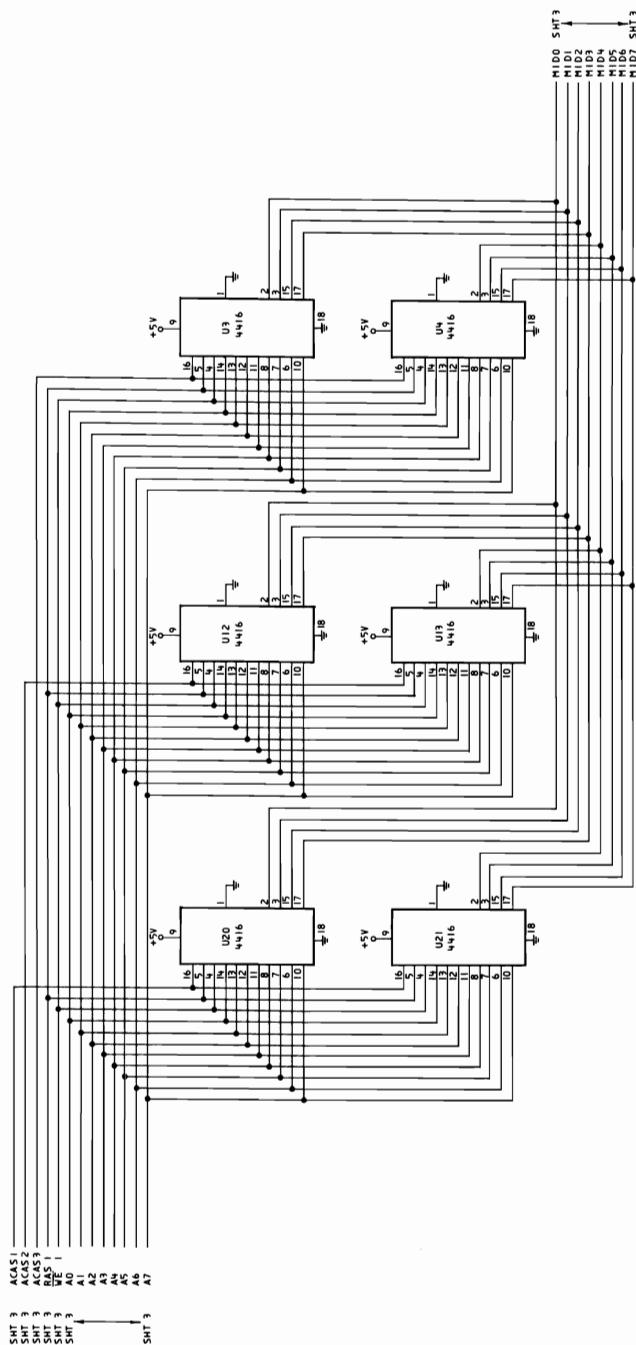
Graphics Memory Expansion Card Sheet 1 of 5

# ENHANCED GRAPHICS ADAPTER



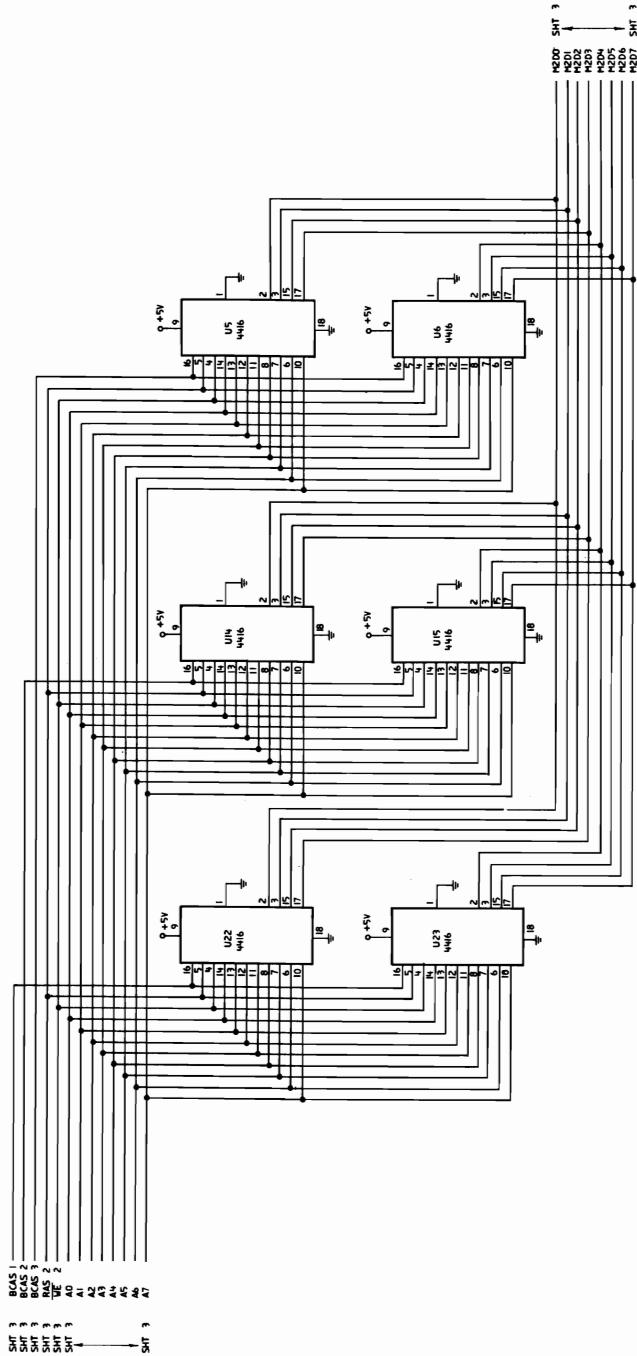
Graphics Memory Expansion Card Sheet 2 of 5

## ENHANCED GRAPHICS ADAPTER



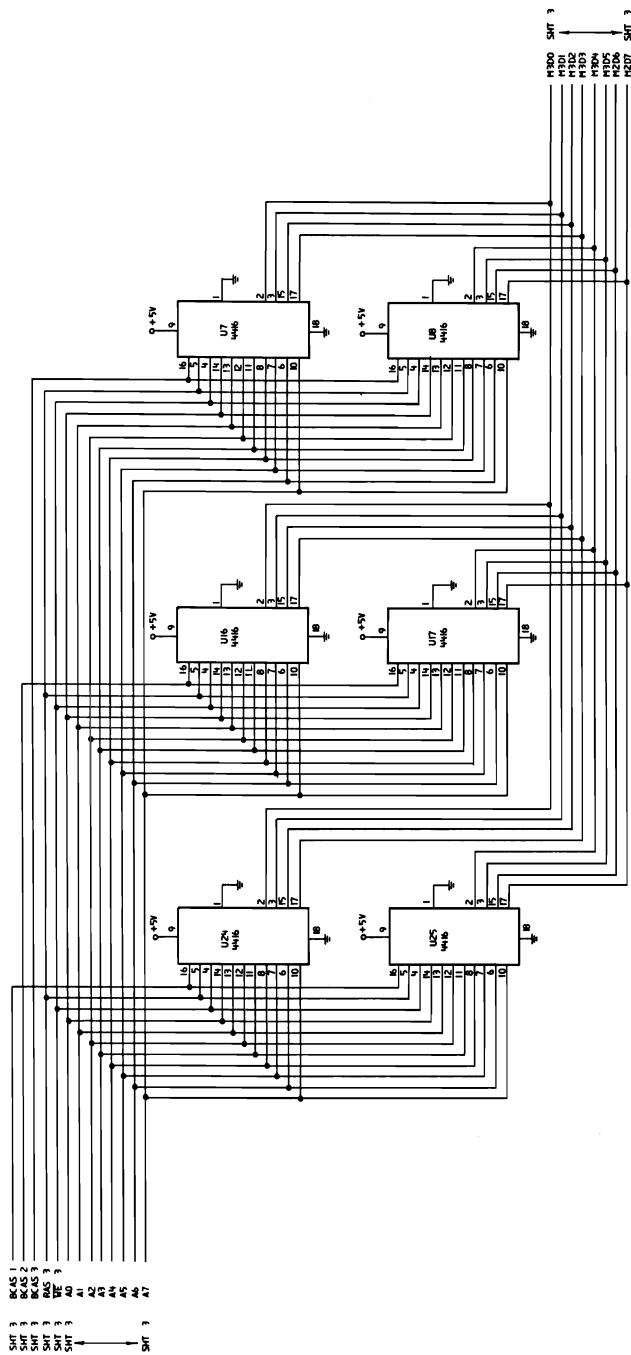
Graphics Memory Expansion Card Sheet 3 of 5

# ENHANCED GRAPHICS ADAPTER



Graphics Memory Expansion Card Sheet 4 of 5

## ENHANCED GRAPHICS ADAPTER



Graphics Memory Expansion Card Sheet 5 of 5

# **BIOS Listing**

## **Vectors with Special Meanings**

### **Interrupt Hex 42 - Reserved**

When an IBM Enhanced Graphics Adapter is installed, the BIOS routines use interrupt 42 to revector the video pointer.

### **Interrupt Hex 43 - IBM Enhanced Graphics Video Parameters**

When an IBM Enhanced Graphics Adapter is installed, the BIOS routines use this vector to point to a data region containing the parameters required for the initializing of the IBM Enhanced Graphics Adapter. Note that the format of the table must adhere to the BIOS conventions established in the listing. The power-on routines initialize this vector to point to the parameters contained in the IBM Enhanced Graphics Adapter ROM.

### **Interrupt Hex 44 - Graphics Character Table**

When an IBM Enhanced Graphics Adapter is installed the BIOS routines use this vector to point to a table of dot patterns that will be used when graphics characters are to be displayed. This table will be used for the first 128 code points in video modes 4, 5, and 6. This table will be used for 256 characters in all additional graphics modes. See the appropriate BIOS interface for additional information on setting and using the graphics character table pointer.

```

1 PAGE, 120
2 TITLE ENHANCED GRAPHICS ADAPTER BIOS
3 EXTRN CGMN:NEAR, CGDDOT:NEAR, INT_1F_1:NEAR, CGMN_FDC:NEAR
4 EXTRN END_ADDRESS:NEAR
5
6
7 THE BIOS ROUTINES ARE MEANT TO BE ACCESSED THROUGH
8 SOFTWARE INTERRUPTS ONLY. ANY ADDRESSES PRESENT IN
9 THIS LISTING ARE INCLUDED FOR COMPLETENESS
10 NOT FOR REFERENCE. APPLICATIONS WHICH RELIANCE
11 ABSOLUTE ADDRESSES WITHIN THE CODE SEGMENT
12 VIOLATE THE STRUCTURE AND DESIGN OF BIOS.
13
14
15 .LIST
16 INCLUDE VFRONT.INC
17 SUBTTL VFRONT.INC
18 PAGE
19
20
21 ----- INT 10 -----
22
23 CVIDEO IO THESE ROUTINES PROVIDE THE CRT INTERFACE
24 CTHE FOLLOWING FUNCTIONS ARE PROVIDED:
25 C(AH)=0 SET MODE (AL) CONTAINS MODE VALUE
26
27 AL AD TYPE RES NOTES DF-DIM DISPLAY MAX PCS
28 * 0 BB ALPHA 640X200 40X25 COLOR - BW 8
29 * 1 BB ALPHA 640X200 40X25 COLOR 8
30 * 2 BB ALPHA 640X200 80X25 COLOR - BW 8
31 * 3 BB ALPHA 640X200 80X25 COLOR 8
32 * 4 BB GRPHX 320X200 40X25 COLOR - BW 1
33 * 5 BB GRPHX 320X200 40X25 COLOR - BW 1
34 * 6 BB GRPHX 640X200 80X25 COLOR - BW 1
35 * 7 BB ALPHA 720X350 80X25 MONOCHROME 8
36
37 8 RESERVED
38 9 RESERVED
39 A RESERVED
40 B RESERVED - INTERNAL USE
41 C RESERVED - INTERNAL USE
42
43 D AO GRPHX 320X200 40X25 COLOR 8
44 E AO GRPHX 640X200 80X25 COLOR 4
45 F AU GRPHX 640X350 80X25 MONOCHROME 2
46 10 AO GRPHX 640X350 80X25 HI RES 2
47
48 NOTE : HIGH BIT AL SET PREVENTS REGEN BUFFER CLEAR ON
49 HODES RUNNING ON THE COMBO VIDEO ADAPTER
50
51 *** NOTE BW MODES OPERATE SAME AS COLOR MODES, BUT
52 COLOR BURST IS NOT ENABLED
53 (AH)=1 SET CURSOR POSITION
54 (CH) = BITS 4-0 = START LINE FOR CURSOR
55 *** HARDWARE WILL ALWAYS CAUSE BLINK
56 *** SETTING BIT 5 OR 6 WILL CAUSE ERRATIC
57 *** BEHAVIOR, TURNING ON NO CURSOR AT ALL
58 (CL) = BITS 5-9 END LINE FOR CURSOR
59
60 (AH)=2 SET CURSOR POSITION
61 (DH,DL) = ROW,COLUMN (0,0) IS UPPER LEFT
62 BH = PAGE NUMBER
63 RAD CURSOR POSITION
64 (AH)=3 RAD CURSOR POSITION
65 (BH) = PAGE NUMBER
66 ON EXIT (DH,DL) = ROW,COLUMN OF CURRENT CURSOR
67 (CH,CL) = CURSOR MODE CURRENTLY SET
68 (AH)=4 READ LIGHT PEN POSITION
69 ON EXIT
70 (AH) = 0 -- LIGHT PEN SWITCH NOT DOWN/NOT_TRIGGERED
71 (AH) = 1 -- VALID LIGHT PEN VALUE IN REGISTERS
72 (DH,DL) = ROW,COLUMN OF CHARACTER LP POSN
73 (CX) = COUNT OF CHARACTERS (0-199)
74 (CX) = RASTER LINE NUMBER FOR NEW GRAPHICS MODES
75 (BX) = PIXEL COLUMN (0-319,639)
76 (AH)=5 SELECT ACTIVE DISPLAY PAGE
77 (AL) = PAGE VALUE, SEE AH=0 FOR PAGE INFO
78 (AH)=6 SCROLL ACTIVE PAGE UP
79 (AL) = NUMBER OF LINES, INPUT LINES BLANKED AT BOTTOM
80 OF WINDOW
81 AL = 0 MEANS BLANK ENTIRE WINDOW
82 (CH,CL) = ROW,COLUMN OF UPPER LEFT CORNER OF SCROLL
83 (DH,DL) = ROW,COLUMN OF LOWER RIGHT CORNER OF SCROLL
84 (BH) = ATTRIBUTE TO BE USED ON BLANK LINE
85 (AH)=7 SCROLL ACTIVE PAGE DOWN
86 (AL) = NUMBER OF LINES, INPUT LINES BLANKED AT TOP
87 OF WINDOW
88 AL = 0 MEANS BLANK ENTIRE WINDOW
89 (CH,CL) = ROW,COLUMN OF UPPER LEFT CORNER OF SCROLL
90 (DH,DL) = ROW,COLUMN OF LOWER RIGHT CORNER OF SCROLL
91 (BH) = ATTRIBUTE TO BE USED ON BLANK LINE
92
93 CHARACTER HANDLING ROUTINES
94 (AH) = 8 READ ATTRIBUTE/CHARACTER AT CURRENT CURSOR POSITION
95 (BH) = DISPLAY PAGE
96 ON EXIT:
97 (AL) = CHAR READ
98 (AH) = ATTRIBUTE OF CHARACTER READ (ALPHA MODES ONLY)
99 (AH) = 9 WRITE ATTRIBUTE/CHARACTER AT CURRENT CURSOR POSITION
100 (BH) = DISPLAY PAGE
101 (CX) = COUNT OF CHARACTERS TO WRITE
102 (AL) = CHAR TO WRITE
103 (BL) = ATTRIBUTE OF CHARACTER (ALPHA)/COLOR OF CHAR
104 (GRAPHICS)
105 SEE NOTE ON WRITE DOT FOR BIT 7 OF BL = 1.
106 (AH) = A WRITE CHARACTER ONLY AT CURRENT CURSOR POSITION
107 (BH) = DISPLAY PAGE
108 (CX) = COUNT OF CHARACTERS TO WRITE
109 (AL) = CHAR TO WRITE
110 FOR READ/WRITE CHARACTER INTERFACE WHILE IN GRAPHICS MODE, THE
111 CHARACTERS ARE FORMED FROM A CHARACTER GENERATOR IMAGE
112 WHICH IS STORED IN THE SYSTEM ROM. ONLY THE FIRST 128 CHARS
113 ARE CONTAINED THERE. TO READ/WRITE THE SECOND 128
114 CHARS, THE USER MUST INITIALIZE THE POINTER AT
115 INTERRUPT 1FH (LOCATION 0007CH) TO POINT TO THE 1K BYTE
116 TABLE CONTAINING THE CODE POINTS FOR THE SECOND
117 128 CHARS (128-255).
118
119 FOR THE NEW GRAPHICS MODES 256 GRAPHICS CHARS ARE
120 SUPPLIED IN THE SYSTEM ROM.
121
122 FOR WRITE CHARACTER INTERFACE IN GRAPHICS MODE, THE REPLICATION
123 FACTOR CONTAINED IN (CX) ON ENTRY WILL PRODUCE VALID
124 RESULTS ONLY FOR CHARACTERS CONTAINED ON THE SAME ROW.
125 CONTINUATION TO SUCCEEDING LINES WILL NOT PRODUCE
126 CORRECTLY.

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127
128 C : GRAPHICS INTERFACE
129 C : (AH) = B SET COLOR PALETTE
130 C : FOR USE IN COMPATIBILITY MODES
131 C : (BH) = COLOR ID TO BE SET (0-197)
132 C : (BL) = COLOR VALUE TO BE USED WITH THAT COLOR ID
133 C : NOTE: FOR THE CURRENT COLOR CARD, THIS ENTRY POINT
134 C : HAS MEANING ONLY FOR 320X200 GRAPHICS.
135 C : COLOR ID = 0 SELLS THE COLOR ID COLOR (0-15).
136 C : COLOR ID > 0 SELECTS THE PALETTE TO BE USED:
137 C : 0 = GREEN(1)/RED(2)/BROWN(3)
138 C : 1 = CYAN(1)/MAGENTA(2)/WHITE(3)
139 C : IN 40X25 OR 80X25 ALPHA MODES, THE VALUE SET
140 C : BY COLOR ID IS THE BORDER COLOR, NOT THE
141 C : BORDER COLOR TO BE USED (VALUES 0-31,
142 C : WHERE 16-31 SELECT THE HIGH INTENSITY
143 C : BACKGROUND SET).
144 C : (AH) = C WRITE DOT
145 C : (BH) = PAGE
146 C : (DX) = ROW NUMBER
147 C : (CX) = COLUMN NUMBER
148 C : (AL) = COLOR VALUE
149 C : IF THE VALUE OF AL = 1, THEN THE COLOR VALUE IS
150 C : EXCLUSIVE OR'D WITH THE CURRENT CONTENTS OF
151 C : THE DOT
152 C : (AH) = D READ DOT
153 C : (BH) = PAGE
154 C : (DX) = ROW NUMBER
155 C : (CX) = COLUMN NUMBER
156 C : (AL) RETURNS THE DOT READ
157 C :
158 C : ASCII TELETYPE ROUTINE FOR OUTPUT
159 C :
160 C : (AH) = E WRITE TELETYPE TO ACTIVE PAGE
161 C : (AL) = CHAR TO WRITE
162 C : (BL) = FOREGROUND COLOR IN GRAPHICS MODE
163 C : NOTE -- SCREEN WIDTH IS CONTROLLED BY PREVIOUS MODE SET
164 C :
165 C : (AH) = F CURRENT VIDEO STATE
166 C : RETURNS THE CURRENT VIDEO STATE
167 C : (AL) = 0 RETURN ALL (SEE AH=0 FOR EXPLANATION)
168 C : (AH) = NUMBER OF CHARACTER COLUMNS ON SCREEN
169 C : (BH) = CURRENT ACTIVE DISPLAY PAGE
170 C :
171 C : (AH) = 10 SET PALETTE REGISTERS
172 C :
173 C : (AL) = 0 SET INDIVIDUAL PALETTE REGISTER
174 C : BL = PALETTE REGISTER TO BE SET
175 C : BH = VALUE TO SET
176 C :
177 C : AL = 1 SET OVERSCAN REGISTER
178 C : BH = VALUE TO SET
179 C :
180 C : AL = 2 SET ALL PALETTE REGISTERS AND OVERSCAN
181 C : ES:DX POINTS TO A 17 BYTE TABLE
182 C : BYTES 0 - 15 ARE THE PALETTE VALUES, RESPECTIVELY
183 C : BYTE 16 IS THE OVERSCAN VALUE
184 C :
185 C : AL = 3 TOGGLE INTENSIFY/BLINKING BIT
186 C : BL = 0 ENABLE INTENSIFY
187 C : BL = 1 ENABLE BLINKING
188 C :
189 C : (AH) = 11 CHARACTER GENERATOR ROUTINE
190 C : NOTE : THIS CALL WILL INITIATE A MODE SET, COMPLETELY
191 C : RESETTING THE VIDEO ENVIRONMENT BUT MAINTAINING
192 C : THE REGEN BUFFER.
193 C :
194 C : AL = 00 USER ALPHA LOAD
195 C : ES:BP - POINTER TO USER TABLE
196 C : CX - COUNT TO STORE
197 C : DX - CHARACTER OFFSET INTO TABLE
198 C : BL - BLOCK TO LOAD
199 C : BH - NUMBER OF BYTES PER CHARACTER
200 C :
201 C : AL = 01 ROM MONOCHROME SET
202 C : BL - BLOCK TO LOAD
203 C : BH - NUMBER OF BYTES PER CHARACTER
204 C :
205 C : AL = 02 ROM 8XB DOUBLE DOT
206 C : BL - BLOCK TO LOAD
207 C : BH - NUMBER OF BYTES PER CHARACTER
208 C :
209 C : AL = 03 SET BLOCK SPECIFIER
210 C : BL - CHAR GEN BLOCK SPECIFIER
211 C : D3-D2 ATTR BIT 3 ONE, CHAR GEN 0-3
212 C : D1-D0 ATTR BIT 3 ZERO, CHAR GEN 0-3
213 C : NOTE : THIS MUST CALL = 03 AS A FUNCTION CALL
214 C : AX = 1000H
215 C : BX = 0712H
216 C : IS RECOMMENDED TO SET THE COLOR PLANES
217 C : REGIONS TO 5, 2 CHARACTERS AND EIGHT
218 C : CONSISTENT COLORS.
219 C :
220 C : NOTE : THE FOLLOWING INTERFACE (AL=1X) IS SIMILAR IN FUNCTION
221 C : TO (AL=0X), EXCEPT THAT:
222 C : - PAGE ZERO MUST BE ACTIVE
223 C : - POINTS (BYTES/CHAR) WILL BE RECALCULATED
224 C : - ROWS WILL BE CALCULATED FROM THE FOLLOWING:
225 C :   INT((200 OR 350) / POINTS) - 1
226 C : - CRT_LEN MUST BE SET AFTER ROW :
227 C :   (ROWS + 1) * CRT_COLS - 2
228 C : - THE CRTC WILL BE REPROGRAMMED AS FOLLOWS :
229 C :   R09H = POINTS - 1
230 C :   R09H DONE ONLY IN MODE SET
231 C :   ROAH = POINTS - 2
232 C :   ROAH = CURSOR START
233 C :   ROBH = 0
234 C :   R12H = CURSOR END
235 C :   R12H = VERT DISP END
236 C :   R14H = POINTS UNDERLINE LOC
237 C :
238 C : THE ABOVE REGISTER CALCULATIONS MUST BE CLOSE TO THE
239 C : ORIGINAL TABLE VALUES OR UNDETERMINED RESULTS WILL
240 C : OCCUR.
241 C :
242 C : NOTE : THE FOLLOWING INTERFACE IS DESIGNED TO BE
243 C : CALLED ONLY IMMEDIATELY AFTER A MODE SET HAS
244 C : BEEN ISSUED. FAILURE TO ADHERE TO THIS PRACTICE
245 C : MAY CAUSE UNDETERMINED RESULTS.
246 C :
247 C : AL = 10 USER ALPHA LOAD
248 C : ES:BP - POINTER TO USER TABLE
249 C : CX - COUNT TO STORE
250 C : DX - CHARACTER OFFSET INTO TABLE
251 C : BL - BLOCK TO LOAD
252 C : BH - NUMBER OF BYTES PER CHARACTER

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253 C . . . . .  
 254 C . . . . .  
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 329 C . . . . .  
 330 C . . . . .  
 331 C . . . . .  
 332 C . . . . .  
 333 C . . . . .  
 334 C SRLOAD MACRO SEGREG, VALUE  
 335 C IFNB <VALUE>  
 336 C 1FN <VALUE>,<0>  
 337 C SUB DX,DX  
 338 C ELSE  
 339 C MOV DX, VALUE  
 340 C ENDIF  
 341 C ENDF  
 342 C MOV SEGREG, DX  
 343 C ENDM  
 344 C  
 345 C ;----- LOW MEMORY SEGMENT  
 346 C ABS0 SEGMENT AT 0  
 347 C PLANAR\_VIDEO ORG 042H#4 LABEL DWWORD ; PRINT SCREEN VECTOR  
 348 C INT5\_PTR ORG 010H#4 LABEL DWWORD ; VIDEO I/O VECTOR  
 349 C VIDEO ORG 01FH#4 LABEL DWWORD ; GRAPHIC CHARS 128-255  
 350 C EXT\_PTR ORG 01FH#4 LABEL DWWORD  
 351 C  
 352 C EQUIP\_L\_ORG 010H LABEL DWWORD ; REVECTORED 10H#4  
 353 C EQUIP\_L\_? ORG 010H LABEL BYTE  
 354 C EQUIP\_FLAG DW ? ; GRAPHIC CHARS 0-255  
 355 C  
 356 C ;----- REUSE RAM FROM PLANAR BIOS  
 357 C  
 358 C  
 359 C GRX\_SET ORG 043H#4 LABEL DWWORD  
 360 C  
 361 C  
 362 C  
 363 C  
 364 C  
 365 C  
 366 C  
 367 C  
 368 C  
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 372 C  
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 375 C  
 376 C  
 377 C  
 378 C  
 NOTE : THE FOLLOWING INTERFACE IS DESIGNED TO BE  
 CALLED ONLY IMMEDIATELY AFTER A MODE SET HAS  
 BEEN ISSUED. FAILURE TO ADHERE TO THIS PRACTICE  
 MAY CAUSE UNDETERMINED RESULTS.  
 AL = 20 USER GRAPHICS CHARS INT 01FH (8x8)  
 ES:BP - POINTER TO USER TABLE  
 AL = 21 USER GRAPHICS CHARS  
 ES:BP - POINTER TO USER TABLE  
 CX - POINTS (BYTES PER CHARACTER)  
 BL - ROW SPECIFIER  
 BL = 0 USER  
 DL - ROWS  
 BL = 1 14 (OEH)  
 BL = 2 25 (19H)  
 BL = 3 43 (2BH)  
 AL = 22 ROM 8 X 14 SET  
 BL - ROW SPECIFIER  
 AL = 23 ROM 8 X 14 DOUBLE DOT  
 BL - ROW SPECIFIER  
 AL = 30 INFORMATION  
 CX - POINTS  
 DL - ROWS  
 BH = 0 RETURN CURRENT INT 1FH PTR  
 BH = 1 RETURN CURRENT INT 4FH PTR  
 BH = 2 RETURN ROM 8 X 14 PTR  
 BH = 3 RETURN ROM DOUBLE DOT PTR  
 BH = 4 RETURN ROM DOUBLE DOT PTR (TOP)  
 BH = 5 RETURN ROM ALPHA ALTERNATE 9X14  
 ES:BP - PTR TO TABLE  
 (AH) = 12 ALTERNATE SELECT  
 BL = 10 RETURN EGA INFORMATION  
 BH = 0 - COLOR MODE IN EFFECT <3><D><0>  
 1 - MONOC MODE IN EFFECT <3><B><0>  
 BL = MEMORY VALUE  
 0 - 128K 0 1 - 128K  
 1 0 - 192K 1 1 - 256K  
 CH = FEATURE BITS  
 CL = SWITCH SETTING  
 BL = 20 SELECT ALTERNATE PRINT SCREEN ROUTINE  
 (AH) = 13 WRITE STRING  
 ES:BP - POINTER TO STRING TO BE WRITTEN  
 CX - CHARACTER ONLY COUNT  
 DX - POSITION TO BEGIN STRING, IN CURSOR  
 TERMS  
 BH - PAGE NUMBER  
 AL = 0  
 BL - ATTRIBUTE  
 STRING - (CHAR, CHAR, CHAR, ...)  
 CURSOR NOT MOVED  
 AL = 1  
 BL - ATTRIBUTE  
 STRING - (CHAR, CHAR, CHAR, ...)  
 CURSOR IS MOVED  
 AL = 2  
 STRING - (CHAR, ATTR, CHAR, ATTR, ...)  
 CURSOR NOT MOVED  
 AL = 3  
 STRING - (CHAR, ATTR, CHAR, ATTR, ...)  
 CURSOR IS MOVED  
 NOTE : CHAR RET, LINE FEED, BACKSPACE, AND BELL ARE  
 TREATED AS COMMANDS RATHER THAN PRINTABLE  
 CHARACTERS.

0000  
 0014  
 0016  
 0040  
 0040  
 007C  
 007C  
 0108  
 0108  
 010C  
 010C  
 0410  
 0410  
 0410  
 0449  
 0449  
 044A  
 044C  
 044E  
 0450  
 08 [  
 ????]  
 0460  
 0462

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0463 ????    379 C ADDR_6845 DW ?
0465 ??     380 C CRT_MODE_SET DB ?
0466 ??     381 C CRT_PALETTE DB ?
0467 ????    382 C
0472 ????    383 C ORG 0472H
0472 ????    384 C RESET_FLAG DW ?
0484 ????    385 C ORG 0484H
0484 ??     386 C ROWS DB ?
0485 ????    387 C POINTS DW ? ; ROWS ON THE SCREEN
0485 ????    388 C ; BYTES PER CHARACTER
0487 ??     389 C INFO DB ?
0487 ????    390 C
0488 ??     391 C ; INFO
0488 ??     392 C ; D7 - HIGH BIT OF MODE SET, CLEAR/NOT CLEAR REGEN
0488 ??     393 C ; D6 - MEMORY D6 D5 = 0 0 - 064K 0 1 - 128K
0488 ??     394 C ; D5 - 10 - 192K 1 1 - 256K
0488 ??     395 C ; D4 - RESERVED
0488 ??     396 C ; D3 - ACTIVE MONITOR (0), EGA NOT ACTIVE (1)
0488 ??     397 C ; D2 - WAIT FOR DISPLAY ENABLE (1)
0488 ??     398 C ; D1 - EGA HAS A MONOCHROME ATTACHED (1)
0488 ??     399 C ; D0 - SET C_TYPE EMULATE ACTIVE (0)
0488 ??     400 C
0488 ??     401 C ; INFO_3 DB ?
0488 ??     402 C
0488 ??     403 C ; INFO_3
0488 ??     404 C ; D7-D4 FEATURE BITS
0488 ??     405 C ; D3-D0 SWITCHES
0488 ??     406 C
0488 ??     407 C ; SAVE_PTR ORG 04A8H
0488 ??     408 C LABEL DWORD
0488 ??     409 C
0488 ??     410 C ----- SAVE_PTR
0488 ??     411 C
0488 ??     412 C ; SAVE_PTR IS A POINTER TO A TABLE AS DESCRIBED AS FOLLOWS :
0488 ??     413 C
0488 ??     414 C ; DWORD_1 VIDEO PARAMETER TABLE POINTER
0488 ??     415 C ; DWORD_2 DYNAMIC SAVE AREA POINTER
0488 ??     416 C ; DWORD_3 ALPHA MODE AUXILIARY CHAR GEN POINTER
0488 ??     417 C ; DWORD_4 GRAPICS MODE AUXILIARY CHAR GEN POINTER
0488 ??     418 C ; DWORD_5 RESERVED
0488 ??     419 C ; DWORD_6 RESERVED
0488 ??     420 C ; DWORD_7 RESERVED
0488 ??     421 C
0488 ??     422 C ; DWORD_1 PARAMETER TABLE POINTER
0488 ??     423 C ; INITIALIZED TO BIOS EGA PARAMETER TABLE.
0488 ??     424 C ; THIS VALUE MUST EXIST.
0488 ??     425 C
0488 ??     426 C ; DWORD_2 PARAMETER SAVE AREA POINTER
0488 ??     427 C ; INITIALIZED TO 0000:0000. THIS VALUE IS OPTIONAL.
0488 ??     428 C ; WHEN NON-ZERO, THIS POINTER WILL BE USED AS A POINTER
0488 ??     429 C ; TO A RAM AREA WHERE CERTAIN DYNAMIC VALUES ARE TO
0488 ??     430 C ; BE SAVED. WHEN IN EGA OPERATION THIS RAM AREA WILL
0488 ??     431 C ; HOLD THE EGA ALIASED STREAMS. THE OVERSCAN VALUE IN BYTES IS 160
0488 ??     432 C ; RESPECTIVELY. THE OVERSCAN VALUE IN BYTES IS 160
0488 ??     433 C ; AT LEAST 256 BYTES MUST BE ALLOCATED FOR THIS AREA.
0488 ??     434 C
0488 ??     435 C ; DWORD_3 ALPHA MODE AUXILIARY POINTER
0488 ??     436 C ; INITIALIZED TO 0000:0000. THIS VALUE IS OPTIONAL.
0488 ??     437 C ; WHEN NON-ZERO, THIS POINTER IS USED AS A POINTER
0488 ??     438 C ; TO A TABLES DESCRIBED AS FOLLOWS :
0488 ??     439 C
0488 ??     440 C ; BYTE BYTES/CHARACTER
0488 ??     441 C ; WORD BLOCK TO LOAD, SHOULD BE ZERO FOR NORMAL
0488 ??     442 C ; OPERATION
0488 ??     443 C ; WORD COUNT TO STORE, SHOULD BE 256D FOR NORMAL
0488 ??     444 C ; OPERATION
0488 ??     445 C ; WORD OPERATE OFFSET, SHOULD BE ZERO FOR NORMAL
0488 ??     446 C ; OPERATION
0488 ??     447 C ; WORD POINTER TO A FONT TABLE
0488 ??     448 C ; BYTE DISPLAYABLE ROM
0488 ??     449 C ; WORD MAXIMUM CALCULATED VALUE WILL BE
0488 ??     450 C ; USED. ELSE THIS VALUE WILL BE USED
0488 ??     451 C ; CONSECUTIVE BYTES OF MODE VALUES FOR WHICH
0488 ??     452 C ; THIS FONT DESCRIPTION IS TO BE USED.
0488 ??     453 C ; THE END OF THIS STREAM IS INDICATED BY A
0488 ??     454 C ; BYTE CODE OF 'FF'
0488 ??     455 C
0488 ??     456 C ; NOTE : USE OF THIS POINTER MAY CAUSE UNEXPECTED
0488 ??     457 C ; CURSOR TYPE OPERATION. FOR AN EXPLANATION
0488 ??     458 C ; OF CURSOR TYPE SEE AH = 01 IN THE INTERFACE
0488 ??     459 C ; SECTION.
0488 ??     460 C
0488 ??     461 C ; DWORD_4 GRAPHICS MODE AUXILIARY POINTER
0488 ??     462 C ; INITIALIZED TO 0000:0000. THIS VALUE IS OPTIONAL.
0488 ??     463 C ; WHEN NON-ZERO, THIS POINTER IS USED AS A POINTER
0488 ??     464 C ; TO A TABLES DESCRIBED AS FOLLOWS :
0488 ??     465 C
0488 ??     466 C ; BYTE DISPLAYABLE ROM
0488 ??     467 C ; WORD BYTES/CHARACTER
0488 ??     468 C ; DWORD POINTER TO A FONT TABLE
0488 ??     469 C ; BYTE CONSECUTIVE BYTES OF MODE VALUES FOR WHICH
0488 ??     470 C ; THIS FONT DESCRIPTION IS TO BE USED.
0488 ??     471 C ; THE END OF THIS STREAM IS INDICATED BY A
0488 ??     472 C ; BYTE CODE OF 'FF'
0488 ??     473 C
0488 ??     474 C ; DWORD_5 THRU DWORD_7 RESERVED AND SET TO 0000:0000.
0488 ??     475 C
0488 ??     476 C
0488 ??     477 C
0500 ????    478 C ; STATUS_BYTE ORG 0500H
0500 ????    479 C DB ?
0501 ????    480 C ABS0 ENDS
0501 ????    481 C
= 0061     482 C ; PORT_B EQU 61H ; 8255 PORT B ADDR
= 0040     483 C ; TIMER EQU 40H
0501 ????    484 C
0501 ????    485 C ;----- EQUATES FOR CARD PORT ADDRESSES
0501 ????    486 C
= 00C4     487 C ; SEQ_ADDR EQU 0C4H
= 00C5     488 C ; SEQ_DATA EQU 0C5H
= 00D1     489 C ; CRTC_ADDR EQU 0D1H
= 00B4     490 C ; CRTC_ADDR_B EQU 0B4H
= 00D5     491 C ; CRTC_DATA EQU 0D5H ; OR 0B5H
= 00CC     492 C ; GRAPH_1_POS EQU 0CCH
= 00CA     493 C ; GRAPH_2_POS EQU 0CAH
= 00CE     494 C ; GRAPH_ADDR EQU 0CEH
= 00CF     495 C ; GRAPH_OUTPUT EQU 0CFH
= 00C2     496 C ; MISC_OUTPUT EQU 0C2H
= 00C2     497 C ; INPUT_STATUS EQU 0C2H
= 00BA     498 C ; INPUT_STATUS_B EQU 0BAH
= 00DA     499 C ; INPUT_STATUS EQU 0DAH
= 00DA     500 C ; ATTR_READ EQU 0DAH
= 00C0     501 C ; ATTR_WRITE EQU 0COAH
0502 ????    502 C
0503 ????    503 C ;----- EQUATES FOR ADDRESS REGISTER VALUES
0504 C

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= 0000      505     C      S_RESET      EQU    00H
= 0001      506     C      S_CLOCK      EQU    01H
= 0002      507     C      S_IEN       EQU    02H
= 0003      508     C      S_CGEN      EQU    04H
= 0004      509     C      S_MEM       EQU    04H
= 0005      510
= 0006      511     C      C_HRZ_TOT   EQU    00H
= 0007      512     C      C_HRZ_CTRP  EQU    01H
= 0008      513     C      C_STRT_HRZ_BLK EQU    02H
= 0009      514     C      C_END_HRZ_BLK EQU    03H
= 000A      515     C      C_STRT_HRZ_SYN EQU    04H
= 000B      516     C      C_END_HRZ_SYN EQU    05H
= 000C      517     C      C_OVERFLOW   EQU    06H
= 000D      518     C      C_OVERFLOW_F EQU    07H
= 000E      519     C      C_PRE_ROW    EQU    08H
= 000F      520     C      C_MAX_SCAN_LN EQU    09H
= 0010      521     C      C_CURSOR_ART EQU    0AH
= 0011      522     C      C_CURSOR_END EQU    0BH
= 0012      523     C      C_STRT_HGH   EQU    0CH
= 0013      524     C      C_STRT_LW    EQU    0DH
= 0014      525     C      C_CURSOR_LOC_HGH EQU    0EH
= 0015      526     C      C_CURSOR_LOC_LW EQU    0FH
= 0016      527     C      C_VRT_SYN_STRT EQU    10H ; WRITE ONLY
= 0017      528     C      C_LIGHT_PEN_HGH EQU    10H ; READ ONLY
= 0018      529     C      C_VRT_SYN_END EQU    11H ; WRITE ONLY
= 0019      530     C      C_VRT_SYN_LOW  EQU    11H ; READ ONLY
= 0020      531     C      C_VRT_DBL_END EQU    12H
= 0021      532     C      C_OFFSET     EQU    13H
= 0022      533     C      C_UNDERLN_LOC EQU    14H
= 0023      534     C      C_STRT_VRT_BLK EQU    15H
= 0024      535     C      C_END_VRT_BLK EQU    16H
= 0025      536     C      C_MODE_CNTL   EQU    17H
= 0026      537     C      C_LN_COMP    EQU    18H
= 0027      538
= 0028      539     C      G_SET_RESET   EQU    00H
= 0029      540     C      G_ENBL_SET   EQU    01H
= 0030      541     C      G_CLR_COMP   EQU    02H
= 0031      542     C      G_DATA_ROT   EQU    03H
= 0032      543     C      G_READ_MAP   EQU    04H
= 0033      544     C      G_WRITE_MAP  EQU    05H
= 0034      545     C      G_MISC        EQU    06H
= 0035      546     C      G_COLOR       EQU    07H
= 0036      547     C      G_BIT_MASK   EQU    08H
= 0037      548
= 0038      549     C      P_MODE        EQU    10H
= 0039      550     C      F_OVERSC     EQU    11H
= 0040      551     C      F_CPLANE    EQU    12H
= 0041      552     C      F_HELP       EQU    13H
= 0042      553
= 0043      554     C      SUBTTL
= 0044      555
= 0045      556     C      ;----- CODE SEGMENT
= 0046      557
= 0047      558     C      CODE SEGMENT PUBLIC
= 0048      559     C      INCLUDE VPOST.INC
= 0049      560     C      SUBTTL VPOST.INC
= 0050      561     C      PAGE
= 0051      562
= 0052      563
= 0053      564
= 0054      565     C      ;----- POST
= 0055      566     C      ASSUME CS:CODE,DS:ABSO
= 0056      567     C      ORG OH
= 0057      568     C      DB 055H : SIGNATURE
= 0058      569     C      DB 0AAH : BYTES
= 0059      570     C      DB 020H : LENGTH INDICATOR
= 0060      571
= 0061      572     C      ;----- NOTE : DO NOT USE THE SIGNATURE BYTES AS A PRESENCE TEST
= 0062      573
= 0063      574     C      ;----- PLANAR VIDEO SWITCH SETTINGS
= 0064      575
= 0065      576     C      0 0 - UNUSED
= 0066      577     C      0 1 - 40 X 25 COLOR
= 0067      578     C      1 0 - 80 X 25 COLOR
= 0068      579     C      1 1 - 80 X 25 MONOCHROME
= 0069      580     C      ; NOTE : 0 0 MUST BE SET WHEN THIS ADAPTER IS INSTALLED.
= 0070      581
= 0071      582     C      ;----- VIDEO ADAPTER SWITCH SETTINGS
= 0072      583
= 0073      584     C      0 0 0 0 - MONOC PRIMARY, EGA COLOR, 40X25
= 0074      585     C      0 0 0 1 - MONOC PRIMARY, EGA COLOR, 80X25
= 0075      586     C      0 0 1 0 - MONOC PRIMARY, EGA HI RES EMULATE (SAME AS 0001)
= 0076      587     C      0 0 1 1 - MONOC PRIMARY, EGA HI RES ENHANCED
= 0077      588     C      0 1 0 0 - COLOR NO PRIMARY, EGA MONOCHROME
= 0078      589     C      0 1 0 1 - COLOR 80 PRIMARY, EGA MONOCHROME
= 0079      590
= 0080      591     C      0 1 1 0 - MONOC SECONDARY, EGA COLOR, 40X25
= 0081      592     C      0 1 1 1 - MONOC SECONDARY, EGA COLOR, 80X25
= 0082      593     C      1 0 0 0 - MONOC SECONDARY, EGA HI RES EMULATE (SAME AS 0111)
= 0083      594     C      1 0 0 1 - MONOC SECONDARY, EGA HI RES ENHANCED
= 0084      595     C      1 0 1 0 - COLOR NO SECONDARY, EGA MONOCHROME
= 0085      596     C      1 0 1 1 - COLOR 80 SECONDARY, EGA MONOCHROME
= 0086      597
= 0087      598     C      1 1 0 0 - RESERVED
= 0088      599     C      1 1 0 1 - RESERVED
= 0089      600     C      1 1 1 0 - RESERVED
= 0090      601     C      1 1 1 1 - RESERVED
= 0091      602
= 0092      603     C      ;----- SETUP ROUTINE FOR THIS MODULE
= 0093      604
= 0094      605     C      VIDEO_SETUP PROC FAR
= 0095      606     C      JMP SHORT L1
= 0096      607     C      DB 1200H
= 0097      608     C      DB '6277356 (C)COPYRIGHT IBM 1984'
= 0098      609
= 0099      610     C      DB '9/13/84'
= 0100      611
= 0101      612
= 0102      613
= 0103      614
= 0104      615
= 0105      616
= 0106      617     C      ;----- SET UP VIDEO VECTORS
= 0107      618
= 0108      619     C      L1:
= 0109      620     C      MOV DH, 3
= 0110      621     C      MOV AL, INPUT_STATUS
= 0111      622     C      IN AL, DX
= 0112      623     C      MOV DL, INPUT_STATUS_B
= 0113      624     C      MOV AL, DX
= 0114      625     C      MOV DL, ATTR_WRITE
= 0115      626     C      MOV AL, 0
= 0116      627     C      OUT DX, AL
= 0117      628
= 0118      629     C+     SRLOAD DS, 0
= 0119      630     C+     SUB DX, DX
= 0120      631     C+     MOV DS, DX

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003E FA 631 C CLI WORD PTR VIDEO_OFFSET COMBO_VIDEO
003F C7 06 0040 R OCD7 R 632 C MOV WORD PTR VIDEO+42 CS
0040 C7 06 0040 R 633 C MOV WORD PTR PLANAR_VIDEO_OF065H
0041 C7 06 010A R F000 634 C MOV WORD PTR PLANAR_VIDEO+2,0F000H
0042 C7 06 010A R 010C R 635 C
0043 C7 06 007C R 0000 E 636 C MOV WORD PTR SAVE_PTR,OFFSET SAVE_TBL
0044 C7 06 007C R 0000 E 637 C MOV WORD PTR EXT_PTR,OFFSET INT_1F_1
0045 C7 06 007C R 0000 E 638 C MOV WORD PTR EXT_PTR+2, CS
0046 C7 06 010C R 0000 E 639 C
0047 C8 0E 007E R 640 C MOV WORD PTR GRX_SET,OFFSET CGDDOT
0048 C8 0E 010E R 641 C MOV WORD PTR GRX_SET+2, CS
0049 C8 0E 010E R 642 C ST1
0049 C8 0E 010E R 643 C
0049 C8 0E 010E R 644 C ;---- POST FOR COMBO VIDEO CARD
0049 C8 0E 010E R 645 C
0049 C8 0E 010E R 646 C
0049 C8 0E 010E R 647 C MOV INFO_00000100B
0049 C8 0E 010E R 648 C CALL RD_INFO
0049 C8 0E 010E R 649 C MOV INFO_3_BL
0049 C8 0E 010E R 650 C CALL F_BTS
0049 C8 0E 010E R 651 C OR INFO_3_AL
0049 C8 0E 010E R 652 C MOV BL,INFO_3
0049 C8 0E 010E R 653 C CALL MK_ENV
0049 C8 0E 010E R 654 C JMP POST
0049 C8 0E 010E R 655 C SKIP: RET
0049 C8 0E 010E R 656 C VIDEO_SETUP ENDP
0049 C8 0E 010E R 657 C
0049 C8 0E 010E R 658 C
0049 C8 0E 010E R 659 C POR_1 PROC NEAR
0049 C8 0E 010E R 660 C OUT DL,AL
0049 C8 0E 010E R 661 C PUSH AX
0049 C8 0E 010E R 662 C POP AX
0049 C8 0E 010E R 663 C IN AL,DX
0049 C8 0E 010E R 664 C AND AL,010H
0049 C8 0E 010E R 665 C SHR AL,1
0049 C8 0E 010E R 666 C RET
0049 C8 0E 010E R 667 C POR_1 ENDP
0049 C8 0E 010E R 668 C
0049 C8 0E 010E R 669 C ;---- READ THE SWITCH SETTINGS ON THE CARD
0049 C8 0E 010E R 670 C RD_SWS PROC NEAR
0049 C8 0E 010E R 671 C ASSUME DS:ABSO
0049 C8 0E 010E R 672 C MOV DL,MISC_OUTPUT
0049 C8 0E 010E R 673 C MOV DL,MISC_OUTPUT
0049 C8 0E 010E R 674 C MOV AL,1
0049 C8 0E 010E R 675 C MOV AL,1
0049 C8 0E 010E R 676 C OUT DX,AL
0049 C8 0E 010E R 677 C
0049 C8 0E 010E R 678 C ;---- COULD BE 0,4,8,C
0049 C8 0E 010E R 679 C
0049 C8 0E 010E R 680 C MOV AL,0DH
0049 C8 0E 010E R 681 C CALL POR_1
0049 C8 0E 010E R 682 C SHR AL,1
0049 C8 0E 010E R 683 C SHR AL,1
0049 C8 0E 010E R 684 C SHR AL,1
0049 C8 0E 010E R 685 C MOV BL,AL
0049 C8 0E 010E R 686 C
0049 C8 0E 010E R 687 C MOV AL,9
0049 C8 0E 010E R 688 C CALL POR_1
0049 C8 0E 010E R 689 C SHR AL,1
0049 C8 0E 010E R 690 C SHR AL,1
0049 C8 0E 010E R 691 C OR BL,AL
0049 C8 0E 010E R 692 C
0049 C8 0E 010E R 693 C MOV AL,5
0049 C8 0E 010E R 694 C CALL POR_1
0049 C8 0E 010E R 695 C SHR AL,1
0049 C8 0E 010E R 696 C OR BL,AL
0049 C8 0E 010E R 697 C
0049 C8 0E 010E R 698 C MOV AL,1
0049 C8 0E 010E R 699 C CALL POR_1
0049 C8 0E 010E R 700 C OR BL,AL
0049 C8 0E 010E R 701 C
0049 C8 0E 010E R 702 C AND BL,0FH
0049 C8 0E 010E R 703 C RET
0049 C8 0E 010E R 704 C RD_SWS ENDP
0049 C8 0E 010E R 705 C
0049 C8 0E 010E R 706 C ;---- OBTAIN THE FEATURE BITS FROM DAUGHTER CARD
0049 C8 0E 010E R 707 C
0049 C8 0E 010E R 708 C F_BTS PROC NEAR
0049 C8 0E 010E R 709 C MOV DL,OBABH
0049 C8 0E 010E R 710 C MOV DL,OBABH
0049 C8 0E 010E R 711 C MOV AL,1
0049 C8 0E 010E R 712 C OUT DX,AL
0049 C8 0E 010E R 713 C MOV DL,ODAH
0049 C8 0E 010E R 714 C OUT DX,AL
0049 C8 0E 010E R 715 C MOV DL,IN_STAT_0
0049 C8 0E 010E R 716 C IN AL,DX
0049 C8 0E 010E R 717 C AND AL,060H ; READ FEATURE BITS
0049 C8 0E 010E R 718 C SHL AL,1
0049 C8 0E 010E R 719 C MOV BL,AL
0049 C8 0E 010E R 720 C MOV DL,OBABH
0049 C8 0E 010E R 721 C MOV AL,2
0049 C8 0E 010E R 722 C OUT DX,AL
0049 C8 0E 010E R 723 C MOV DL,ODAH
0049 C8 0E 010E R 724 C OUT DX,AL
0049 C8 0E 010E R 725 C MOV DL,IN_STAT_0 ; READ FEATURE BITS
0049 C8 0E 010E R 726 C IN AL,DX
0049 C8 0E 010E R 727 C AND AL,060H
0049 C8 0E 010E R 728 C SHL AL,1
0049 C8 0E 010E R 729 C OR AL,BL
0049 C8 0E 010E R 730 C
0049 C8 0E 010E R 731 C RET
0049 C8 0E 010E R 732 C F_BTS ENDP
0049 C8 0E 010E R 733 C
0049 C8 0E 010E R 734 C ;---- ESTABLISH THE VIDEO ENVIRONMENT, KEYED OFF OF THE SWITCHES
0049 C8 0E 010E R 735 C
0049 C8 0E 010E R 736 C MK_ENV PROC NEAR
0049 C8 0E 010E R 737 C ASSUME DS:ABSO
0049 C8 0E 010E R 738 C SUB BH,BH
0049 C8 0E 010E R 739 C AND BL,0FH
0049 C8 0E 010E R 740 C SAL BX,1
0049 C8 0E 010E R 741 C PUSH DH
0049 C8 0E 010E R 742 C MOV DH,3
0049 C8 0E 010E R 743 C MOV AH,DH
0049 C8 0E 010E R 744 C POP DX
0049 C8 0E 010E R 745 C ADD AH,1
0049 C8 0E 010E R 746 C INC AH
0049 C8 0E 010E R 747 C NOT AH
0049 C8 0E 010E R 748 C JMP WORD PTR CS:[BX + OFFSET T5]
010C 0717 R 749 C SAVE_TBL LABEL DWORD
010C 0717 R 750 C DW OFFSET VIDEO_PARMS ; PARMs
010E C000 751 C DW 00C00H ; PARMs
0110 0000 752 C DW 0 ; PAL SAVE AREA
0111 0000 753 C DW 0 ; PAL SAVE AREA
0114 0000 754 C DW 0 ; ALPHA TABLES
0116 0000 755 C DW 0 ; ALPHA TABLES
0118 0000 756 C DW 0 ; GRAPHICS TABLES

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011A	0000	757	C	DW	0	
011C	0000	758	C	DW	0	; GRAPHICS TABLES
011E	0000	759	C	DW	0	
0120	0000	760	C	DW	0	
0122	0000	761	C	DW	0	
0124	0000	762	C	DW	0	
0126	0000	763	C	DW	0	
		764	C	DW	0	
		765	C	DW	0	
0128	0171 R	766	C	T5	LABEL WORD	
0128	0172 R	767	C	DW	OFFSET PST_0	
0128	0173 R	768	C	DW	OFFSET PST_1	
0128	0174 R	769	C	DW	OFFSET PST_2	
0128	0189 R	770	C	DW	OFFSET PST_3	
0130	0194 R	771	C	DW	OFFSET PST_4	
0132	01A8 R	772	C	DW	OFFSET PST_5	
0134	01BC R	773	C	DW	OFFSET PST_6	
0136	01C7 R	774	C	DW	OFFSET PST_7	
		775	C			
0138	01C7 R	776	C	DW	OFFSET PST_8	
0138	01D0 R	777	C	DW	OFFSET PST_9	
013C	01DD R	778	C	DW	OFFSET PST_A	
013E	01F1 R	779	C	DW	OFFSET PST_B	
0140	0204 R	780	C	DW	OFFSET PST_OUT	
0142	0204 R	781	C	DW	OFFSET PST_OUT	
0144	0204 R	782	C	DW	OFFSET PST_OUT	
0146	0204 R	783	C	DW	OFFSET PST_OUT	
		784	C			
0148		785	C	ENV_X	PROC NEAR ; SET 40X25 COLOR ALPHA	
0148	80 26 0410 R CF	786	C	AND OR	EQUIP_LOW,0CFH	
0148	80 0E 0410 R 10	787	C	OR OR	EQUIP_LOW,010H	
0152	88 0001	788	C	MOV	AX,1H	
0155	CD 10	789	C	INT	10H	
0157	C3	790	C	RET		
0158		791	C	ENDP		
0158		792	C			
0158		793	C	ENV_0	PROC NEAR ; SET 80X25 COLOR ALPHA	
0158	80 26 0410 R CF	794	C	AND OR	EQUIP_LOW,0CFH	
0158	80 0E 0410 R 20	795	C	OR OR	EQUIP_LOW,020H	
0162	88 0003	796	C	MOV	AX,03H	
0165	CD 10	797	C	INT	10H	
0167	C3	798	C	RET		
0168		799	C	ENV_0	ENDP	
0168		800	C			
0168		801	C	ENV_3	PROC NEAR ; SET MONOCHROME ALPHA	
0168	80 0E 0410 R 30	802	C	OR OR	EQUIP_LOW,030H	
0169	BB 0007	803	C	MOV	AX,07H	
0170	CD 10	804	C	INT	10H	
0172	C3	805	C	RET		
0173		806	C	ENV_3	ENDP	
0173		807	C			
0173		808	C	PST_0:		
0173		809	C	AND	INFO_AH	
0177	E8 0148 R	810	C	CALL	ENV_X	
0178	E8 0168 R	811	C	CALL	ENV_3	
0179	C3	812	C	RET		
017E		813	C	PST_1:		
017E	20 26 0487 R	814	C	AND	INFO_AH	
0182	E8 0154 R	815	C	CALL	ENV_0	
0182	E8 0168 R	816	C	CALL	ENV_3	
0188	C3	817	C	RET		
0189		818	C	PST_2:		
0189	20 26 0487 R	819	C	AND	INFO_AH	
0182	E8 0154 R	820	C	CALL	ENV_0	
0190	E8 0168 R	821	C	CALL	ENV_3	
0193	C3	822	C	RET		
0194		823	C	PST_3:		
0194	B6 03	824	C	AND	INFO_AH	
0196	B2 C2	825	C	CALL	ENV_0	
0198	B0 00	826	C	CALL	ENV_3	
019A	EE	827	C	RET		
019B	F6 D4	828	C	PST_4:		
019B	F6 D4	829	C	AND	INFO_AH	
019B	F6 D4	830	C	CALL	ENV_0	
019B	00 26 0487 R	831	C	CALL	ENV_3	
01A1	E8 0168 R	832	C	NOT	AH	
01A4	E8 0148 R	833	C	CALL	INFO_AH	
01A7	C3	834	C	CALL	ENV_X	
01A9		835	C	RET		
01AA	B6 C2	836	C	PST_5:		
01AA	B2 C2	837	C	MOV	DH,3	
01AC	B0 00	838	C	MOV	DL,MISC_OUTPUT	
01AE	EE	839	C	MOV	AL,0	
01AF	F6 D4	840	C	OUT	DX,AL	
01B1	08 26 0487 R	841	C	NOT	AH	
01B5	E8 0168 R	842	C	CALL	INFO_AH	
01B8	E8 0154 R	843	C	CALL	ENV_X	
01C1	C3	844	C	RET		
01C9		845	C	PST_6:		
01BC	20 26 0487 R	846	C	AND	INFO_AH	
01CO	E8 0168 R	847	C	CALL	ENV_3	
01C3	E8 0148 R	848	C	CALL	ENV_X	
01C6	C3	849	C	RET		
01C7		850	C	PST_7:		
01C7	20 26 0487 R	851	C	AND	INFO_AH	
01C7	E8 0168 R	852	C	CALL	ENV_3	
01CE	E8 0154 R	853	C	CALL	ENV_O	
01D1	C3	854	C	RET		
01D2		855	C	PST_8:		
01D2	20 26 0487 R	856	C	AND	INFO_AH	
01D2	E8 0168 R	857	C	CALL	ENV_3	
01D9	E8 0154 R	858	C	CALL	ENV_O	
01DC	C3	859	C	RET		
01DD		860	C	PST_A:		
01DD	B6 03	861	C	MOV	DH,3	
01DF	B2 C2	862	C	MOV	DL,MISC_OUTPUT	
01E1	B0 00	863	C	MOV	AL,0	
01E3	EE	864	C	OUT	DX,AL	
01E3	F6 D4	865	C	NOT	AH	
01E4	E8 0148 R	866	C	CALL	INFO_AH	
01E4	08 26 0487 R	867	C	CALL	ENV_X	
01EA	E8 0148 R	868	C	CALL	ENV_3	
01ED	E8 0168 R	869	C	CALL	ENV_X	
01FO	C3	870	C	RET		
01F1	B6 03	871	C	PST_B:		
01F3	B2 C2	872	C	MOV	DH,3	
01F5	B0 00	873	C	MOV	DL,MISC_OUTPUT	
01F6	EE	874	C	MOV	AL,0	
01FB	F6 D4	875	C	OUT	DX,AL	
01FA	08 26 0487 R	876	C	NOT	AH	
01FE	E8 0154 R	877	C	CALL	INFO_AH	
0201	E8 0168 R	878	C	CALL	ENV_O	
0204	C3	879	C	CALL	ENV_3	
0205		880	C	PST_OUT:		
		881	C	MK_ENV	RET	
		882	C	ENDP		

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883 C : THIS ROUTINE TESTS THE CRT CARD INTERNAL DATA BUS AND IN A LIMITED
884 C : WAY TESTS THE CRTC VIDEO CHIP BY WRITING/READING FROM CURSOR REGISTER
885 C : CARRY IS SET IF AN ERROR IS FOUND
886 C : REGISTERS BX, SI, ES, DS ARE PRESERVED.
887 C : REGISTERS AX, CX, DX ARE MODIFIED.
888 C : CD_PRESENCE_TST PROC NEAR
889 C :     PUSH BX ; SAVE BX
890 C :     MOV AX, 07FH ; INITIAL WORD PATTERN BYTE
891 C :     PUSH AX ; SAVE PORT ADDRESS
892 C :     CALL RD_CURSOR ; SAVE ORIGINAL VALUE
893 C :     MOV SI, AX ; RECOVER PORT VALUE
894 C :     POP AX ; SAVE PORT ADDRESS
895 C :     PUSH AX ; WRITE CURSOR
896 C :     CALL RD_CURSOR ; RECOVER PORT ADDRESS
897 C :     POP AX ; SAVE PORT ADDRESS
898 C :     CMP AX, DI ; READ IT BACK
899 C :     JNZ NOT_PRESENT ; SAME?
900 C :     POP AX ; EXIT IF NOT EQUAL
901 C :     PUSH AX ; NOT_PRESENT: TST_EX
902 C :     CALL RD_CURSOR ; SET PRESENT ON EXIT
903 C :     POP AX ; RESTORE BX
904 C :     POP BX ; SET PRESENT ON EXIT
905 C :     RET ; RESTORE BX
906 C :     CMP AX, DI ; SAME?
907 C :     JNZ NOT_PRESENT ; EXIT IF NOT EQUAL
908 C :     POP AX ; NOT_PRESENT: TST_EX
909 C :     XOR AX, AX ; SET NOT PRESENT
910 C :     POP BX ; RESTORE BX
911 C :     RET ; RESTORE BX
912 C : TST_EX:
913 C :     MOV AX, 1 ; SET PRESENT ON EXIT
914 C :     POP BX ; RESTORE BX
915 C :     RET ; RESTORE BX
916 C :     CD_PRESENCE_TST ENDP
917 C :
918 C : RD_CURSOR PROC NEAR
919 C :     PUSH DX ; SAVE REGS USED
920 C :     MOV AX, 0DE ; MODULE NAME RD_CURSOR
921 C :     OUT DX, AL ; READ CURSOR POSITION [ADDRESS] (FROM CRTC) TO AX
922 C :     INC DX ; REGISTER AX IS MODIFIED.
923 C :     RET ; RESTORE REGS USED
924 C : RD_CURSOR PROC NEAR
925 C :     PUSH DX ; SAVE REGS USED
926 C :     MOV AX, 0DE ; MODULE NAME RD_CURSOR
927 C :     OUT DX, AL ; READ CURSOR POSITION [ADDRESS] (FROM CRTC) TO AX
928 C :     INC DX ; REGISTER AX IS MODIFIED.
929 C :     INC DX ; RETURN WITH CURSOR POS IN AX
930 C :     IN AL, DX ; RESTORE REGS USED
931 C :     RET ; RESTORE REGS USED
932 C : RD_CURSOR ENDP
933 C : RD_CURSOR PROC NEAR
934 C :     PUSH DX ; SAVE REGS USED
935 C :     RET ; RESTORE REGS USED
936 C : RD_CURSOR ENDP
937 C :
938 C : WR_CURSOR PROC NEAR
939 C :     PUSH AX ; SAVE REGS USED
940 C :     MOV AH, 0DE ; MODULE NAME WR_CURSOR
941 C :     OUT AL, 07FH ; WRITE CURSOR POSITION [ADDRESS] (TO CRTC) WITH CONTENTS OF AX
942 C :     INC AX ; ALL REGISTERS PRESERVED
943 C :     RET ; RESTORE REGS USED
944 C : WR_CURSOR PROC NEAR
945 C :     PUSH AX ; SAVE REGS USED
946 C :     PUSH DX ; SAVE REGS USED
947 C :     MOV AX, 0DE ; MODULE NAME WR_CURSOR
948 C :     OUT DX, AX ; WRITE CURSOR POSITION [ADDRESS] (TO CRTC) WITH CONTENTS OF AX
949 C :     INC AX ; ALL REGISTERS PRESERVED
950 C :     MOV AH, C_CURSOR_LOC_HGH ; CURSOR LOCATION HIGH INDEX
951 C :     MOV AL, 07FH ; TEST VALUE
952 C :     CALL OUT_DX ; RETURN WITH CURSOR POS IN AX
953 C :     RET ; RESTORE REGS USED
954 C : WR_CURSOR ENDP
955 C : POST:
956 C :     PUSH AX ; INITIALIZE AND START CRT CONTROLLER (6845)
957 C :     MOV AH, 0DE ; ON COLOR GRAPHICS AND MONOCHROME CARDS
958 C :     TEST VIDEO READ/WRITE STORAGE.
959 C :     DESCRIPTION:
960 C :     TEST THE VIDEO ENABLE SIGNAL.
961 C :     SELECT ALPHANUMERIC MODE, NO * 25, B & W.
962 C :     READ/WRITE DATA PATTERNS TO STG, CHECK STG
963 C :     ADDRESSABILITY.
964 C :     ADDRESS: DS:ABSO,ES:ABSO
965 C :     CALL DOS
966 C :     TEST INFO_2
967 C :     JNZ COLOR_PRESENCE_TST
968 C :     MOV AH, 0BH
969 C :     CALL CD_PRESENCE_TST
970 C :     CMP AX, 1
971 C :     JE CONT1
972 C :     JMP POD14
973 C :     CONT1:
974 C :     MOV AH, 30H ; MONOCHROME CARD INSTALLED
975 C :     JMP SHORT_OVER
976 C :     COLOR_PRESENCE_TST:
977 C :     MOV AH, 03DH ; COLOR CARD INSTALLED
978 C :     CALL CD_PRESENCE_TST
979 C :     CMP AX, 1
980 C :     JE CONT2
981 C :     JMP POD14
982 C :     CONT2:
983 C :     MOV AH, 20H ; COLOR GRAPHICS CARD INSTALLED
984 C :     OVER:
985 C :     PUSH AX ; RESAVE VALUE.
986 C :     MOV BX, 0B0000H ; COLOR RAM ADDR B/W CO
987 C :     MOV DX, 3B80H ; MODE CONTROL B/W CO
988 C :     MOV CX, 4096 ; RAM BYTE CNT FOR B/W CD
989 C :     MOV AL, 1 ; SET MODE FOR BW CARD
990 C :     CMP AH, 30H ; VIDEO MODE NEEDED?
991 C :     JE END ; YES - GO TEST VIDEO STG
992 C :     CALL BP, DS:RESET_FLAG ; BEG VIDEO RAM ADDR COLOR CD
993 C :     DEC AL ; MODE CONTROL COLOR
994 C :     MOV BH, 0B8H ; RAM BYTE CNT FOR COLOR CD
995 C :     MOV DL, 0D8H ; SET MODE FOR COLOR CD
996 C :     MOV CH, 40H ; TEST VIDEO STG
997 C :     CMP AX, 1 ; DISABLE VIDEO FOR COLOR CD
998 C :     JE END ; POINT ES TO VIDEO RAM STG
999 C :     OUT DX, AL ; POINT ES TO VIDEO RAM STG
1000 C :     MOV BP, DS:RESET_FLAG ; POD INITIALIZED BY KBD RESET
1001 C :     RET ; POD INITIALIZED BY KBD RESET?
1002 C :     E9: OUT DX, AL ; POINT ES TO VIDEO RAM STG
1003 C :     MOV BP, DS:RESET_FLAG ; POD INITIALIZED BY KBD RESET?
1004 C :     RET ; POD INITIALIZED BY KBD RESET?
1005 C :     MOV BP, 1234H ; POINT ES TO VIDEO RAM STG
1006 C :     CMP BP, 1234H ; POINT ES TO VIDEO RAM STG
1007 C :     MOV ES, BX ; POINT ES TO VIDEO RAM STG
1008 C :

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0294 74 07          1009   JE    E10      ; YES - SKIP VIDEO RAM TEST
0296 8E DB          1010   MOV    DS,BX    ; POINT DS TO VIDEO RAM STG
0298 E8 02DF R      1011   ASSUME DS:NOSHING,ES:NOSHING
029B 75 2E          1012   CALL   STGTST_CNT    ; GO TEST VIDEO R/W STG
1013   JNE    E17      ; R/W STG FAILURE - BEEP SPK
1014
1015
1016
1017
1018
1019
029D
029D 58            1020
029E 50            1021
029F B8 7020        1022   POP    AX      ; GET VIDEO SENSE SWS (AH)
02A0 2B FF          1023   PUSH   AX      ; SAVE IT
02A1 B0 0028        1024   MOV    AX,7020H   ; WRT BLANKS IN REVERSE VIDEO
02A4 B3 AB          1025   SUB    DI,DI    ; SETUP STARTING LOC
02A7 F3 AB          1026   HLT    CX=00    ; NO. OF BLANKS TO DISPLAY
1027   REP    STOSB   ; WRITE VIDEO STORAGE
1028
1029
1030
1031
1032
02A9 58            1033   POP    AX      ; GET VIDEO SENSE SW INFO
02AA 59            1034   PUSH   AX      ; B/W CARD ATTACHED?
02AB 80 FC 30        1035   CMP    AH,30H    ; SETUP ADDR OF BW STATUS PORT
02AE BA 03BA        1036   MOV    DX,03BAH   ; YES - GO TEST LINES
02B1 74 02            1037   JE    E11      ; COLOR_CARD IS ATTACHED
02B2 B2 DA          1038   MOV    DL,ODAH   ; LINE_TST:
02B5 00 00            1039
02B5 B4 08            1040
02B7 00 00            1041
02B8 2B C9          1042
02B9 00 00            1043
02B9 EC            1044   IN     AL,DX    ; READ CRT STATUS PORT
02BA 22 C4          1045   AND    AL,AH    ; CHECK VIDEO/HORZ LINE
02BC 75 04            1046   JNZ    E14      ; ITS ON - CHECK IF IT GOES OFF
02C0 EB F9          1047   LOOP   E13      ; LOOP TILL ON OR TIMEOUT
02C0 EB 03          1048   JHP    SHORT E17    ; GO PRINT ERROR MSG
02C2 00 00            1049
02C2 2B C9          1050
02C3 00 00            1051
02C4 EC            1052
02C5 22 C4          1053
02C7 74 0A            1054
02C9 E2 F9          1055
02C9 00 00            1056
02CB BA 0102        1057
02CE E8 06CB R      1058   MOV    DX,102H    ; GO BEEP SPEAKER
02D1 EB 06            1059   CALL   ERR_BEEP   ; NXT_LINE
02D3 00 00            1060
02D4 B1 03            1061
02D5 D2 EC          1062   SHR    AH,CL    ; GO CHECK HORIZONTAL LINE
02D7 75 DE          1063   JNZ    E12      ; DISPLAY CURSOR:
02D9 00 00            1064   POP    AX      ; GET VIDEO SENSE SWS (AH)
02D9 58            1065
02DA EB 3B          1066
1067
1068
1069
1070
1071
1072
1073
1074
1075
1076
1077
1078
1079
1080
1081
1082
02DC
02DC B9 4000        1083   STGTST PROC    NEAR DS:4000H ; SETUP CNT TO TEST A 16K BLK
02DF
02DF FC            1084   MOV    DS,BX    ; SET DIR FLAG TO INCREMENT
02DF D0            1085   CLD    DS:BX    ; SAVE CNT (4K FOR VIDEO OR 16K)
02E2 B8 AAAA        1086   MOV    BX,CX    ; GET DATA PATTERN TO WRITE
02E5 BA FF55        1087   MOV    AX,0AAAAAH   ; SETUP OTHER DATA PATTERNS TO REG
02E8 2B FF          1088   MOV    DX,0FF55H   ; DI = OFFSET O RELATIVE TO ES REG
02E9 F3 AA          1089   SUB    DI,DI    ; WRITE STORAGE LOCATIONS
02EC 00 00            1090   REP    STOSB   ; SET DIR FLAG TO GO BACKWARDS
02ED 4F            1091
02ED FD            1092
02EE 00 00            1093   C3:   DEC    DS:SI    ; POINT TO LAST BYTE JUST WRITTEN
02F0 BB F7          1094   STD    DS:SI    ; SET DIR FLAG TO GO BACKWARDS
02F0 BB CB          1095
02F2 00 00            1096
02F2 AC            1097
02F2 32 C4          1098
02F4 1E            1099
02F5 8A C2          1100
02F9 AA            1101
02FA E2 F6          1102   LODSB DS:BX    ; INNER TEST LOOP
02FC 22 EN          1103   XCHG AX,AL    ; READ OLD TEST BYTE [SI] +
02FE 74 13          1104   MOV    AL,DL    ; DATA READ AS EXPECTED ?
0300 8A E0            1105   ANI    AH,AH    ; NO - CALL END READ ROUTINE
0302 86 F2            1106   JNC    C7       ; GET NEXT DATA PATTERN TO WRITE
0303 86 E0            1107   STOSB DS:BX    ; WRITE INTO LOCATION JUST READ
0306 75 04            1108   LOOP   C5       ; DECREMENT COUNT AND LOOP CX
0308 8A D4            1109
030A EB E0            1110   AND    AH,AH    ; ENDING O PATTERN WRITTEN TO STG?
030A EB E0            1111   JZ    C6       ; YES - RETURN TO CALLER WITH AL=0
030C 47            1112   MOV    AH,AL    ; SETUP NEW VALUE FOR COMPARE
030D 47            1113   XCHG DH,DL    ; MOVE NEXT DATA PATTERN TO DL
030E 74 DE            1114   ANI    AH,AH    ; READ ZEROES FROM STG THIS PASS ?
0310 4F            1115   JNC    C7       ; CONTINUE TEST SEQUENCE TILL 0
0311 EB D9            1116   MOV    DL,AH    ; ELSE SET 0 FOR END READ PATTERN
0313 80 00            1117   C3:   INC    DI       ; AND MAKE FINALS BACKWARDS PASS
0313 80 00            1118   CLD    DS:BX    ; SET DIR FLAG TO GO FORWARD
0315 FC            1119
0315 FC            1120
0316 C3            1121
0317 1122
0317 1123
0317 1124
0317 1125
0317 1126
0317 1127
0317 1128
0317 1129
0317 1130
0317 1131
0317 1132
0317 1133
0317 1134

1. INIT CRT TO 40X25 - BW ****SET TO MODE****  

2. CHIPSET VERTICAL AND VIDEO ENABLES, AND CHECK  

3. TIMING OF CRT SIGNALS  

4. CHECK VERTICAL INTERRUPT  

5. CHECK RED, BLUE, GREEN, AND INTENSIFY DOTS

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1135 C ; 5. INIT TO 40X25 - COLOR/MONO ****SET TO MODE**** : 
1136 C ;----- 
1137 C ;----- NOMINAL TIME IS B286H FOR 60 HZ. 
1138 C ;----- NOMINAL TIME IS A2FEH FOR 50 HZ. 
1139 C ;----- 
1140 C MAX_VERT_COLOR EQU 0AA0H ; MAX TIME FOR VERT/VERT 
1141 C MIN_VERT_COLOR EQU 0C460H ; (NOMINAL + 10%) 
1142 C ;----- MAX TIME FOR VERT/VERT 
1143 C ;----- (NOMINAL - 10%) 
1144 C ;----- NUM OF ENABLES PER FRAME 
1145 C CENAB_PER_FRAME EQU 200 ; MAX TIME FOR VERT/VERT 
1146 C MAX_VERT_MONO EQU 0B099H ; (NOMINAL + 10%) 
1147 C ;----- MAX TIME FOR VERT/VERT 
1148 C MIN_VERT_MONO EQU 0B862H ; (NOMINAL - 10%) 
1149 C ;----- ENHANCED ENABLES PER FRAME 
1150 C FENAB_PER_FRAME EQU 350 ; ENHANCED ENABLES PER FRAME 
1151 C MENAB_PER_FRAME EQU 350 ; NUM OF ENABLES PER FRAME 
1152 C ;----- 
1153 C TIM_CTL EQU 043H ; 8253 TIMER CONTROL PORT 
1154 C TMR0 EQU 040H ; 8253 TIMER/CNTER 0 PORT 
1155 C ;----- 
1156 C ;----- RESERVE 5 WORDS ON STACK 
1157 C SUB SP,0AH ; INIT SCRATCH PAD POINTER 
1158 C MOV BP,SP ;----- 
1159 C ;----- 
1160 C ASSUME DS:ABSO,ES:ABSO ;----- 
1161 C CALL DDS ;----- 
1162 C MOV AL,0011000B ; SET TIMER 0 TO MODE 0 
1163 C ;----- 
1164 C OUT TIM_CTL,AL ;----- 
1165 C MOV AL,0DH ;----- 
1166 C OUT DL,0E0H,AL ; SEND FIRST BYTE TO TIMER 
1167 C TEST INT,2 ;----- 
1168 C JZ COLOR_EGA_V ;----- 
1169 C CALL ENV_3 ;----- 
1170 C MOV WORD PTR [BP][2],MENAB_PER_FRAME ; SET UP IN MONOCHROME 
1171 C MOV WORD PTR [BP][4],MAX_VERT_MONO ; NUM. OF FRAMES FOR MONO 
1172 C MOV WORD PTR [BP][6],MIN_VERT_MONO ; MAX TIME FOR VERT/VERT 
1173 C MOV DL,CRTC_ADDR_B ;----- 
1174 C MOV AH,C_HRZ_DSP_B ;----- 
1175 C MOV AL,A2FEH ;----- 
1176 C CALL OUT_DX ;----- 
1177 C MOV DL,_INPUT_STATUS_B ;----- 
1178 C JMP SHORT COMMON ;----- 
1179 C ;----- 
1180 C COLOR_EGA_V: CALL ENV_X ;----- 
1181 C CALL BRST_DET ;----- 
1182 C JNC COLOR_V ;----- 
1183 C MOV DL,CRTC_ADDR ;----- 
1184 C MOV AH,01H ;----- 
1185 C MOV AL,20H ;----- 
1186 C CALL OUT_DX ;----- 
1187 C MOV WORD PTR [BP][2],EENAB_PER_FRAME ;----- 
1188 C JMP BRST_COLOR_V ;----- 
1189 C ;----- 
1190 C COLOR_V: ;----- 
1191 C MOV WORD PTR [BP][2],CENAB_PER_FRAME ;----- 
1192 C BRST_COLOR_V: ;----- 
1193 C MOV WORD PTR [BP][4],MAX_VERT_COLOR ;----- 
1194 C MOV WORD PTR [BP][6],MIN_VERT_COLOR ;----- 
1195 C MOV DL,_INPUT_STATUS ;----- 
1196 C ;----- 
1197 C COMMON: ;----- 
1198 C MOV AX,0500H ;----- 
1199 C INT 10H ;----- 
1200 C SUB CX,CX ;----- 
1201 C ;----- LOOK FOR VERTICAL 
1202 C ;----- 
1203 C 037E EC ;----- 
1204 C 037F A8 08 ;----- 
1205 C 0381 E5 07 ;----- 
1206 C 0383 E2 F9 ;----- 
1207 C 0385 B3 00 ;----- 
1208 C 0387 E9 0448 R ;----- 
1209 C ;----- 
1210 C 0377 B8 0500 ;----- 
1211 C 037A CD 10 ;----- 
1212 C 037C 2B C9 ;----- 
1213 C ;----- GOT VERTICAL - START TIMER 
1214 C ;----- 
1215 C 038A B0 00 ;----- 
1216 C 038C E6 40 ;----- 
1217 C ;----- 
1218 C 038E 2B DB ;----- 
1219 C ;----- 
1220 C 0390 33 C9 ;----- 
1221 C 0392 EC ;----- 
1222 C 0393 A8 08 ;----- 
1223 C 0394 E5 07 ;----- 
1224 C 0397 E2 F9 ;----- 
1225 C 0399 B3 01 ;----- 
1226 C 039B E9 0448 R ;----- 
1227 C ;----- 
1228 C ;----- NOW START LOOKING FOR ENABLE TRANSITIONS 
1229 C ;----- 
1230 C 039E 2B C9 ;----- 
1231 C 039F A8 08 ;----- 
1232 C 03A0 EC ;----- 
1233 C 03A1 A8 01 ;----- 
1234 C 03A3 74 15 ;----- 
1235 C 03A4 E5 07 ;----- 
1236 C 03A7 A8 05 ;----- 
1237 C 03A8 E5 23 ;----- 
1238 C 03A9 E2 F5 ;----- 
1239 C 03AB B3 02 ;----- 
1240 C 03AD E9 0448 R ;----- 
1241 C ;----- 
1242 C 0380 B3 03 ;----- 
1243 C 0382 E9 0448 R ;----- 
1244 C ;----- 
1245 C 0385 B3 04 ;----- 
1246 C 0387 E9 0448 R ;----- 
1247 C ;----- 
1248 C ;----- MAKE SURE VERTICAL WENT OFF WITH ENABLE GOING ON 
1249 C ;----- 
1250 C 038A A8 08 ;----- 
1251 C 038C 75 F2 ;----- 
1252 C ;----- 
1253 C 03BE EC ;----- 
1254 C 03BF A8 01 ;----- 
1255 C 03C1 E1 FB ;----- 
1256 C 03C3 E3 FO ;----- 
1257 C ;----- 
1258 C 0354 IN AL,DX ;----- 
1259 C TEST AL,00000001B ;----- 
1260 C JNZ POD14_4A ;----- 
1254 C ;----- NOW WAIT FOR ENABLE TO GO OFF ;----- 
1255 C ;----- 
1256 C ;----- 
1257 C TEST AL,00000001B ;----- 
1258 C LOOP POD14_6 ;----- 
1259 C JCXZ POD14_4B ;----- 
1260 C ;----- ENABLE HAS TOGGLED, BUMP COUNTER AND TEST FOR NEXT VERTICAL

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03C5 1261 C POD14_7:
03C5 1262 C INC BX
03C6 1263 C JZ POD14_75
03C6 1264 C TEST AL,000001000B
03C8 1265 C JZ POD14_3
03CA 1266 C JZ POD14_75
03C5 1267 C ;----- HAVE HAD COMPLETE VERTICAL-VERTICAL CYCLE
03C5 1268 C ;----- BUMP ENABLE COUNTER
03C5 1269 C ; IF COUNTER WRAPS,
03C5 1270 C ; SOMETHING IS WRONG
03C5 1271 C ; DID ENABLE GO LOW
03C5 1272 C ; BECAUSE OF VERTICAL
03C5 1273 C ; IT NEEDS TO LOOK FOR ANOTHER
03C5 1274 C ; ENABLE TOGGLE
03C5 1275 C ; NOW TEST RESULTS
03CC 1276 C ;----- LATCH TIMER0
03CE 1277 C ; OUT AL,00
03CE 1278 C ; CMP BX,WORD PTR[BP][2]
03D0 1279 C ; NUMBER OF ENABLES BETWEEN
03D0 1280 C ; VERTICALS O.K.?
03D3 1281 C ;----- JE POD14_8
03D5 1282 C ; MOV BL,05H
03D7 1283 C ; JMP SHORT POD14_ERR
03D9 1284 C ;----- IN AL,TIMER0
03D9 1285 C ; MOV AH,AL
03D9 1286 C ; XCHG AH,AL
03D9 1287 C ; NOP
03D9 1288 C ; CMP AX,WORD PTR[BP][4]
03D9 1289 C ; JMP SHORT POD14_ERR
03D9 1290 C ;----- GET TIMER VALUE LOW
03D9 1291 C ;----- SAVE IT
03D9 1292 C ;----- IN AL,TIMER0
03D9 1293 C ; MOV BL,06H
03D9 1294 C ; JMP SHORT POD14_ERR
03D9 1295 C ;----- GET TIMER HIGH
03D9 1296 C ;----- MAXIMUM VERTICAL TIMING
03D9 1297 C ;----- SEE IF RED, GREEN, BLUE AND INTENSIFY DOTS WORK
03D9 1298 C ;----- FIRST, SET A LINE OF REVERSE VIDEO, INTENSIFIED BLANKS INTO BUFFER
03F6 1299 C ;----- POD14_10:
03F6 1300 C ; MOV AX,09DBH
03F9 1301 C ; MOV BX,000FFH
03FC 1302 C ;----- WRITE CHARS, BLANKS
03FC 1303 C ;----- PAGE 0, REVERSE VIDEO,
03FF 1304 C ;----- HIGH INTENSITY
0401 1305 C ;----- 80 CHARACTERS
0402 1306 C ;----- INT 10H
0403 1307 C ;----- IN AL,DX
0405 1308 C ;----- PUSH DX
0407 1309 C ;----- CALL DL,ATTR_WRITE
0409 1310 C ;----- MOV AH,0FH
0409 1311 C ;----- POP DX
0409 1312 C ;----- POP AX
0410 1313 C ;----- POP CX
0410 1314 C ;----- PUSH AX
0410 1315 C ;----- PUSH DX
0412 1316 C ;----- MOV DL,ATTR_WRITE
0414 1317 C ;----- MOV AH,32H
0416 1318 C ;----- CALL OUT_DX
0419 1319 C ;----- POP DX
0419 1320 C ;----- POP AX
0418 1321 C ;----- SUB CX,CX
0418 1322 C ;----- SEE IF DOT COMES ON
0418 1323 C ;----- POD14_13:
0418 1324 C ;----- IN AL,DX
0418 1325 C ;----- TEST AL,00110000B
0420 1326 C ;----- JNZ POD14_15
0422 1327 C ;----- LOOP POD14_14
0424 1328 C ;----- MOV BL,10H
0426 1329 C ;----- OR BL,AH
0428 1330 C ;----- JMP POD14_ERR
0428 1331 C ;----- SEE IF DOT GOES OFF
0428 1332 C ;----- POD14_15:
0428 1333 C ;----- SUB CX,CX
0420 1334 C ;----- IN AL,DX
0420 1335 C ;----- TEST AL,00110000B
0424 1336 C ;----- JE POD14_17
0430 1337 C ;----- LOOP POD14_16
0432 1338 C ;----- MOV BL,20H
0434 1339 C ;----- OR BL,AH
0438 1340 C ;----- JMP SHORT POD14_ERR
0434 1341 C ;----- SEE IF DOT GOES ON
0434 1342 C ;----- ADJUST TO POINT TO NEXT DOT
0434 1343 C ;----- POD14_17:
0434 1344 C ;----- INC AH
0434 1345 C ;----- CMP AH,030H
0434 1346 C ;----- JE POD14_18
0434 1347 C ;----- OR AH,0FH
0434 1348 C ;----- MOV AL,AH
0434 1349 C ;----- JMP POD14_13
0448 1350 C ;----- POD14_ERR:
0448 1351 C ;----- INC AH
0448 1352 C ;----- CMP AH,030H
0448 1353 C ;----- JE POD14_18
0448 1354 C ;----- OR AH,0FH
0448 1355 C ;----- MOV AL,AH
0448 1356 C ;----- ADD SP,0AH
0451 1357 C ;----- MOV AL,00110110B
0454 1358 C ;----- OUT DL,00110110B
0456 1359 C ;----- SUB AL,AL
0458 1360 C ;----- OUT AL,AL
0458 1361 C ;----- OUT TIMERO,AL
0458 1362 C ;----- NOP
0458 1363 C ;----- OUT TIMERO,AL
0458 1364 C ;----- OUT TIMERO,AL
0458 1365 C ;----- MOV BP,0
0460 1366 C ;----- JMP SK1P
0463 1367 C ;----- ASSUME DS:ABSO
0466 1368 C ;----- POD14_18:
0466 1369 C ;----- CALL DOS
0466 1370 C ;----- MOV AX,0500H
0466 1371 C ;----- INT 10H
0466 1372 C ;----- MOV AL,00110110B
0470 1373 C ;----- OUT TIMERO,AL
0471 1374 C ;----- SUB AL,AL
0474 1375 C ;----- OUT TIMERO,AL
0476 1376 C ;----- NOP
0477 1377 C ;----- OUT TIMERO,AL
0477 1378 C ;----- OUT TIMERO,AL
0478 1379 C ;----- ADD SP,0AH
0478 1380 C ;----- MOV BP,0
0480 1381 C ;----- ENDP
0480 1382 C ;----- ;----- TEST STORAGE
0480 1383 C ;----- MEM_TEST:
0480 1384 C ;----- PUSH DS

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0481 E8 0CFE R    1387 C   CALL DDS
0484 F6 06 0487 R 02 1388 C   ASSUME DS:ABSO
0484 F7 12 0487 R 02 1389 C   TEST INFO_2
0484 F8 06 0410 R 30 1390 C   JZ D_COLOR_M
0484 F9 06 0410 R 30 1391 C   OR EC,00F0H,030H
0490 88 000F       1392 C   MOV AX,00F0H
0493 80 0E 0487 R 60 1393 C   OR INFO_050H
0498 BB 000F       1394 C   MOV AX,0FH
0498 EB 0D          1395 C   JMP SHORT D_OUT_M
0499 80 0E 0487 R 60 1396 C   AND EQUIP_LOW,OCFH
049A BB 000F       1397 C   OR EQUIP_LOW,020H
049A EB 0D          1398 C   MOV AX,0EH
0497 BB 000E       1399 C   MOV AX,OEH
0497 80 0E 0410 R 20 1400 C   ; INTERNAL COLOR MODE
0497 BB 000E       1401 C   ; TEST IN COLOR
04AC CD 83 10      1402 C   ; RESERVE 3 WORDS ON STACK
04AC EC 06          1403 C   ; SET BP
04AF BB EC          1404 C   ; PUT BUFFER ADDRESS IN AX
0481 BA A000       1405 C   ; SET UP SEC REGS TO POINT
0484 8E D8          1406 C   ; TO BUFFER AREA
0486 BE CO          1407 C   ; INITIALIZE
0486 C7 46 02 0000 1408 C   ; INITIALIZE
0484 BB 0C 04 0000 1409 C
04CE BB 04 0000     1410 C
04C4 BB C4          1411 C
04C6 BB 0201       1412 C
0459 EB 0015 R     1413 C
047F BB CE          1414 C   ; ADDRESS READ MAP SELECT
04CE BB 0400       1415 C
04D1 EB 0015 R     1416 C
04D4 52             1417 C
04D4 DA             1418 C
04D7 EC 0A           1419 C
04D8 BB CO          1420 C
04DA BB 3200       1421 C
04D9 E8 0015 R     1422 C
0500 E8 0015 R     1423 C
04E3 BB 0C 00       1424 C
04E6 74 03           1425 C
04E8 E9 05CD R     1426 C
04E9 8E 0000       1427 C
04EB E8 05D9 R     1428 C
04EE BB FC 00       1429 C
04F1 74 03           1430 C
04F9 E9 05CD R     1431 C
04F6 5A             1432 C
04F7 BB C4          1433 C
04FC BB 0202       1434 C
04FC BB 0015 R     1435 C
04FF BB CE          1436 C
0501 BB 0401       1437 C
0504 E8 0015 R     1438 C
0505 E8 0015 R     1439 C
0508 BB 0A           1440 C
0508 BB 0A           1441 C
0508 BB CO          1442 C
0508 BB 3200       1443 C
0509 E8 0015 R     1444 C
0513 C7 46 04 0000 1445 C
0518 EB 068F R     1446 C
0518 BB FC 00       1447 C
0518 74 03           1448 C
0520 E9 05CD R     1449 C
0523 E8 05D9 R     1450 C
0526 BB 0C 00       1451 C
0526 74 03           1452 C
0528 E9 05CD R     1453 C
0528 E9 05CD R     1454 C
0528 E9 05CD R     1455 C
0528 E9 05CD R     1456 C
0528 E9 05CD R     1457 C
0529 BB CH          1458 C
0531 BB 0204       1459 C
0534 E8 0015 R     1460 C
0537 52             1461 C
0533 BB 0402       1462 C
0530 E8 0015 R     1463 C
0540 BB DA          1464 C
0542 EC 0A           1465 C
0542 BB CO          1466 C
0545 BB 3200       1467 C
0548 E8 0015 R     1468 C
0548 C7 46 04 0000 1469 C
054B E8 0015 R     1470 C
0550 E8 068F R     1471 C
0550 BB 0C 00       1472 C
0556 74 03           1473 C
0558 E8 73 90       1474 C
0558 E8 05D9 R     1475 C
055E BB 0C 00       1476 C
0561 74 03           1477 C
0563 E8 68 90       1478 C
0564 E8 0000       1479 C
0566 5A             1480 C
0567 BB C4          1481 C
0569 BB 0208       1482 C
0569 E8 0015 R     1483 C
056C E8 0015 R     1484 C
0571 BB 0403       1485 C
0574 E8 0015 R     1486 C
0577 52             1487 C
0577 BB DA          1488 C
057A EC 0A           1489 C
057B BB CO          1490 C
057D BB 3200       1491 C
0580 E8 0015 R     1492 C
0580 E8 0015 R     1493 C
0588 E8 06AF R     1494 C
0588 BB FC 00       1495 C
058E 75 3D           1496 C
058E 80 0000       1497 C
0593 BB 0C 00       1498 C
0596 75 35           1499 C
0598 55             1500 C
0599 BB 0000       1501 C
059C BB 0000       1502 C
059C 5E             1503 C
059D 5A             1504 C
059E E8 0CFE R     1505 C
059E E8 0CFE R     1506 C   ; SET DATA SEGMENT
059E E8 0CFE R     1507 C   ; RESTORE
059E E8 0CFE R     1508 C   ; GET EGA MEMORY SIZE
05A1 36:BB 5C 02    1508 C   ; DIVIDE BY 64 TO GET
05A1 BB 01 06       1509 C   ; NUMBER OF 64KB BLOCKS
05A7 D3 EB          1510 C
05A9 BB 0000       1511 C
05AA BB 01 05       1512 C

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05AC D3 E3          1513   SHL    BX,CL
05AE 80 E3 60       1514   AND    BL,_0100000B      ; ISOLATE BITS 5 AND 6
05B1 80 26 0487 R 9F 1515
05B6 08 1E 0487 R   1516
05BA 80 0E 0487 R 04 1517
05BF 8A 1E 0488 R   1518   OR     INFO,10011111B
05C3 E8 00F3 R     1519   OR     INFO,BL
05C6 82 C4 06       1520   OR     INFO,000000100B ; 04H SET 3XX ACTIVE
05D9 E9 0091 R     1521   MOV    BL,INFO_3
05CD 00 00 00       1522   CALL   MK_ENV
05E0 00 00 00       1523   ADD    SP,6           ; RESTORE STACK
05E1 00 00 00       1524   DS    DS
05E2 00 00 00       1525   JMP    SKIP           ; GO TO END
05E3 BA 0103       1526   EGA_MEM_ERROR:
05E4 EB 06C8 R     1527   MOV    DX,0103H      ; ONE LONG AND THREE SHORT
05E5 00 00 00       1528   CALL   ERR_BEEP
05E6 00 00 00       1529   MOV    BP,1           ; SAVE SCRATCH PAD POINTER
05D4 BD 0001       1530   MOV    BP,1           ; INDICATE ERROR FOR XT
05D7 EB C3         1531   JMP    EGA_MEM_EXIT
05D8 00 00 00       1532
05D9 BB A000       1533
05D9 8E DB         1534
05DE 8E C3         1535
05E0 88 46 04       1536   MEMORY_OK PROC NEAR
05E3 8A E8         1537   MOV    BX,0000H      ; SET PTR. TO BUFFER SEG
05E5 2A C9         1538   MOV    DS,BX          ; SET SEG.REG.
05E7 00 00 00       1539   MOV    ES,DS
05E9 EB 05FB R     1540   MOV    AX,WORD PTR[BP][4] ; SET COUNT FOR 32K WORDS
05EC 80 FC 00       1541   MOV    CH,AL          ; SET AMOUNT OF BUFFER
05EF 75 09         1542   SUB    CL,CL          ; TO BE TESTED
05F1 BB A6 04       1543   CALL   PODSTG      ; MULTIPLY BY TWO
05F4 01 46 02       1544   CMP    AH,0           ; TEST FOR ERROR
05F7 BB 0000       1545   JNZ    MEMORY_OK_ERR ; IF ERROR GO PRINT IT
05FA C3           1546
05FB 00 00 00       1547   MOV    AX,WORD PTR[BP][4]
05F9 00 00 00       1548   ADD    WORD PTR[BP][2],AX ; AMOUNT OF MEMORY GOOD
05FA C3           1549   MOV    AX,0           ; AMOUNT OF MEMORY GOOD
05FB 00 00 00       1550
0551 00 00 00       1551
0552 00 00 00       1552
0553 00 00 00       1553
0554 00 00 00       1554
0555 00 00 00       1555
0556 00 00 00       1556
0557 00 00 00       1557
0558 00 00 00       1558
0559 00 00 00       1559
0560 00 00 00       1560
0561 00 00 00       1561
0562 00 00 00       1562
0563 00 00 00       1563
0564 00 00 00       1564
0565 00 00 00       1565
0566 00 00 00       1566
0567 00 00 00       1567
0568 00 00 00       1568
0569 00 00 00       1569
056A 00 00 00       1570
056B 00 00 00       1571
056C 00 00 00       1572
056D 00 00 00       1573
056E 00 00 00       1574
0601 E8 0CFE R     1575   SUB    DI,DI          ; SET DIR TO INCREMENT
0604 8B 1E 0472 R   1576   PUSH   BP           ; SET DI=0000 REL TO START
0605 F5 1234       1577   CALL   DDS           ; OF SEGMENT
0606 80 00 00       1578   MOV    BX,1234H      ; INITIAL DATA PATTERN FOR
0607 80 00 00       1579   CMP    AX,DX          ; 00-FF TEST
0610 7E 62         1580   JE    PODSTG_5      ; WARM START?
0612 81 FB 4321     1581   CALL   DDS           ; RESTORE DS
0616 74 5C         1582   MOV    BX,4321H      ; GO DO FILL WITH 0000
0618 88 05         1583   JE    PODSTG_5      ; IF WARM START
0620 80 A0         1584   ASSUME DS:ABSO      ; DCP WARM START?
0621 80 00 00       1585   MOV    AL,[DI]        ; DO FILL IF SO
0622 80 DA         1586   CMP    AX,DX          ; WRITE TEST DATA
0624 75 F2         1587   JE    PODSTG_5      ; GET WORD BACK
0626 88 E9         1588   MOV    AX,[DI]        ; COMPARE EXPECTED
0628 8B AA55 H     1589   CMP    AH,AL          ; ERROR EXIT IF MISCOMPARE
062D 80 00 00       1590   INC    AH           ; FORM NEW DATA PATTERN
0630 F3 / AB       1591   MOV    AL,AH          ; LOOP TILL ALL 256 DATA
0632 4F            1592   JE    PODSTG_1      ; PATTERNS DONE
0633 4F            1593   MOV    BP,CX          ; SAVE WORD COUNT
0634 FD            1594   MOV    AX,0A55H      ; LOAD DATA PATTERN
0635 88 F7         1595   CMP    BX,AX          ; LOAD OTHER DATA PATTERN
0637 88 CD         1596   JE    PODSTG_5      ; FILL WORDS FROM LOW TO
0639 AD            1597   REP    STOSH        ; HIGH WITH AAAA
063A 33 C3         1598   DEC    DI           ; POINT TO LAST WORD
063C 75 22         1599   STD    DI           ; SET DIR FLAG TO GO DOWN
063E 88 C2         1600   MOV    SI,DI          ; SET INDEX REGS. EQUAL
0640 80 00 00       1601   MOV    CX,BP          ; RECOVER WORD COUNT
0641 F4 2F         1602   LODSW            ; GO FROM HIGH TO LOW
0643 88 CD         1603   XOR    AX,BX          ; GET WORD FROM MEMORY
0645 FC            1604   MOV    AX,DX          ; EQUAL WHAT S/B THERE?
0647 46            1605   STD    DI           ; GO ERROR EXIT IF NOT
0648 8B FE         1606   LODSW            ; GET 55 DATA PATTERN AND
064A AD            1607   XOR    AX,DX          ; STORE WORDS FROM READ
064B 73 C2         1608   MOV    AX,DX          ; LOOP TILL ALL BYTES DONE
064D 55 11         1609   STD    DI           ; RECOVER WORD COUNT
064F AB            1610   LODSW            ; BACK TO INCREMENT
0650 E2 F8         1611   XOR    CX,BP          ; ADJUST PTRS
0652 FD            1612   DEC    SI           ; LOW TO HIGH WORDS
0653 4E            1613   LODSW            ; GET WORD
0655 8B CD         1614   XOR    AX,DX          ; SHOULD COMPARE TO DX
0657 AD            1615   MOV    AX,DX          ; GO ERROR IF NOT
0659 80 00 00       1616   STD    DI           ; WRITE 0000 BACK TO LOC
065A 75 04         1617   LODSW            ; JUST READ
065C E2 F9         1618   XOR    AH,AH          ; LOOP TILL DONE
065E EB 11         1619   JNZ    PODSTG_ERR2 ; BACK TO DECREMENT
0660 8B C8         1620   STOSW            ; ADJUST POINTER DOWN TO
0662 32 E4         1621   LODSW            ; LAST WORD WRITTEN
0664 0A ED         1622   DEC    SI           ; GET WORD COUNT
0668 00 00 00       1623   LODSW            ; GET WORD
0669 00 00 00       1624   XOR    AX,DX          ; ERROR IF NOT
066A 00 00 00       1625   DEC    SI           ; LOOP TILL DONE
066B 00 00 00       1626   LODSW            ; SAVE BITS IN ERROR
066C 00 00 00       1627   XOR    CX,CH          ; HIGH BYTE ERROR?
066D 00 00 00       1628

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0666 74 02          1639   C     JZ    PODSTG_ERR1
0668 84 01          1640   C     MOV   AH,1             ; SET HIGH BYTE ERROR
066A 8A C9          1641   C     OR    CL,CL
066C 74 03          1642   C     JZ    PODSTG_ERR2
066E 80 C4 02          1643   C     ADD  AH,2             ; LOW BYTE ERROR?
0671           1644   C     ADD  AH,2
0671 5D             1645   C     POP   BP
0671 FC             1646   C     CLD
0673 C3             1647   C     RET
0674           1648   C     POP   AX
0674           1649   C     PODSTG_51:
0674           1650   C     PUSH  AX
0675 50             1651   C     PUSH  DX
0676 B6 03             1652   C     MOV   DH,3             ; SET DIR FLAG BACK TO INC
0678 B2 C4             1653   C     MOV   DL,SEQ_ADDR
067A BB 020F R          1654   C     MOV   AX,020FH
067B 16             1655   C     DELL  OUTDX
0680 5A             1656   C     POP   DX
0681 58             1657   C     POP   AX
0682 F3 AB             1658   C     REP   STOSW
0684 E8 OCFE R          1659   C     CALL  DL
0687 89 1E 0472 R          1660   C     ASSUME DS:ABS0
0688 8E DA             1661   C     MOV   DS,RESET_FLAG,BX
068D EE E2             1662   C     JMP   PODSTG_ERR2
068F           1663   C     DS,DX
068F           1664   C     JMP   DS,DX
068F           1665   C     PODSTG_ENDP:
068F           1666   C     DS,DX
068F           1667   C     DS,DX
068F           1668   C     DS,DX
068F 8C DA             1669   C     HOW_BIC PROC NEAR
0691 2B DB             1670   C     MOV   DX,DX
0693           1671   C     SUB   BX,BX
0694 8E C2             1672   C     FILL_LOOP:
0695 2B FF             1673   C     MOV   ES,DX
0697 BB AA55             1674   C     DB   D1
069A 88 C8             1675   C     MOV   AX,0AA55H
069C 26 89 05             1676   C     MOV   CX,CX
06A1 86 BB 05             1677   C     MOV   ES:[D1],AX
06A4 33 C1             1678   C     MOV   AX,FF
06A6 75 14             1679   C     MOV   AX,ES:[D1]
06A8 B9 0000             1680   C     XOR  AX,CX
06AB 13 AB             1681   C     JNZ   CX,200H
06AD 81 C2 0400             1682   C     REP   STOSW
06B1 83 C3 10             1683   C     ADD  DX,0400H
06B3 80 FE BO             1684   C     ADD  BX,16
06B7 75 00             1685   C     CMP   DH,0B0H
06B9 EB 01 90             1686   C     JNE   FILL_LOOP
06BC 80 FE AO             1687   C     JMP   HOW_B16_END
06C0 74 06             1688   C     HOW_B16_END:
06C1           1689   C     CMD  DH,0A0H
06C1 01 5E 04             1690   C     HB_ERROR_EXIT
06C4 B8 0000             1691   C     RESUME: ADD WORD PTR[BP][4],BX
06C7 C3             1692   C     MOV   AX,0             ; SAVE BUFFER FOUND
06C8           1693   C     HB_ERROR_EXIT:
06C8           1694   C     REI
06C8           1695   C     HOW_BIG ENDP
06C8           1696   C     DS,DX
06C8           1697   C     DS,DX
06C8           1698   C     DS,DX
06C8           1699   C     DS,DX
06C8           1700   C     DS,DX
06C8           1701   C     DS,DX
06C8           1702   C     SUBROUTINES FOR POWER ON DIAGNOSTICS :
06C8           1703   C     THIS PROCEDURE WILL ISSUE ONE LONG TONE (3 SEC) AND ONE OR
06C8           1704   C     MORE SHORT TONES (1 SEC) TO INDICATE A FAILURE ON THE PLANAR
06C8           1705   C     BOARD, A BAD RAM MODULE, OR A PROBLEM WITH THE CRT.
06C8           1706   C     ENTRY REQUIREMENTS: DS,DX,CL,DH,BL,WORD PTR[BP][4]
06C8           1707   C     DL=NUMBER OF LONG TONES TO BEEP
06C8           1708   C     DL=NUMBER OF SHORT TONES TO BEEP.
06C8           1709   C     ERR_BEEP PROC NEAR
06C8           1710   C     PUSHF
06C9 FA             1711   C     CLI             ; SAVE FLAGS
06CA 1E             1712   C     PUSH  DS             ; DISABLE SYSTEM INTS
06CB E8 OCFE R          1713   C     CALL  DDS
06CE 0A F6             1714   C     DS,DX
06D0 74 0B             1715   C     DS,DX,ABS0
06D2 B3 06             1716   C     OR   DH,DH
06D4 E8 0D20 R          1717   C     G1:  JZ   G3
06D7 E2 FE             1718   C     MOV   BL,6
06D7 E2 FE             1719   C     CALL  BEEP
06D7 E2 FE             1720   C     G2:  LOOP G2
06D7 E2 FE             1721   C     DEC   DH
06D8 75 F5             1722   C     JNZ   G1
06D9           1723   C     G3:  MOV   BL,1
06D9           1724   C     CALL  BEEP
06D9 B3 01             1725   C     G4:  LOOP G4
06DF E8 0D20 R          1726   C     DEC   DL
06E0 74 02             1727   C     JNZ   G3
06E2 E2 FE             1728   C     G5:  LOOP G5
06E4 FE CA             1729   C     DEC   DL
06E6 75 F5             1730   C     JNZ   G3
06E8 E2 FE             1731   C     G6:  LOOP G6
06EA           1732   C     DEC   DL
06EA           1733   C     JNZ   G3
06EA E2 FE             1734   C     POP   DS             ; RESTORE CONTENTS OF DS
06EC 9D             1735   C     RET
06EE C3             1736   C     ERR_BEEP ENDP
06EF           1737   C     DS,DX
06EF           1738   C     DS,DX
06EF           1739   C     DS,DX
06EF           1740   C     DS,DX
06EF           1741   C     DS,DX
06EF           1742   C     DS,DX
06F1 0EB3 R             1743   C     T2   LABEL WORD
06F1 10EF R             1744   C     DW   OFFSET AH0   ; MODE SET
06F3 1157 R             1745   C     DW   OFFSET AH1   ; SET CURSOR TYPE
06F5 1186 R             1746   C     DW   OFFSET AH2   ; SET CURSOR POSITION
06F7 119D R             1747   C     DW   OFFSET AH3   ; READ CURSOR POSITION
06F8 1209 R             1748   C     DW   OFFSET AH4   ; READ LIGHT PEN POSITION
06FB 50E0 R             1749   C     DW   OFFSET AH5   ; ACTIVE DISPLAY PAGE
06FD 1580 R             1750   C     DW   OFFSET AH6   ; SCROLL DOWN
06FF 170E R             1751   C     DW   OFFSET AH7   ; SCROLL UP
0701 1820 R             1752   C     DW   OFFSET AH8   ; READ CHAR/ATTRIBUTE
0703 18D0 R             1753   C     DW   OFFSET AH9   ; WRITE CHAR/ATTRIBUTE
0705 A745 R             1754   C     DW   OFFSET AH10  ; WRITE CHARACTER ONLY
0707 18C7 R             1755   C     DW   OFFSET AHB   ; SET COLOR PALETTE
0709 C971 R             1756   C     DW   OFFSET AHC   ; WRITE DOT
070B 1C91 R             1757   C     DW   OFFSET AHD   ; READ DOT
070D 10B5 R             1758   C     DW   OFFSET AH1   ; WRITE BRIGHTNESS
070F 1DC5 R             1759   C     DW   OFFSET AHF   ; CURRENT VIDEO STATE
0711 19F8 R             1760   C     DW   OFFSET AH10  ; SET PALETTE REGISTERS
0713 10BF R             1761   C     DW   OFFSET AH11  ; CHAR GENERATOR ROUTINE
0715 2118 R             1762   C     DW   OFFSET AH12  ; ALTERNATE SELECT
0715 = 0020             1763   C     DW   OFFSET AH13  ; WRITE STRING
0715           1764   C     T2L  EQU S-12

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1765
1766 INCLUDE VPARMS.INC
1767 SUBTTL VPARMS.INC
1768 PAGE
1769
1770 VIDEO_PARMS LABEL BYTE
1771 ; STRUCTURE OF THIS TABLE
1772
1773 COLUMNS, ROWS, PELS PER CHARACTER
1774 PAGE LENGTH
1775 SUBSEQUENT PARAMETERS
1776 MISCELLANEOUS REGISTER
1777 CRTC PARAMETERS
1778 ATTRIBUTE PARAMETERS
1779 GRAPHICS PARAMETERS
1780
1781 C BASE_1 EQU $ - VIDEO_PARMS
1782 C BASE_1_L LABEL BYTE
1783
1784 ;---- DEFAULT MODES
1785
1786 C---DB 40D,24D,0BD
1787 C---DW 00800H
1788
1789 C TFS_LEN EQU $ - BASE_1_L
1790
1791 C SEQ_PARAMS LABEL BYTE
1792 C DB 00BH,003H,000H,003H
1793 M1 EQU $ - SEQ_PARAMS
1794
1795 C DB 023H
1796
1797 C CRT_PARAMS LABEL BYTE
1798 C DB 037H,027H,02DH,037H,031H,015H
1799 C DB 004H,011H,000H,007H,006H,007H
1800 C DB 000H,000H,000H,000H,0E1H,024H
1801 C DB 00FH,014H,008H,0E0H,0F0H,0A3H
1802 C DB 01FH,000H
1803
1804 C M4 EQU S-CRT_PARAMS
1805
1806 C LN_4 EQU $ - BASE_1_L
1807
1808 C ATTR_PARAMS LABEL BYTE
1809 C DB 000H,001H,002H,003H,004H,005H
1810 C DB 006H,007H,010H,011H,012H,013H
1811 C DB 000H,001H,002H,003H,004H,005H
1812 C DB 00FH,000H
1813
1814 C M5 EQU S-ATTR_PARAMS
1815
1816 C LN_2 EQU $ - BASE_1_L
1817 C GRAPH_PARAMS LABEL BYTE
1818 C DB 000H,000H,000H,000H,000H,010H
1819 C DB 00EH,000H,0FFH
1820
1821 C M_TBL_LEN EQU $ - BASE_1_L
1822
1823 C---DB 40D,24D,0BD
1824 C---DW 00800H
1825
1826 C DB 008H,003H,000H,003H
1827 C DB 023H
1828
1829 C DB 000H,000H,000H,000H,000H,010H
1830
1831 C DB 037H,027H,02DH,037H,031H,015H
1832 C DB 004H,011H,000H,007H,006H,007H
1833 C DB 000H,000H,000H,000H,0E1H,024H
1834 C DB 00FH,014H,008H,0E0H,0F0H,0A3H
1835 C DB 0FFH,000H
1836
1837 C DB 000H,001H,002H,003H,004H,005H
1838 C DB 006H,007H,010H,011H,012H,013H
1839 C DB 014H,015H,016H,017H,000H,000H
1840 C DB 00FH,000H
1841
1842 C DB 000H,000H,000H,000H,000H,010H
1843 C DB 00EH,000H,0FFH
1844
1845 C---DB 80D,24D,0BD
1846 C---DW 01000H
1847
1848 C DB 001H,003H,000H,003H
1849
1850 C DB 023H
1851
1852 C DB 070H,04FH,05CH,02FH,05FH,007H
1853 C DB 004H,011H,000H,007H,006H,007H
1854 C DB 000H,000H,000H,000H,0E1H,024H
1855 C DB 00FH,028H,008H,0E0H,0F0H,0A3H
1856 C DB 0FFH,000H
1857
1858 C DB 000H,001H,002H,003H,004H,005H
1859 C DB 004H,007H,010H,011H,012H,013H
1860 C DB 014H,015H,016H,017H,000H,000H
1861 C DB 00FH,000H
1862
1863 C DB 000H,000H,000H,000H,000H,010H
1864 C DB 00EH,000H,0FFH
1865
1866 C---DB 80D,24D,0BD
1867 C---DW 01000H
1868
1869 C DB 001H,003H,000H,003H
1870
1871 C DB 023H
1872
1873 C DB 070H,04FH,05CH,02FH,05FH,007H
1874 C DB 004H,011H,000H,007H,006H,007H
1875 C DB 000H,000H,000H,000H,0E1H,024H
1876 C DB 00FH,028H,008H,0E0H,0F0H,0A3H
1877 C DB 0FFH,000H
1878
1879 C DB 000H,001H,002H,003H,004H,005H
1880 C DB 004H,007H,010H,011H,012H,013H
1881 C DB 014H,015H,016H,017H,000H,000H
1882 C DB 00FH,000H
1883
1884 C DB 000H,000H,000H,000H,000H,010H
1885 C DB 00EH,000H,0FFH
1886
1887 C DB 000H,001H,002H,003H,004H,005H
1888 C DB 004H,007H,010H,011H,012H,013H
1889 C DB 014H,015H,016H,017H,000H,000H
1890 C---DB 40D,24D,0BD

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081A	4000	1891	C	DW	04000H
081C	08 03 00 02	1892	C	DB	00BH, 003H, 000H, 002H
0820	23	1893	C	DB	023H
0821	37 27 2D 37 30 14	1894	C	DB	037H, 027H, 02DH, 037H, 030H, 014H
0827	04 11 00 01 00 01	1895	C	DB	004H, 011H, 000H, 001H, 000H, 000H
082D	00 00 00 00 E1 24	1896	C	DB	000H, 000H, 000H, 000H, 0E1H, 024H
0833	C7 14 08 EO FO A2	1897	C	DB	0C7H, 014H, 000H, 0E0H, 0FOH, 0A2H
0839	FF	1898	C	DB	0FFH
083A	00 13 15 17 02 04	1899	C	DB	000H, 013H, 015H, 017H, 002H, 004H
0840	06 07 10 11 12 13	1900	C	DB	006H, 007H, 010H, 011H, 012H, 013H
0841	14 15 16 17 01 00	1901	C	DB	014H, 015H, 016H, 017H, 001H, 000H
084C	03 00	1902	C	DB	003H, 000H
084E	00 00 00 00 00 30	1903	C	DB	000H, 000H, 000H, 000H, 000H, 030H
0854	0F 00 FF	1904	C	DB	00FH, 000H, 0FFH
0857	28 18 08	1905	C	DB	000H, 000H, 000H, 000H, 000H, 030H
085A	4000	1906	C	DB	000H, 000H, 000H, 000H, 000H, 030H
085C	08 03 00 02	1907	C	DB	037H, 027H, 02DH, 037H, 030H, 014H
0860	23	1908	C	DB	004H, 011H, 000H, 001H, 000H, 000H
0861	37 27 2D 37 30 14	1909	C	DB	037H, 027H, 02DH, 037H, 030H, 014H
0867	04 11 00 01 00 00	1910	C	DB	004H, 011H, 000H, 001H, 000H, 000H
086D	00 00 00 00 E1 24	1911	C	DB	000H, 000H, 000H, 000H, 0E1H, 024H
0873	C7 14 08 EO FO A2	1912	C	DB	0C7H, 014H, 000H, 0E0H, 0FOH, 0A2H
0879	FF	1913	C	DB	0FFH
087A	00 13 15 17 02 04	1914	C	DB	000H, 013H, 015H, 017H, 002H, 004H
0880	06 07 10 11 12 13	1915	C	DB	006H, 007H, 010H, 011H, 012H, 013H
0886	14 15 16 17 01 00	1916	C	DB	014H, 015H, 016H, 017H, 001H, 000H
088C	03 00	1917	C	DB	003H, 000H
088E	00 00 00 00 00 30	1918	C	DB	000H, 000H, 000H, 000H, 000H, 030H
0894	0F 00 FF	1919	C	DB	00FH, 000H, 0FFH
0897	50 18 08	1920	C	DB	000H, 000H, 000H, 000H, 000H, 030H
089A	4000	1921	C	DB	000H, 000H, 000H, 000H, 000H, 030H
089C	01 01 00 06	1922	C	DB	001H, 001H, 000H, 000H, 000H, 030H
08A0	23	1923	C	DB	023H
08A1	70 4F 59 2D 5E 06	1924	C	DB	070H, 04FH, 059H, 02DH, 05EH, 006H
08A7	04 11 00 01 00 00	1925	C	DB	004H, 011H, 000H, 001H, 000H, 000H
08AD	00 00 00 00 E0 23	1926	C	DB	000H, 000H, 000H, 000H, 0E0H, 023H
08B3	C7 28 00 QF EF C2	1927	C	DB	0C7H, 028H, 000H, 0DFH, 0EFH, 0C2H
08B9	FF	1928	C	DB	0FFH
08BA	00 17 17 17 17 17	1929	C	DB	000H, 017H, 017H, 017H, 017H, 017H
08C0	17 17 17 17 17 17	1930	C	DB	017H, 017H, 017H, 017H, 017H, 017H
08C6	17 17 17 17 01 00	1931	C	DB	017H, 017H, 017H, 017H, 001H, 000H
08CC	01 00	1932	C	DB	001H, 000H
08CE	00 00 00 00 00 00	1933	C	DB	000H, 000H, 000H, 000H, 000H, 000H
08D4	0D 00 FF	1934	C	DB	00DH, 000H, 0FFH
08D7	50 18 0E	1935	C	DB	000H, 000H, 000H, 000H, 000H, 000H
08DA	1000	1936	C	DB	01000H
08DC	00 03 00 03	1937	C	DB	000H, 003H, 000H, 003H
08E0	A6	1938	C	DB	0A6H
08E1	60 4F 56 3A 51 60	1939	C	DB	060H, 04FH, 056H, 03AH, 051H, 060H
08E7	70 1F 00 00 0B 0C	1940	C	DB	070H, 01FH, 000H, 000H, 00BH, 00CH
08ED	00 00 00 00 5E 2E	1941	C	DB	000H, 000H, 000H, 000H, 05EH, 02EH
08F3	5D 28 00 5E 6E A3	1942	C	DB	05DH, 028H, 000H, 05EH, 06EH, 0A3H
08F9	FF	1943	C	DB	0FFH
08FA	00 08 08 08 08 08	1944	C	DB	000H, 008H, 008H, 008H, 008H, 008H
0900	08 08 10 18 18 18	1945	C	DB	008H, 008H, 010H, 018H, 018H, 018H
0906	18 18 18 18 0E 00	1946	C	DB	018H, 018H, 018H, 018H, 00EH, 000H
090C	07 08	1947	C	DB	00FH, 008H
090E	00 00 00 00 00 10	1948	C	DB	000H, 000H, 000H, 000H, 000H, 010H
0914	04 00 FF	1949	C	DB	00AH, 000H, 0FFH
0917	28 18 08	1950	C	DB	000H, 000H, 000H, 000H, 000H, 000H
091A	4000	1951	C	DB	04000H
091C	00 00 00 03	1952	C	DB	000H, 000H, 000H, 003H
0920	23	1953	C	DB	023H
0921	37 27 2D 37 31 15	1954	C	DB	037H, 027H, 02DH, 037H, 031H, 015H
0927	04 11 00 07 06 07	1955	C	DB	004H, 011H, 000H, 007H, 006H, 007H
092D	00 00 00 00 E1 24	1956	C	DB	000H, 000H, 000H, 000H, 0E1H, 024H
0933	C7 14 08 EO FO A3	1957	C	DB	0C7H, 014H, 000H, 0E0H, 0FOH, 0A3H
0939	FF	1958	C	DB	0FFH
093A	00 01 02 03 04 05	1959	C	DB	000H, 001H, 002H, 003H, 004H, 005H
0940	06 07 10 11 12 13	1960	C	DB	006H, 007H, 010H, 011H, 012H, 013H
0946	14 15 16 17 08 00	1961	C	DB	014H, 015H, 016H, 017H, 008H, 000H
094C	0F 00	1962	C	DB	00FH, 000H
094E	00 00 00 00 00 10	1963	C	DB	000H, 000H, 000H, 000H, 000H, 010H
0954	0E 00 FF	1964	C	DB	00EH, 000H, 0FFH
0957	28 18 08	1965	C	DB	000H, 000H, 000H, 000H, 000H, 000H
095A	4000	1966	C	DB	04000H
095C	00 00 00 03	1967	C	DB	000H, 000H, 000H, 003H
0960	23	1968	C	DB	023H
0961	37 27 2D 37 31 15	1969	C	DB	037H, 027H, 02DH, 037H, 031H, 015H
0967	04 11 00 07 06 07	1970	C	DB	004H, 011H, 000H, 007H, 006H, 007H
096D	00 00 00 00 E1 24	1971	C	DB	000H, 000H, 000H, 000H, 0E1H, 024H
0973	C7 14 08 EO FO A3	1972	C	DB	0C7H, 014H, 000H, 0E0H, 0FOH, 0A3H
0979	FF	1973	C	DB	0FFH
097A	00 01 02 03 04 05	1974	C	DB	000H, 001H, 002H, 003H, 004H, 005H
0980	06 07 10 11 12 13	1975	C	DB	006H, 007H, 010H, 011H, 012H, 013H
0986	14 15 16 17 08 00	1976	C	DB	014H, 015H, 016H, 017H, 008H, 000H
098C	0F 00	1977	C	DB	00FH, 000H
098D	50 18 0E	1978	C	DB	000H, 000H, 000H, 000H, 000H, 000H
0991	FF	1979	C	DB	0FFH
0992	00 00 00 03	1980	C	DB	000H, 000H, 000H, 003H
0994	23	1981	C	DB	023H
0995	37 27 2D 37 31 15	1982	C	DB	037H, 027H, 02DH, 037H, 031H, 015H
0996	04 11 00 07 06 07	1983	C	DB	004H, 011H, 000H, 007H, 006H, 007H
0997	00 00 00 00 E1 24	1984	C	DB	000H, 000H, 000H, 000H, 0E1H, 024H
0998	FF	1985	C	DB	0FFH
0999	00 00 00 00 00 10	1986	C	DB	000H, 000H, 000H, 000H, 000H, 010H
099A	0E 00 FF	1987	C	DB	00EH, 000H, 0FFH
099B	28 18 08	1988	C	DB	000H, 000H, 000H, 000H, 000H, 000H
099C	4000	1989	C	DB	04000H
099D	00 00 00 03	1990	C	DB	000H, 000H, 000H, 003H
099E	23	1991	C	DB	023H
099F	37 27 2D 37 31 15	1992	C	DB	037H, 027H, 02DH, 037H, 031H, 015H
099G	04 11 00 07 06 07	1993	C	DB	004H, 011H, 000H, 007H, 006H, 007H
099H	00 00 00 00 E1 24	1994	C	DB	000H, 000H, 000H, 000H, 0E1H, 024H
099I	FF	1995	C	DB	0FFH
099J	00 00 00 00 00 10	1996	C	DB	000H, 000H, 000H, 000H, 000H, 010H
099K	0E 00 FF	1997	C	DB	00EH, 000H, 0FFH
099L	28 18 08	1998	C	DB	000H, 000H, 000H, 000H, 000H, 000H
099M	4000	1999	C	DB	04000H
099N	00 00 00 03	2000	C	DB	000H, 000H, 000H, 003H
099O	23	2001	C	DB	023H
099P	37 27 2D 37 31 15	2002	C	DB	037H, 027H, 02DH, 037H, 031H, 015H
099Q	04 11 00 07 06 07	2003	C	DB	004H, 011H, 000H, 007H, 006H, 007H
099R	00 00 00 00 E1 24	2004	C	DB	000H, 000H, 000H, 000H, 0E1H, 024H
099S	FF	2005	C	DB	0FFH
099T	00 00 00 00 00 10	2006	C	DB	000H, 000H, 000H, 000H, 000H, 010H
099U	0E 00 FF	2007	C	DB	00EH, 000H, 0FFH
099V	28 18 08	2008	C	DB	000H, 000H, 000H, 000H, 000H, 000H
099W	4000	2009	C	DB	04000H
099X	00 00 00 03	2010	C	DB	000H, 000H, 000H, 003H
099Y	23	2011	C	DB	023H
099Z	37 27 2D 37 31 15	2012	C	DB	037H, 027H, 02DH, 037H, 031H, 015H
099A	04 11 00 07 06 07	2013	C	DB	004H, 011H, 000H, 007H, 006H, 007H
099B	00 00 00 00 E1 24	2014	C	DB	000H, 000H, 000H, 000H, 0E1H, 024H
099C	FF	2015	C	DB	0FFH
099D	00 00 00 00 00 10	2016	C	DB	000H, 000H, 000H, 000H, 000H, 010H

098E	00 00 00 00 00 10	2017	C		
0994	OE 00 FF	2018	C	DB	000H,000H,000H,000H,000H,010H
		2019	C	DB	00EH,000H,0FFH
		2020	C		
		2021	C	;;--A--	
0997	28 18 08	2022	C	DB	40D,24D,08D
099A	4000	2023	C	DW	04000H
		2024	C	DB	000H,000H,000H,003H
09A0	23	2025	C	DB	023H
		2026	C		
		2028	C		
09A1	37 27 20 37 31 15	2029	C	DB	037H,027H,02DH,037H,031H,015H
09A7	08 11 00 07 00 07	2030	C	DB	004H,011H,000H,007H,006H,007H
09A9	00 00 00 00 E1 24	2031	C	DB	000H,000H,000H,000H,01H,024H
09B3	C7 14 00 EO F0 A3	2032	C	DB	00H,014H,008H,0E0H,0F0H,0A3H
09B9	FF	2033	C	DB	0FFH
		2034	C		
09BA	00 01 02 01 04 05	2035	C	DB	000H,001H,002H,003H,004H,005H
09C0	05 07 10 11 12 13	2036	C	DB	00H,005H,010H,011H,012H,013H
09C6	14 15 16 17 08 00	2037	C	DB	014H,015H,016H,017H,008H,000H
09CC	00 FF	2038	C	DB	00FH,000H
		2039	C		
09CE	00 00 00 00 00 10	2040	C	DB	000H,000H,000H,000H,000H,010H
09D4	00 00 00 00 00 10	2041	C	DB	00EH,000H,0FFH
		2042	C	;;--B--	
09D7	50 18 08	2043	C	DB	80D,24D,08D
09DA	1000	2044	C	DW	01000H
		2046	C	DB	001H,004H,000H,007H
09DC	01 04 00 07	2047	C		
09E0	23	2048	C		
		2049	C	DB	023H
09E1	70 4F 5C 2F 5F 07	2051	C	DB	070H,04FH,05CH,02FH,05FH,007H
09E7	08 11 00 07 00 07	2052	C	DB	004H,011H,000H,007H,006H,007H
09F3	00 00 00 00 E1 24	2053	C	DB	000H,000H,000H,000H,01H,024H
09F9	FF	2054	C	DB	0C7H,028H,008H,0E0H,0F0H,0A3H
		2055	C	DB	0FFH
09FA	00 00 00 00 00 00	2056	C	DB	000H,000H,000H,000H,000H,000H
09AD	00 00 00 00 00 00	2057	C	DB	000H,000H,000H,000H,000H,000H
09A6	00 00 00 00 00 00	2058	C	DB	000H,000H,000H,000H,000H,000H
09AC	00 FF	2059	C	DB	000H,000H,000H,000H,000H,000H
		2060	C	DB	00FH,000H
09AE	00 00 00 00 00 00	2061	C		
09A4	00 00 FF	2062	C	DB	000H,000H,000H,000H,000H,000H
		2063	C	DB	004H,000H,0FFH
A117	50 18 0E	2064	C	;;--C--	
A1A	1000	2065	C	DB	80D,24D,14D
		2066	C	DW	01000H
A1C	00 04 00 07	2067	C	DB	000H,004H,000H,007H
A20	A6	2068	C		
		2069	C	DB	0A6H
		2070	C		
A21	60 4F 56 3A 51 60	2071	C	DB	060H,04FH,056H,03AH,051H,060H
A22	70 1F 00 00 08 0C	2072	C	DB	070H,01FH,000H,000H,008H,00CH
A2D	00 00 00 00 5E 2E	2073	C	DB	000H,000H,000H,000H,05EH,02EH
A33	50 28 5E 6E A3	2074	C	DB	050H,028H,00DH,05EH,06EH,0A3H
A39	FF	2075	C	DB	0FFH
		2077	C		
A3A	00 00 00 00 00 00	2078	C	DB	000H,000H,000H,000H,000H,000H
A40	00 00 00 00 00 00	2079	C	DB	000H,000H,000H,000H,000H,000H
A46	00 00 00 00 00 00	2080	C	DB	000H,000H,000H,000H,000H,000H
AUC	0F 08	2081	C	DB	00FH,008H
		2082	C		
A4HE	00 00 00 00 00 00	2083	C	DB	000H,000H,000H,000H,000H,000H
A54	00 00 FF	2084	C	DB	004H,000H,0FFH
		2085	C	;;--D--	
A57	28 18 08	2086	C	DB	80D,24D,08D
A5A	2000	2087	C	DW	02000H
		2088	C	DB	00BH,00FH,000H,006H
A60	23	2089	C		
		2091	C	DB	023H
		2092	C		
A61	37 27 20 37 30 14	2093	C	DB	037H,027H,02DH,037H,030H,014H
A63	00 00 00 00 01 24	2094	C	DB	000H,011H,000H,001H,000H,000H
A73	C7 14 EO F0 E3	2095	C	DB	000H,000H,000H,000H,01H,024H
A79	FF	2096	C	DB	0C7H,014H,000H,0E0H,0F0H,0E3H
		2097	C	DB	0FFH
		2098	C		
A7A	00 01 02 03 04 05	2099	C	DB	000H,001H,002H,003H,004H,005H
A80	07 10 11 12 13	2100	C	DB	006H,007H,010H,011H,012H,013H
A86	14 15 16 17 01 00	2101	C	DB	014H,015H,016H,017H,001H,000H
A8C	00 FF	2102	C	DB	00FH,000H
		2103	C		
A8E	00 00 00 00 00 00	2104	C	DB	000H,000H,000H,000H,000H,000H
A94	05 0F FF	2105	C	DB	005H,00FH,0FFH
		2106	C	;;--E--	
A97	50 18 08	2107	C	DB	80D,24D,08D
A9A	4000	2108	C	DW	04000H
		2109	C	DB	001H,00FH,000H,006H
A9C	01 0F 00 06	2110	C		
AAC	23	2111	C	DB	023H
		2112	C		
AAC1	70 4F 59 2D 5E 06	2113	C	DB	070H,04FH,059H,02DH,05EH,006H
AAC7	08 11 00 00 00 00	2114	C	DB	004H,011H,000H,000H,000H,000H
AAC9	00 00 00 00 E2 23	2115	C	DB	000H,000H,000H,000H,0E0H,023H
AAB3	FF C7 28 DF EF F0	2116	C	DB	0C7H,028H,00DH,0EFH,0FH,0E3H
AAB9	FF	2117	C	DB	0FFH
		2118	C		
AABA	00 01 02 03 04 05	2119	C	DB	000H,001H,002H,003H,004H,005H
AACD	06 07 10 11 12 13	2120	C	DB	004H,007H,010H,011H,012H,013H
AACF	14 15 16 17 01 00	2121	C	DB	014H,015H,016H,017H,001H,000H
AACG	0F 00	2122	C	DB	00FH,000H
		2123	C	DB	00FH,000H
AACE	00 00 00 00 00 00	2124	C		
AAD4	05 0F FF	2125	C	DB	000H,000H,000H,000H,000H,000H
		2126	C	DB	005H,00FH,0FFH
		2127	C	;;--F--	
AAD7	50 18 0E	2128	C	DB	80D,24D,14D
AADA	8000	2129	C	DW	08000H
		2130	C	DB	005H,00FH,000H,000H
AADC	05 0F 00 00	2131	C		
AAD0	A2	2132	C	DB	0A2H
		2133	C		
AEE1	60 4F 56 1A 50 EO	2134	C	DB	060H,04FH,056H,01AH,050H,0EOH
AEE7	70 1F 00 00 00 00	2135	C	DB	070H,01FH,000H,000H,000H,000H
AEE9	00 00 00 00 5E 2E	2136	C	DB	000H,000H,000H,000H,05EH,02EH
AEE3	50 14 00 00 00 00	2137	C	DB	050H,014H,000H,000H,05EH,02EH
AEE9	FF	2138	C	DB	0FFH
		2139	C	DB	00FH,000H
AEEA	00 08 00 00 18 18	2140	C		
AB00	00 00 00 00 00 00	2141	C	DB	000H,008H,000H,000H,018H,018H
		2142	C	DB	000H,000H,000H,000H,008H,000H

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OB06 00 18 00 00 0B 00 2183 C DB 000H,018H,000H,000H,00BH,000H
OB0C 05 00 2184 C DB 005H,000H
OB0E 00 00 00 00 00 10 2185 C DB 000H,000H,000H,000H,000H,010H
OB14 07 0F FF 2186 C ;--10-- DB 007H,00FH,0FFH
OB17 50 18 0E 2188 C DB 80D,24D,14D
OB1A 8000 2189 C DW 08000H
OB1C 05 0F 00 2190 C
OB20 A7 2191 C
OB21 5B 4F 53 17 50 BA 2192 C
OB27 6C 1F 00 00 00 00 2193 C
OB30 5D 28 0F 5F 0A 8B 2194 C
OB33 5D 14 0F 5F 0A 8B 2195 C
OB39 FF 2196 C
OB3A 00 01 00 00 04 07 2197 C
OB40 00 02 00 01 00 00 2198 C
OB46 04 07 00 00 01 00 2199 C
OB4C 05 00 2200 C
OB4E 00 00 00 00 00 10 2201 C
OB54 07 0F FF 2202 C
= 0440 2203 C
2204 C BASE_2 EQU S - VIDEO_PARMS
2205 C ;----- > 16K MODE VALUES
2206 C ;---F--- DB 80D,24D,14D
2207 C DW 08000H
OB57 50 18 0E 2208 C
OB5A 8000 2209 C
OB5C 01 0F 00 06 2210 C
OB60 A2 2211 C
OB61 60 4F 56 3A 50 60 2212 C
OB67 70 1F 00 00 00 00 2213 C
OB73 00 00 00 00 5E 2E 2214 C
OB79 FF 2215 C
OB7A 00 08 00 00 18 18 2216 C
OB80 00 00 00 00 08 00 2217 C
OB86 00 18 00 00 0B 00 2218 C
OB8C 05 00 2219 C
OB8E 00 00 00 00 00 00 2220 C
OB94 05 0F FF 2221 C
OB97 50 18 0E 2222 C
OB9A 8000 2223 C
OB9C 01 0F 00 06 2224 C
OBAA A7 2225 C
OBAB 5B 4F 53 37 52 00 2226 C
OBAT 6C 1F 00 00 00 00 2227 C
OBAD 00 00 00 00 5E 2B 2228 C
OB83 5D 28 0F 5F 0A E3 2229 C
OB89 FF 2230 C
OBBA 00 01 02 03 04 05 2231 C
OBBD 14 07 38 39 3A 3B 2232 C
OBCE 3C 3D 3E 3F 01 00 2233 C
OBCC 0F 00 2234 C
OBCE 00 00 00 00 00 00 2235 C
OB04 05 0F FF 2236 C
= 04C0 2237 C
2238 C BASE_3 EQU S - VIDEO_PARMS
2239 C ;----- HI RES ALTERNATE VALUES
2240 C ;---0--- DB 40D,24D,14D
2241 C DW 08000H
OB07 28 18 0E 2242 C
OBDA 8000 2243 C
OBDC 0B 03 00 03 2244 C
OBEO A7 2245 C
OBF1 2D 27 2B 2D 2B 60 2246 C
OBF7 6C 1F 00 00 06 07 2247 C
OBF9 5D 28 0F 5E 0A A3 2248 C
OBF9 FF 2249 C
OBFA 00 01 02 03 04 05 2250 C
OC00 14 07 38 39 3A 3B 2251 C
OC06 3C 3D 3E 3F 08 00 2252 C
OC0C 0F 00 2253 C
OC0E 00 00 00 00 00 10 2254 C
OC14 0E 00 FF 2255 C
OC17 28 18 0E 2256 C ;--1-- DB 40D,24D,14D
OC1A 8000 2257 C DW 08000H
OC1C 0B 03 00 03 2258 C
OC20 A7 2259 C
OC21 2D 27 2B 2D 2B 60 2260 C
OC27 6C 1F 00 00 06 07 2261 C
OC29 5D 28 0F 5E 0A A3 2262 C
OC33 5D 14 0F 5E 0A A3 2263 C
OC39 FF 2264 C
OC3A 00 01 02 03 04 05 2265 C
OC40 14 07 38 39 3A 3B 2266 C
OC46 3C 3D 3E 3F 08 00 2267 C ;--2-- DB 80D,24D,14D
OC4C 0F 00 2268 C

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0C5A 1000          2269   C      DW    01000H
0C5C 01 03 00 03    2270   C      DB    001H,003H,000H,003H
0C60 A7            2271   C      DB    0A7H
0C61 5B AF 53 37 51 5B  2272   C      DB    05BH,0FH,053H,037H,051H,05BH
0C67 6C 1F 00 00 06 07  2273   C      DB    06CH,01FH,000H,00DH,00CH,007H
0C6D 00 00 00 00 5E 2B  2274   C      DB    000H,000H,000H,000H,05EH,02BH
0C73 50 28 5E 0A A3   2275   C      DB    05DH,028H,00FH,05EH,00AH,0A3H
0C79 FF            2276   C      DB    0FFH
0C7A 00 01 02 03 04 05  2281   C      DB    000H,001H,002H,003H,004H,005H
0C80 14 07 38 39 3A 3B  2282   C      DB    014H,007H,038H,039H,03AH,03BH
0C86 3C 3D 3E 3F 08 00  2283   C      DB    03CH,03DH,03EH,03FH,008H,000H
0C8C 0F 00          2284   C      DB    00FH,000H
0C8E 00 00 00 00 10    2285   C      DB    000H,000H,000H,000H,000H,010H
0C94 0E 00 FF        2286   C      DB    00EH,000H,0FFH
0C9A 1000          2287   C      DB    01000H
0C97 50 18 0E        2288   C      DB    80D,24D,14D
0C9A 1000          2289   C      DB    01000H
0C9C 01 03 00 03        2290   C      DB    001H,003H,000H,003H
0CA0 A7            2291   C      DB    0A7H
0CA1 5B AF 53 37 51 5B  2292   C      DB    05BH,0FH,053H,037H,051H,05BH
0CA7 3C 1F 00 00 06 07  2293   C      DB    06CH,01FH,000H,00DH,00CH,007H
0CAD 00 00 00 5E 2B    2294   C      DB    000H,000H,000H,000H,05EH,02BH
0CB3 50 28 5E 0A A3   2295   C      DB    05DH,028H,00FH,05EH,00AH,0A3H
0CB9 FF            2296   C      DB    0FFH
0CBA 00 01 02 03 04 05  2301   C      DB    000H,001H,002H,003H,004H,005H
0CCC 14 07 38 39 3A 3B  2302   C      DB    014H,007H,038H,039H,03AH,03BH
0CC6 3C 3D 3E 3F 08 00  2303   C      DB    03CH,03DH,03EH,03FH,008H,000H
0CCC 0F 00          2304   C      DB    00FH,000H
0CCF 00 00 00 00 10    2305   C      DB    000H,000H,000H,000H,000H,010H
0CD4 0E 00 FF        2306   C      DB    00EH,000H,0FFH
0CD7 FB            2310   C      SUBTTL
0CD7 55            2311   C      ;----- VECTOR INTO <AH> SPECIFIED FUNCTION
0CD8 FC            2312   C      COMBO_VIDEO PROC NEAR
0CD9 55            2313   C      STI
0CD9 00            2314   C      CLD
0CD9 55            2315   C      PUSH BP
0CD9 00            2316   C      PUSH DS
0CD9 55            2317   C      PUSH ES
0CD9 00            2318   C      PUSH DX
0CD9 55            2319   C      PUSH CX
0CD9 00            2320   C      PUSH BX
0CD9 55            2321   C      PUSH SI
0CD9 00            2322   C      PUSH DI
0CD9 55            2323   C      PUSH AX
0CD9 00            2324   C      MOV S1,AX
0CD9 55            2325   C      CMP AX,T2L
0CD9 00            2326   C      JB MS
0CD9 55            2327   C      POP AX
0CE1 50            2328   C      INT 42H
0CE2 8C C4          2329   C      JMP V_RET
0CE4 32 E4          2330   C      ;----- INTERRUPTS ON
0CE6 D1 E0          2331   C      ; SET DIRECTION FORWARD
0CE8 88 F0          2332   C      ; SAVE THE REGISTER SET
0CEA 3D 0028         2333   C      ; * 2 FOR TABLE LOOKUP
0CEA 3D 06          2334   C      ; PUT INTO SI FOR BRANCH
0CEC 58            2335   C      ; TEST FOR WITHIN RANGE
0CF0 CD 42          2336   C      ; BRANCH IF ABOVE BRANCH
0CF2 E9 219E R      2337   C      ; RECOVER REGISTER
0CF5 33             2338   C      ; PASS UNRECOGNIZED CALL
0CF5 33             2339   C      ; RETURN TO CALLER
M2: ASSUME DS:ABSO
CALL DDS
POP AX
JMP WORD PTR CS:[SI + OFFSET T2] ; RECOVER
; JMP TO AH=0 THRU AH=XX

;----- UTILITY ROUTINES
2340
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;----- SET DS TO THE DATA SEGMENT
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DDS PROC NEAR
PUSH AX
SUB AX,AX
MOV DS,AX
POP AX
RET
ENDP
WHAT_BASE ASSUME DS:ABSO
PUSH DS
CALL DDS
MOV DX,ADDR_6845
AND DL,0FH
OR DL,0AH
POP DS
RET
ENDP
OUT_RX PROC NEAR
XCHG AL,AH
OUT DX,AL
INC DX
XCHG AL,AH
OUT DX,AL
DEC DX
RET
ENDP
OUT_RX ENDN
WHAT_BASE ENDP
OUT_RX PROC NEAR
XCHG AL,AH
OUT DX,AL
INC DX
SET INDEX TO DATA REG
XCHG AL,AH
GET DATA VALUE
OUT DX,AL
SET DATA REG
DEC DX
SET DX BACK TO INDEX
RET
ENDP
OUT_RX ENDN
;----- ROUTINE TO SOUND BEEPER
BP_1 PROC NEAR
OUT DX,AL
RET
ENDP
BEEP PROC NEAR
PUSH DX
MOV DX,TIMER+4
MOV AL,10110110B
CALL BP_1
; SEL TIM 2, LSP, MSR, BINARY
; WRITE THE TIMER MODE REG
; DIVISOR FOR 1000 HZ
;----- ROUTINE TO SET UP BEEPER
BP_1 PROC NEAR
MOV AX,533H
MOV BX,BP_1
CALL BP_1
MOV AL,AH
CALL BP_1
MOV DX,PORT_B

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0D38 EC 2395 IN AL,DX ; GET SETTING OF PORT
0C39 8A E0 2396 MOV AH,AL ; SAVE THAT SETTING
0C3B OC 03 2397 OR AL,03 ; TURN SPEAKER ON
0C3D E8 0D1E R 2398 CALL BP_1 ; SET CNT TO WAIT 500 MS
0C3E 28 C9 2399 SUB CX,CX
0D42 2400 G7: ; SET CNT TO WAIT 500 MS
0D42 E2 FE 2401 LOOP G7 ; DELAY BEFORE TURNING OFF
0D44 FE CB 2402 DEC BL ; DELAY CNT EXPIRED?
0D46 75 FA 2403 JNZ G7 ; NO-CONTINUE BEEEPING SPK
0D48 40 C4 2404 MOV AH,AH ; RECOVER VALUE OF PORT
0D4A E8 0D1E R 2405 CALL BP_1
0D4D 5A 2406 POP DX
0D4E C3 2407 RET
0D4F 2408 BEEP ENDP ; RETURN TO CALLER
0D4F 2409
0D4F 2410 ;----- FIND THE PARAMETER TABLE VECTOR IN THE SAVE TABLE
0D4F 2411
0D4F 2412 SET_BASE PROC NEAR
0D4F 2413 ASSUME DS:ABSO
0D4F 2414 CALL DDS
0D4F 2415 LES BX,SAVE_PTR ; GET PTR TP PTR TABLE
0D4F 2416 LES BX,DWORD PTR ES:[BX] ; GET PARAMETER PTR
0D4F 2417 RET
0D4F 2418 SET_BASE ENDP
0D4F 2419
0D4F 2420 ;----- ESTABLISH ADDRESSING TO THE CORRECT MODE TABLE ENTRY
0D4F 2421
0D5A 51 2422 MAKE_BASE PROC NEAR
0D5A 52 2423 ASSUME DS:ABSO
0D5B 52 2424 PUSH CX
0D5B 52 2425 PUSH DX
0D5B 52 2426 CALL SET_BASE ; GET PARM TBL PTR
0D5B 52 2427 MOV AH,CRT_MODE ; TEST FOR BASE CARD
0D5B 52 2428 TEST INFO,_060H ; MIN MEMORY
0D5B 52 2429 JZ B_M_1
0D5B 52 2430
0D5B 52 2431
0D5B 52 2432 ;----- WE HAVE A MEMORY EXPANSION OPTION HERE
0D5B 52 2433 CMP AH,0FH
0D5B 52 2434 JNE B_M_2
0D5B 52 2435 ADD BX,BASE_2 - BASE_1
0D5B 52 2436 JMP B_M_OUT
0D5B 52 2437 B_M_2: CMP AH,010H
0D5B 52 2438 JNE B_M_1
0D5B 52 2439 ADD BX,BASE_2 + M_TBL_LEN - BASE_1
0D5B 52 2440 JMP B_M_OUT
0D5B 52 2441
0D5B 52 2442 B_M_1: CMP AH,03H
0D5B 52 2443 JA B_M_3 ; SKIP ENHANCED PORTION
0D5B 52 2444
0D5B 52 2445
0D5B 52 2446 ;----- CHECK THE SWITCH SETTING FOR ENHANCEMENT
0D5B 52 2447
0D5B 52 2448 BRS: ADD BX,BASE_3 - BASE_1 ; VECTOR TO ENHANCEMENT TBL
0D5B 52 2449
0D5B 52 2450 B_M_3: ADD BX,BASE_3 - BASE_1 ; VECTOR TO ENHANCEMENT TBL
0D5B 52 2451 MOV CL,CRT_MODE ; SECONDARY EMULATE SETTING
0D5B 52 2452 SUB CH,CH ; PRIMARY EMULATE SETTING
0D5B 52 2453 JE BRS
0D5B 52 2454 BRS
0D5B 52 2455 JMP B_M_3
0D5B 52 2456
0D5B 52 2457 ;----- WE WILL PERFORM ENHANCEMENT
0D5B 52 2458
0D5B 52 2459 BRS: ADD BX,BASE_3 - BASE_1 ; VECTOR TO ENHANCEMENT TBL
0D5B 52 2460 B_M_3: ADD BX,BASE_3 - BASE_1 ; VECTOR TO ENHANCEMENT TBL
0D5B 52 2461 MOV CL,CRT_MODE ; SECONDARY EMULATE SETTING
0D5B 52 2462 SUB CH,CH ; PRIMARY EMULATE SETTING
0D5B 52 2463 JCXZ B_M_4
0D5B 52 2464
0D5B 52 2465 ;----- THIS LOOP WILL MOVE THE PTR TO THE INDIVIDUAL MODE ENTRY
0D5B 52 2466 B_M_5: ADD BX,M_TBL_LEN ; LENGTH OF ONE MODE ENTRY
0D5B 52 2467 LOOP B_M_5
0D5B 52 2468 B_M_OUT: POP DX
0D5B 52 2469 POP CX
0D5B 52 2470 RET
0D5B 52 2471 MAKE_BASE ENDP
0D5B 52 2472
0D5B 52 2473
0D5B 52 2474
0D5B 52 2475
0D5B 52 2476
0D5B 52 2477 ;----- PROGRAM THE EGA REGISTERS FROM THE PARAMETER TABLE
0D5B 52 2478
0D5B 52 2479 SET_REGS PROC NEAR
0D5B 52 2480 ASSUME DS:ABSO,ES:NOTHING
0D5B 52 2481
0D5B 52 2482 ;----- PROGRAM THE SEQUENCER
0D5B 52 2483
0D5B 52 2484 CALL MAKE_BASE ; GET TABLE PTR
0D5B 52 2485 ADD BX,TF5_LEN ; MODE TO SEQUENCER PARMs
0D5B 52 2486 MOV DH,3 ; MODE TO SEQUENCER
0D5B 52 2487 MOV DL,SEQ_ADDR ; DISABLE INTERRUPTS
0D5B 52 2488 MOV AX,0001H ; RESET SEQUENCER
0D5B 52 2489 CLI ; DISABLE INTERRUPTS
0D5B 52 2490 CALL OUT_DX ; GET SEQUENCER VALUE
0D5B 52 2491 MOV AL,ES:[BX] ; NEXT INDEX
0D5B 52 2492 INC AH ; SET IT
0D5B 52 2493 CALL OUT_DX ; NEXT INDEX REGISTER
0D5B 52 2494
0D5B 52 2495 D1: INC AH ; NEXT INDEX REGISTER
0D5B 52 2496 INC BX ; NEXT TABLE ENTRY
0D5B 52 2497 MOV AL,ES:[BX]
0D5B 52 2498 CALL OUT_DX ; GET SEQUENCER VALUE
0D5B 52 2499 CMP AH,H1+1 ; NEXT INDEX
0D5B 52 2500 JB D1 ; SET IT
0D5B 52 2501
0D5B 52 2502
0D5B 52 2503 MOV AL,ES:[BX]
0D5B 52 2504 INC BX
0D5B 52 2505 MOV DL,MISC_OUTPUT ; START SEQUENCER
0D5B 52 2506 OUT DX,AL ; ENABLE INTERRUPTS
0D5B 52 2507 MOV DL,SEQ_ADDR ; CRTC INDEX REGISTER
0D5B 52 2508 MOV AX,0030H ; COUNTER
0D5B 52 2509 CALL OUT_DX ; GET VALUE FROM TABLE
0D5B 52 2510 STI ; SET CRTC REGISTER
0D5B 52 2511
0D5B 52 2512
0D5B 52 2513 X1: MOV DX,ADDR_6845 ; SET CRTC INDEX REGISTER
0D5B 52 2514 SUB AH,AH ; COUNTER
0D5B 52 2515
0D5B 52 2516 MOV AL,ES:[BX] ; GET VALUE FROM TABLE
0D5B 52 2517 CALL OUT_DX ; SET CRTC REGISTER
0D5B 52 2518 INC BX ; NEXT TABLE ENTRY
0D5B 52 2519 INC AH ; NEXT INDEX VALUE
0D5B 52 2520 CMP AH,M4 ; TEST REGISTER COUNT

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0DF4 72 F2 2521 JB X1 ; DO THE REST
0000 26: BB 47 F1 2522 XUNG AH, AL ; GET CURSOR MODE
0DFA 00 D460 R 2523 MOV CURSOR_MODE, AX ; SET LOW RAM VALUE
0DFC A3 D460 R 2524
0DFE 2525 ;----- PROGRAM THE ATTRIBUTE CHIP
0DFF BB F3 2526
0E01 E8 0D05 R 2527
0E04 EC 2528
0E05 BC C0 2529
0E07 2A E4 2530
0E09 2531
0E09 26: 8A 07 2532
0E0C 86 E0 2533
0E0C EE 2534
0E0F 86 E0 2535
0E11 EE 2536
0E11 2537
0E12 43 2538
0E13 FE C4 2539
0E15 80 FC 14 2540
0E18 72 EF 2541
0E1A 80 00 2542
0E1C EE 2543
0E1A 80 00 2544
0E1C EE 2545
0E1D 1E 2546
0E1E 06 2547
0E1F C4 3E 04A8 R 2548
0E23 26: C4 7D 04 2549
0E27 8C C0 2550
0E28 08 C7 2551
0E2B 74 09 2552
0E2D 1F 2553
0E2E 1E 2554
0E2F B9 0010 2555
0E32 F3/ A4 2556
0E34 46 2557
0E35 A4 2558
0E36 07 2559
0E37 1F 2560
0E38 B2 CC 2561
0E39 B0 00 2562
0E3C EE 2563
0E3D B2 CA 2564
0E3F B0 01 2565
0E41 EE 2566
0E42 BE CE 2567
0E44 2A EN 2568
0E46 26: 8A 07 2569
0E46 E8 0D15 R 2570
0E4C F3 2571
0E4D FE C4 2572
0E4F 80 FC 09 2573
0E52 72 F2 2574
0E54 C3 2575
0E55 2576
0E56 2577
0E57 2578
0E58 B2 CC 2579
0E59 B0 00 2580
0E5C EE 2581
0E5D B2 CA 2582
0E5F B0 01 2583
0E61 EE 2584
0E62 BE CE 2585
0E64 2A EN 2586
0E66 26: 8A 07 2587
0E66 E8 0D15 R 2588
0E69 F3 04 2589
0E70 2590
0E55 ;----- PROGRAM THE GRAPHICS CHIPS
2591
0E55 A0 0487 R 2592 BLANK PROC NEAR ; FILL REGEN WITH BLANKS
0E58 BB 80 2593 ASSUME DS:ABSO, ES:NOTHING
0E59 30 2594 MOV AL, INFO
0E5C BA 8800 2595 TEST AL, 080H ; SEE IF BLANK IS TO OCCUR
0E5F A0 0494 R 2596 JNZ AL, 080H ; MODE SET HIGH BIT
0E62 3C 06 2597 MOV DX, 08000H ; SKIP BLANK FOR REGEN
0E65 B0 DA 2598 MOV AL, CRT_MODE ; CURRENT MODE SET
0E66 BA 8800 2599 CMP AL, 6 ; 0-6 ARE COLOR MODES
0E69 3C 07 2600 CMP AL, 7 ; MONOCHROME REGEN ADDRESS
0E71 74 02 2601 CMP AL, 8 ; MONOCHROME MODE
0E73 76 03 2602 JE CG0 ; REMAINING MODES
0E60 BA A000 2603 MOV DX, 0A000H
0E70 2604
0E70 BB 0720 2605
0E73 3C 04 2606
0E75 72 06 2607
0E77 3C 07 2608
0E79 74 02 2609
0E7B 2B DB 2610
0E7D 2611
0E7D BB C2 2612
0E7F 88 DE 044C R + SRLOAD ES ; SET THE REGEN SEGMENT
0E83 E3 10 2613 DS, DX
0E85 B9 8000 2614 CLR,CRT_LEN
0E85 40 0A00 2615 JCXZ OUT_1
0E88 74 02 2616 MOV CX, 08000H
0E8D B5 40 2617 CMP DH, 0A0H
0E8F N_BA: 2618 JNC H_BA
0E8F BB C3 2619 MOV CR, 040H
0E8F 2B FF 2620
0E93 F3/ AB 2621
0E95 C3 2622
0E96 2623
0E96 E8 1DB7 R 2624
0E99 C3 2625
0E9A 2626
0E9A 2627
0E96 2628
0E96 E8 1DB7 R 2629
0E99 C3 2630
0E9A 2631
0E9A 2632
0E9A 2633 ;----- SEE IF WE ARE TO SUPPORT 640 X 350 ON A 640 X 200 MODE
0E9A 2634
0E9A 2635 BRST_DET PROC NEAR
0E9A 50 2636 ASSUME DS:ABSO
0E9B 1E 2637 PUSH AX
0E9C E8 0CFE R 2638 PUSH DS
0E9F 40 0488 R 2639 CALL DDS
0EA2 1F 2640 MOV AL, INFO_3
0EA2 2641 POP DS
0EA3 2C 0F 2642 AND AL, OFH ; EMULATE MODE
0EA3 3C 03 2643 CMP AL, 03H ; EMULATE MODE
0EA7 2C 07 2644 JNC B_YES
0EA9 3C 09 2645 CMP AL, 0DH
0EA9 7C 03 2646 JE B_YES

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OEAD 58          2647      POP    AX
OEEA F8          2648      CLC
OEEC C3          2649      RET
OEBO 2650
OECA 58          2651      B_YES: POP  AX
OEBC F9          2652      STC
OEBC C3          2653      RET
OEBS 2654      BRST_DET ENDP
OEBS 2655
OEBS 2656      ;----- MODE SET
OEBS 2657
OEBS 2658      AHO: ASSUME DS:ABSO
OEBS FA          2659
OEBA 8C 06 010C R 0000 E 2660      CLI
OEBA 8C 06 010C R 0000 E 2661      WORD PTR GRX_SET, OFFSET CGDDOT
OEBA FB          2662      MOV WORD PTR GRX_SET+2, CS
OEBS 2663      STI
OEBS 2664      AND INFO,,1111001B ; TURN OFF RETRACE BIT
OEBS 2665      PUSH AX ; EGA ACTIVE BIT
OEBS 2666      TEST INFO,,2 ; SAVE
OEBS 2667      JZ ST,,1
OECA 74 2C          2668      MOV AX,EQUIP_FLAG ; THERE IS NO MONOCHROME
OECC A1 0410 R 2669      MOV AL,00H ; THERE IS A COLOR
OECD 30 30          2670      OR AL,030H ; OTHER EQUIPMENT FLAG
OECD 30 30          2671      CMP AL,030H ; FOR MONOCHROME CALL
OECD 30 30          2672      JE ST,,2 ; IT IS A MONOCHROME CALL
OECD 74 48          2673
OECD 74 48          2674      ;----- FALL THROUGH => REGULAR COLOR CARD SETUP
OECD 74 48          2675
OEDE 06 0484 R 18 2676      MOV ROWS,,024D
OEDE C7 06 0485 R 0008 2677      MOV POINTS,,8
OEEO 5A 06 0487 R 08 2678      POP AX ; RECOVER
OEEO 5A 06 0487 R 08 2679      OR INFO,,0000100B ; EGA NOT ACTIVE
OEFE 3C 01          2680      CMP AL,1
OEES 76 09          2681      JBE ST,,7 ; WAIT FOR RETRACE ON
OEES 3C 04          2682      CMP AL,,4 ; MODES 2,3 ONLY
OEES 73 02          2683      JAE ST,,7 ; DO RETRACE
OEES 80 0E 0487 R 04 2684      OR INFO,,0000010B
OEFS 03 02          2685      ST,,7: INT 42H ; OTHER ADAPTER MODE CALL
OEFS 03 02          2686      JMP V_RET ; BACK TO CALLER
OEFS CD 42          2687
OEFS E9 219E R 2688
OEFS E9 219E R 2689      ;----- AT THIS POINT THERE IS NO MONOCHROME ATTACHED TO THE ADAPTER
OEFS E9 219E R 2690
OEFS E9 219E R 2691      ST,,1: MOV AX,EQUIP_FLAG ; TEST THE EQUIPMENT FLAG
OEFS E9 219E R 2692      AND AL,030F ; TO SEE IF THIS IS A
OEFS E9 219E R 2693      OR INFO,,0000100B ; MONOCHROME SETUP CALL
OEFS E9 219E R 2694      JNE ST,,3 ; MUST BE COLOR TO CARD
OEFS E9 219E R 2695
OEFS E9 219E R 2696
OEFS E9 219E R 2697      ;----- FALL THROUGH => REGULAR MONOCHROME CARD SETUP
OEFS E9 219E R 2698
OF01 C6 06 0484 R 18 2699      MOV ROWS,,024D
OF01 C7 06 0485 R 000E 2700      MOV POINTS,,014D
OF01 C7 06 0485 R 000E 2701      POP AX ; RECOVER
OF01 C7 06 0485 R 000E 2702      INT 10H ; OTHER ADAPTER MODE CALL
OF01 C7 06 0485 R 000E 2703      MOV CURSOR_MODE,OBOCH ; FIX PLANAR VALUE
OF01 C7 06 0485 R 000E 2704      OR INFO,,8 ; THE EGA IS NOT ACTIVE
OF1A E9 219E R 2705      JMP V_RET ; BACK TO CALLER
OF1D 58          2706
OF1D 58          2707
OF1D 58          2708      ;----- MONOCHROME SETUP TO THE ADAPTER
OF1D 58          2709      ST,,2: POP AX ; RECOVER
OF1E 50          2710      PUSH AX ; SAVE
OF1F B6 03          2711      MOV DH,,3 ; PICK OFF THE CLEAR BIT
OF21 24 80          2712      AND AL,080H ; MASK OFF THE OTHER BITS
OF21 24 80          2713      OR INFO,,0FH ; SAME AS CLEAR BIT
OF21 24 80          2714      AND INFO,,07FH ; RECOVER TRUE D7 VALUE
OF21 24 80          2715      POP AX ; ALREADY DEALT WITH D7
OF21 24 80          2716      AND AL,0FH ; A MONOCHROME MODE
OF21 24 80          2717      CMP AL,0FH ; DO THIS MODE
OF21 74 02          2718      JE ST,,2 ; REGULAR MONOCHROME
OF21 74 02          2719      MOV AL,,7
OF21 80 07          2720
OF21 80 07          2721      ST,,2A: MOV CRT_MODE,AL ; SAVE MODE VALUE
OF23 A2 0449 R 2722      MOV DL,CRTC_ADDR_B ; IT IS 3-B-X
OF23 B2 B4          2723      MOV ADDR,,6845,DX ; SAVE CRTC ADDRESS
OF3A B9 16 0463 R 2724      JMP QQ1 ; CONTINUE THE MODE SET
OF3E EB 1C 90          2725
OF3E EB 1C 90          2726
OF3E EB 1C 90          2727      ;----- COLOR SETUP TO THE ADAPTER
OF41 58          2728
OF41 58          2729      ST,,3: POP AX ; RECOVER PARAMETER VALUE
OF42 50          2730      PUSH AX ; SAVE IT
OF43 B6 03          2731      MOV DH,,3 ; ISOLATE ROWN CLEAR BIT
OF43 B6 03          2732      AND AL,080H ; PREPARE INFO BYTE
OF43 B6 03          2733      OR INFO,,0FH ; SET IT, OR NOT
OF43 B6 03          2734      POP AX ; RECOVER TRUE MODE CALL
OF43 B6 03          2735      AND AL,07FH ; DONE WITH D7
OF43 B6 03          2736      OR INFO,,07FH ; SAME THIS MODE
OF43 B6 03          2737      POP AX ; 3-D-X
OF43 B6 03          2738      MOV CRT_MODE,AL ; SAVE CRTC ADDRESS
OF43 B6 03          2739      MOV DL,CRTC_ADDR ; ADDR,,6845,DX
OF43 B6 03          2740
OF43 B6 03          2741      QQ1: MOV CRT_START,,0 ; SAVE START ADDRESS
OF43 B6 03          2742      MOV ACTIVE_PAGE,,0 ; RESET PAGE VALUE TO ZERO
OF43 B6 03          2743      ASSUME ES:NOTHING ; STORE PAGE ZERO
OF43 B6 03          2744      MOV AX,ES:PAGE,,0 ; GET COLUMN COUNT
OF43 B6 03          2745      MOV CX,,8 ; ZERO HIGH BYTE
OF43 B6 03          2746      SUB AX,AH ; STORE COLUMN VALUE
OF43 B6 03          2747      MOV CRT_COLS,AX ; STORE ROWN CLEAR
OF43 B6 03          2748      POP ES ; ZERO HIGH BYTE
OF43 B6 03          2749      SUP AX,AX ; STORE ROWN CLEAR
OF43 B6 03          2750      REP STOSW ; CLEAR OUT SAVED VALUES
OF73 E8 0D5A R 2751
OF73 E8 0D5A R 2752
OF73 E8 0D5A R 2753      CALL MAKE_BASE ; GET COLUMN COUNT
OF76 26: 8A 07 2754      MOV AL,ES:[BX] ; ZERO HIGH BYTE
OF79 2A E4 044A R 2755      SUB AH,AH ; STORE COLUMN VALUE
OF7B A3 044A R 2756      MOV CRT_COLS,AX ; STORE ROWN CLEAR
OF7B A3 044A R 2757
OF7E 26: 8A 47 01 2758      MOV AL,ES:[BX][1] ; GET ROW VALUE
OF82 A2 0484 R 2759      MOV ROWS,,AL ; STORE ROW VALUE
OF85 26: 8A 47 02 2760      MOV AL,ES:[BX][2] ; GET THE BYTES/CHAR
OF89 2A EH 0485 R 2761      SUB AH,AH ; ZERO HIGH BYTE
OF89 2A EH 0485 R 2762      MOV POINTS,AX ; STORE BYTES/CHAR
OF89 2A EH 0485 R 2763
OF8E 26: 8B 47 03 2764      MOV AX,ES:[BX][3] ; GET PAGE SIZE
OF92 A3 044C R 2765      MOV CRT_LEN,,AX ; STORE PAGE LENGTH
OF92 A3 044C R 2766
OF92 A3 044C R 2767      SUB BX,BX ; ZERO
OF95 2B DB 0485 R 2768      MOV AL,1 ; MONOCHROME ALPHA CHAR GEN
OF97 B0 01 0485 R 2769      MOV AH,CRT_MODE ; GET CURRENT MODE
OF99 8A 26 0449 R 2770      CMP AH,7 ; IS IT MONOCHROME
OF9D 80 FC 07 2771      JE ENTRY,,2 ; 9X14 FONT
OF9D 80 FC 07 2772
OF9D 80 FC 07 2773

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OFA2 80 FC 03      2773    CMP AH_03H
OFA5 77 35          2774    JA ENTRY_1
OFA7 E8 0E9A R      2775    CALL BRST_DET
OFAA 72 02          2776    JC ENTRY_2
OFAE BO 02          2777
OFAE 2779
OFAE E8 1EAE R      2780    MOV AL,2
OFB1 E8 OCFE R      2781    ENTRY_2:
OFB4 8A 26 0494 R    2782    CALL CH_GEN
OFB4 8A 26 0494 R    2783    CALL DDS
OFB5 8C 00 07          2784    MOV AH,CRT_MODE
OFB5 8C 00 07          2785    CMP AH,7
OFB5 8C 00 07          2786    JZ FDG_IT
OFB5 8C 00 07          2787    JMP ENTRY_1
OFC0 EB 10 90          2788    FDG_IT:
OFC0 BB 0000 E        2789    MOV BP,OFFSET CGMN_FDG
OFC0 BB 0000 E        2790    MOV BX,0EOOH
OFC6 0E              2791    FDG:
OFC7 07              2792    PUSH CS
OFC7 26 8B 56 00      2793    POP ES
OFC7 26 8B 56 00      2794    MOV DX,ES:[BP]
OFC7 26 8B 56 00      2795    OR DX,DX
OFC7 26 8B 56 00      2796    MOV CX,1
OFC7 26 8B 56 00      2797    INC BP
OFC7 26 8B 56 00      2798    CALL DO_MAP2
OFC7 26 8B 56 00      2799    ADD BP,014D
OFC7 26 8B 56 00      2800    JMP FDG
OFC7 26 8B 56 00      2801    ENTRY_1:
OFC7 26 8B 56 00      2802    CALL SET_REGS
OFC7 26 8B 56 00      2803    CALL BLANK
OFC7 26 8B 56 00      2804    CALL PH
OFC7 26 8B 56 00      2805    ASSUM DS:ABSO
OFC7 26 8B 56 00      2806    CALL DDS
OFC7 26 8B 56 00      2807    CMP CRT_MODE,0FH
OFC7 26 8B 56 00      2808    JB MS_T
OFC7 26 8B 56 00      2809    MOV WORD PTR GRX_SET , OFFSET CGMN
OFEF C7 06 010C R 0000 E 2810    MS_1:
OFEF C7 06 010C R 0000 E 2811    CMP CRT_MODE,7
OFEF C7 06 010C R 0000 E 2812    JA SAVE_GRPH
OFEF C7 06 010C R 0000 E 2813    JE SAVE_ALPH
OFEF C7 06 010C R 0000 E 2814    CMP CRT_MODE,3
OFEF C7 06 010C R 0000 E 2815    JBE SAVE_ALPH
OFEF C7 06 010C R 0000 E 2816    SAVE_GRPH:
OFEF C7 06 010C R 0000 E 2817    LES BX,SAVE_PTR
OFEF C7 06 010C R 0000 E 2818    ADD BX,0CH
OFEF C7 06 010C R 0000 E 2819    LES BX,DWORD PTR ES:[BX]
OFEF C7 06 010C R 0000 E 2820    MOV AX,ES
OFEF C7 06 010C R 0000 E 2821    OR AX,BX
OFEF C7 06 010C R 0000 E 2822    JZ J4J
OFEF C7 06 010C R 0000 E 2823    MOV SI,0TH
OFEF C7 06 010C R 0000 E 2824    ; JMP AHO_DONE
OFEF C7 06 010C R 0000 E 2825    SG_1:
OFEF C7 06 010C R 0000 E 2826    MOV AL,ES:[BX][SI]
OFEF C7 06 010C R 0000 E 2827    CMP AL,0FH
OFEF C7 06 010C R 0000 E 2828    JE AHO_DONE
OFEF C7 06 010C R 0000 E 2829    CMP AL,CRT_MODE
OFEF C7 06 010C R 0000 E 2830    JE SG_2
OFEF C7 06 010C R 0000 E 2831    INC SI
OFEF C7 06 010C R 0000 E 2832    JMP SG_1
OFEF C7 06 010C R 0000 E 2833    SG_2:
OFEF C7 06 010C R 0000 E 2834    CLI
OFEF C7 06 010C R 0000 E 2835    MOV AL,BYTE PTR ES:[BX]
OFEF C7 06 010C R 0000 E 2836    DEC AL
OFEF C7 06 010C R 0000 E 2837    MOV ROWS,AL
OFEF C7 06 010C R 0000 E 2838    MOV AX,WORD PTR ES:[BX][1]
OFEF C7 06 010C R 0000 E 2839    MOV WORD PTR ES:[BX][2]
OFEF C7 06 010C R 0000 E 2840    MOV AX,WORD PTR ES:[BX][3]
OFEF C7 06 010C R 0000 E 2841    MOV WORD PTR GRX_SET,[BX][3]
OFEF C7 06 010C R 0000 E 2842    MOV AX,WORD PTR ES:[BX][5]
OFEF C7 06 010C R 0000 E 2843    MOV WORD PTR GRX_SET + 2,AX
OFEF C7 06 010C R 0000 E 2844    STI
OFEF C7 06 010C R 0000 E 2845    J4J:   SHORT AHO_DONE
OFEF C7 06 010C R 0000 E 2846    SAVE_ALPH:
OFEF C7 06 010C R 0000 E 2847    LES BX,SAVE_PTR
OFEF C7 06 010C R 0000 E 2848    ADD BX,0SH
OFEF C7 06 010C R 0000 E 2849    LES BX,DWORD PTR ES:[BX]
OFEF C7 06 010C R 0000 E 2850    MOV AX,ES
OFEF C7 06 010C R 0000 E 2851    OR AX,BX
OFEF C7 06 010C R 0000 E 2852    JZ AHO_DONE
OFEF C7 06 010C R 0000 E 2853    MOV SI,0BH
OFEF C7 06 010C R 0000 E 2854    ; JMP AHO_DONE
OFEF C7 06 010C R 0000 E 2855    SA_1:
OFEF C7 06 010C R 0000 E 2856    MOV AL,ES:[BX][SI]
OFEF C7 06 010C R 0000 E 2857    CMP AL,0AH
OFEF C7 06 010C R 0000 E 2858    JE AHO_DONE
OFEF C7 06 010C R 0000 E 2859    CMP AL,CRT_MODE
OFEF C7 06 010C R 0000 E 2860    JE SA_2
OFEF C7 06 010C R 0000 E 2861    INC SI
OFEF C7 06 010C R 0000 E 2862    JMP SA_1
OFEF C7 06 010C R 0000 E 2863    SA_2:
OFEF C7 06 010C R 0000 E 2864    MOV AH,ES:[BX]
OFEF C7 06 010C R 0000 E 2865    MOV AL,ES:[BX][1]
OFEF C7 06 010C R 0000 E 2866    MOV DX,ES:[BX][2]
OFEF C7 06 010C R 0000 E 2867    MOV DS,ES:[BX][4]
OFEF C7 06 010C R 0000 E 2868    MOV BP,ES:[BX][6]
OFEF C7 06 010C R 0000 E 2869    MOV ES,ES:[BX][8]
OFEF C7 06 010C R 0000 E 2870    PUSH BX
OFEF C7 06 010C R 0000 E 2871    BX,AX
OFEF C7 06 010C R 0000 E 2872    MOV AX,1110H
OFEF C7 06 010C R 0000 E 2873    INT 10H
OFEF C7 06 010C R 0000 E 2874    POP BX
OFEF C7 06 010C R 0000 E 2875    MOV AL,ES:[BX][0AH]
OFEF C7 06 010C R 0000 E 2876    CMP AL,0FH
OFEF C7 06 010C R 0000 E 2877    JE AHO_DONE
OFEF C7 06 010C R 0000 E 2878    DEC AL
OFEF C7 06 010C R 0000 E 2879    MOV ROWS,AL
OFEF C7 06 010C R 0000 E 2880    ;----- SET THE LOW RAM VALUES FOR COMPATIBILITY (308 AND 309 SAVE BYTES)
OFEF C7 06 010C R 0000 E 2881
OFEF C7 06 010C R 0000 E 2882
OFEF C7 06 010C R 0000 E 2883
OFEF C7 06 010C R 0000 E 2884    AHO_DONE:
OFEF C7 06 010C R 0000 E 2885    CALL DDS
OFEF C7 06 010C R 0000 E 2886    CMP CRT_MODE,7
OFEF C7 06 010C R 0000 E 2887    JA DNDCS
OFEF C7 06 010C R 0000 E 2888    MOV BX,OFFSET COMPAT_MODE
OFEF C7 06 010C R 0000 E 2889    MOV AL,CRT_MODE
OFEF C7 06 010C R 0000 E 2890    SUB AL,CRT_MODE
OFEF C7 06 010C R 0000 E 2891    ADD BX,AX
OFEF C7 06 010C R 0000 E 2892    MOV AL,CS:[BX]
OFEF C7 06 010C R 0000 E 2893    MOV CRT_MODE_SET,AL
OFEF C7 06 010C R 0000 E 2894    ADD AL,0100H
OFEF C7 06 010C R 0000 E 2895    CMP CRT_MODE,6
OFEF C7 06 010C R 0000 E 2896    JNE DO_PAL
OFEF C7 06 010C R 0000 E 2897    MOV AL,03FH
OFEF C7 06 010C R 0000 E 2898    DO_PAL:
OFEF C7 06 010C R 0000 E 2899    MOV CRT_PALETTE,AL

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10C1 2B 0E 0460 R 2699 NDCS: MOV CX_CURSOR_MODE
10C1 2B 28 90 2900 JMP AH1
10C2 2901
10C2 2902 COMPAT_MODE LABEL BYTE
10C8 2C 28 2D 29 2A 2E 2903 DB 02CH,028H,02DH,029H,02AH,02EH
10CE 1E 29 2904 DB 01EH,029H
10C2 2905 PAGE
10C2 2906
1000 2911 INCLUDE V1+5..INC
1000 80 FD 00 2912 SUBTL V1+5..INC
1003 75 04 2908 PAGE
1005 F5 C1 2909
1007 EE 0A 2910
1009 FE C1 2911 CALC_CURSOR PROC NEAR
10DB 3A 0E 0485 R 2912 ASSUME DS:ABSO
10DF 72 02 2913 CMP CH,0 ; CHECK FOR FULL HEIGHT
10E1 2A C9 2914 JNE CC_1 ; NORMAL CHECK
10E3 51 2915 INC CL ; ADJUST END VALUE
10E4 2A CD 2916 JMP SHORT CALC_OUT
10E5 80 F9 10 2917 CC_1: INC CL ; ADJUST FOR EGA REGISTERS
10E9 29 2918 CMP CL_BYT PTR POINTS ; WILL IT WRAP
10E9 75 02 2919 JB CALC_OUT ; NO, ITS OK
10EC FE C1 2920 C SUB CL,CL ; EGA METHOD FOR CURSOR END
10EE C3 2921 CALC_OUT: PUSH CX ; SAVE CURSOR TYPE VALUE
10EF 2922 SUB CL,CH ; END - START
10F0 2923 CMP CL,010H ; LOW NIBBLE EQUAL
10F1 2924 POP CX ; RESTORE
10F2 2925 JNE COMP_4 ; ADD 1 FOR CORRECT CURSOR
10F3 2926 INC CL
10F4 2927 COMP_4: INC CL
10F5 2928 RET ; BACK TO CALLER
10F6 2929 CALC_CURSOR ENDP
10F7 2930
10F8 = 0004 2931
10F9 2932
10F9 2933 ;-----SET_CURSOR_TYPE----- ;-----SET_CURSOR_TYPE----- ;-----SET_CURSOR_TYPE-----
10F9 2934 SET_CURSOR_TYPE THIS ROUTINE SETS THE CURSOR VALUE ;-----SET_CURSOR_TYPE----- ;-----SET_CURSOR_TYPE----- ;-----SET_CURSOR_TYPE-----
10F9 2935 INPUT : (CX) HAS CURSOR VALUE CH-START LINE, CL-STOP LINE ;-----SET_CURSOR_TYPE----- ;-----SET_CURSOR_TYPE----- ;-----SET_CURSOR_TYPE-----
10F9 2936 OUTPUT : NONE ;-----SET_CURSOR_TYPE----- ;-----SET_CURSOR_TYPE----- ;-----SET_CURSOR_TYPE-----
10F9 2937
10F9 2938
10F9 2939
10F9 2940
10F9 2941 CUT_OFF EQU 4
10F9 2942
10F9 AH1: ASSUME DS:ABSO
10F9 2943 MOV AH,C_CSR_Start ; CRTC REG FOR CURSOR SET
10F9 2944 MOV CURSOR_MODE,CX ; SAVE IN DATA AREA
10F9 2945 TEST INFO_8 ;EGA ACTIVE BIT
10F9 2946 JNZ DO_SET ;0=EGA, 1=OLD CARDS
10F9 2947
10F9 2948 ;----- THIS SECTION WILL EMULATE CURSOR OFF ON THE EGA
10F9 2949
10F9 2950 MOV AL,CH ; GET START VALUE
10F9 2951 AND AL,0E0H ; TURN OFF CURSOR ?
10F9 2952 CMP AL,020H ; TEST THE BITS
10F9 2953 JNE AH1_A ; SKIP CURSOR OFF
10F9 2954 MOV CX,01E00H ; EMULATE CURSOR OFF
10F9 2955 ADD CL,DO_SET
10F9 2956 JMP SHORT DO_SET
10F9 2957
10F9 2958 ;----- THIS SECTION : ADJUST THE CURSOR AND TEST FOR ENHANCED OPERATION
10F9 2959
10F9 2960
10F9 2961 AH1_A: TEST INFO_1 ; CURSOR EMULATE BIT
10F9 2962 JNZ DO_SET ;0=EMULATE, 1=VALUE AS-IS
10F9 2963 CMP CRT_MODE,3 ; POSSIBLE EMULATION
10F9 2964 JA AH1_S ; NO, SET THE CURSOR TYPE
10F9 2965 CALL BRST_DET ; SET EMULATE MODE
10F9 2966 INC AH ; TEST START
10F9 2967 CMP CH,CUT_OFF ; SKIP EMULATING
10F9 2968 JBE AH1_B ; TEST START
10F9 2969 ADD CH,5 ; SKIP ADJUST
10F9 AH1_B: ADD CL,5 ; ADJUST
10F9 2970
10F9 2971 CMP CL,CUT_OFF ; TEST END
10F9 2972 JBE AH1_S ; SKIP ADJUST
10F9 2973 ADD CL,5
10F9 2974 AH1_S: CALL CALC_CURSOR ; ADJUST END REGISTER
10F9 2975 DO_SET: CALL M16 ; OUTPUT CX REG
10F9 2976 JMP V_RET ; RETURN TO CALLER
10F9 2977
10F9 2978
10F9 2979
10F9 2980 ;----- THIS ROUTINE OUTPUTS THE CX REGISTER TO THE CRTC REGS NAMED IN AH
10F9 2981
10F9 2982
10F9 2983 M16: MOV DX,ADDR_6845 ; ADDRESS REGISTER
10F9 2984 MOV AL,CH ; DATA
10F9 2985 CALL OUT_DX ; OUTPUT THE VALUE
10F9 2986 INC AH ; NEXT REGISTER
10F9 2987 MOV AL,CL ; SECOND DATA VALUE
10F9 2988 CALL OUT_DX ; OUTPUT THE VALUE
10F9 2989 RET ; ALL DONE
10F9 2990
10F9 2991
10F9 2992
10F9 2993 POSITION THIS SERVICE ROUTINE CALCULATES THE REGEN BUFFER
10F9 2994 ADDRESS OF A CHARACTER IN THE ALPHA MODE ;-----POSITION----- ;-----POSITION----- ;-----POSITION-----
10F9 2995 INPUT : AX = ROW, COLUMN POSITION ;-----POSITION----- ;-----POSITION----- ;-----POSITION-----
10F9 2996 OUTPUT : AX = OFFSET OF CHAR POSITION IN REGEN BUFFER ;-----POSITION----- ;-----POSITION----- ;-----POSITION-----
10F9 2997
10F9 2998
10F9 2999 POSITION PROC NEAR
1146 53 3000
1147 8B D8 3001 PUSH BX ; SAVE REGISTER
1149 8A C4 3002 MOV BX,AH ; ROW TO AL
114B F6 26 044A R 3003 MOV AL,AH ; DETERMINE BYTES TO ROW
114C 35 FF 3004 MUL BYTE PTR CRT_COLS ; ZERO OUT BYTES
1151 53 3005 XOR BH,BH ; #2 FOR COLUMN VALUE
1153 D1 E0 3006 ADD AX,BX ; * 2 FOR ATTRIBUTE BYTES
1155 5B 3007 SAL AX,1 ; RESTORE REGISTER
1156 C3 3008 POP BX
1157 3009 RET
1157 POSITION ENDP
1157 3010
1157 3011
1157 3012
1157 3013
1157 3014
1157 3015
1157 3016
1157 3017
1157 3018
1157 3019
1157 3020
1157 3021
1157 3022
1157 3023
1157 3024
1157 AH2: CALL SET_CPOS

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115A E9 219E R 3025 C JMP V_RET
115D 3026 C
115D 8A CF 3027 C SET_CPOS:
115F 32 ED 3028 C MOV CL,BH
1161 D1 E1 3029 C XOR CH,CH
1163 31 F1 3030 C SAL CX,1
1165 89 94 0450 R 3031 C ; ESTABLISH LOOP COUNT
1169 38 3E 0462 R 3032 C XOR SI,CX ; WORD OFFSET
116D 75 05 3033 C MOV [SI+OFFSET_CURSOR_POSN],DX ; USE INDEX REGISTER
116F 88 C2 3034 C CMP ACTIVE_PAGE,BH ; SAVE THE POINTER
1171 E8 1175 R 3035 C JNZ M17 ; SET_CPOS_RETURN
1174 C3 3036 C MOV AX,DX ; GET ROW/COLUMN TO AX
1174 3037 C CALL M18 ; CURSOR_SET
1174 C3 3038 C ; SET_CPOS_RETURN
1174 3039 C
1174 3040 C ;----- SET CURSOR POSITION, AX HAS ROW/COLUMN FOR CURSOR
1175 3041 C
1175 E8 1146 R 3042 C M18 PROC NEAR
1178 8B C8 3043 C POSITION ; DETERMINE LOC IN REGEN
117A 03 0E 044E R 3044 C MOV CX,AX
117E D1 F9 3045 C ADD CX,CRT_START ; ADD IN THE START ADDR
1180 B4 0E 3046 C SAR CX,1 ; FOR THIS PAGE
1182 E8 1135 R 3047 C MOV AH,C_RCSR_LOC_HGH ; / 2 FOR CHAR ONLY COUNT
1185 C3 3048 C CALL M16 ; REGISTER NUMBER FOR CURSO
1186 3049 C RET ; SET VALUE TO CRTIC
1186 3050 C M18 ENDP
1186 3051 C
1186 3052 C
1186 3053 C
1186 3054 C
1186 3055 C
1186 3056 C
1186 3057 C
1186 3058 C
1186 3059 C
1186 3060 C
1186 3061 C
1186 3062 C
1186 3063 C
1186 3064 C AH3: MOV BL,BH ; PAGE VALUE
1186 3065 C XOR BH,BH ; ZERO UPPER BYTE
1186 3066 C SAL BX,1 ; WORD OFFSET
1186 3067 C MOV [BX + OFFSET_CURSOR_POSN] ; GET CURSOR FOR THIS PAGE
1186 3068 C MOV CX,CURSOR_MODE ; GET THE CURSOR MODE
1186 3069 C POP DI
1186 3070 C POP SI
1186 3071 C POP BX
1186 3072 C POP AX ; DISCARD CX
1186 3073 C POP DS ; DISCARD DX
1186 3074 C POP ES
1186 3075 C POP BP
1186 3076 C IRET
1186 3077 C
1186 3078 C ;----- READ LIGHT PEN POSITION
1186 3079 C
1186 3080 C AH4: MOV AL,CRT_MODE
1186 3081 C CMP AL,07H
1186 3082 C JA READ_LPEN
1186 3083 C
1186 3084 C
1186 3085 C
1186 3086 C TEST INFO_2
1186 3087 C JZ EGA_IS_COLOR
1186 3088 C
1186 3089 C ;----- MONOCHROME HERE (MONOC BIT 1)
1186 3090 C
1186 3091 C CMP AL,07H
1186 3092 C JE READ_LPEN
1186 3093 C JMP OLD_LP
1186 3094 C
1186 3095 C
1186 3096 C
1186 3097 C
1186 3098 C
1186 3099 C
1186 3100 C
1186 3101 C
1186 3102 C
1186 3103 C
1186 3104 C
1186 3105 C
1186 3106 C
1186 3107 C
1186 3108 C
1186 3109 C
1186 3110 C
1186 3111 C
1186 3112 C
1186 3113 C
1186 3114 C
1186 3115 C
1186 3116 C
1186 3117 C
1186 3118 C
1186 3119 C
1186 3120 C
1186 3121 C
1186 3122 C
1186 3123 C
1186 3124 C
1186 3125 C
1186 3126 C
1186 3127 C
11C1 06 07 07 05 05 3128 C ASSUME CS:CODE,DS:ABSO
11C1 04 05 00 00 00 00 3129 C SUBTRACT_TABLE
11C1 04 06 00 04 04 04 3130 C LABEL BYTE
11C1 04 06 00 04 04 04 3130 C DB 006H,006H,007H,007H,005H,005H ; 0-5
11C1 04 06 00 04 04 04 3131 C DB 004H,005H,000H,000H,000H,000H ; 6-8
11C1 04 06 00 04 04 04 3132 C DB 004H,005H,006H,004H,004H,004H ; 9-11
11C1 04 06 00 04 04 04 3133 C DB 004H,006H,006H,004H,004H,004H ; 12-17
11C1 07 04 3133 C DB 007H,004H ; 18-19
11DB 3134 C
11DB 3135 C READ_LPEN PROC NEAR
11DB 3136 C
11DB 3137 C ;----- WAIT FOR LIGHT PEN TO BE DEPRESSED
11DB 3138 C
11DB 3139 C MOV DX,ADDR_6845 ; GET BASE ADDRESS OF 6845
11DB 3140 C ADD DX,6 ; POINT TO STATUS REGISTER
11E2 EC 3141 C IN AL,4 ; GET STATUS REGISTER
11E3 A8 04 3142 C TEST AL,4 ; TEST LIGHT PEN SWITCH
11E5 B4 00 3143 C MOV AH,0 ; SET NO LIGHT PEN RETURN
11E7 7B 03 3144 C JZ V9 ; CODE
11E9 E9 1291 R 3145 C JMP V6 ; NOT SET, RETURN
11E9 3146 C
11E9 3147 C ;----- NOW TEST FOR LIGHT PEN TRIGGER
11E9 3148 C
11E9 3149 C V9: TEST AL,2 ; TEST LIGHT PEN TRIGGER

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11EE 75 03      3151 C   JNZ    V7A          ; RETURN WITHOUT RESETTING
11F0 E9 129B R   3152 C   JMP    V7          ; TRIGGER
11F3 B4 10      3153 C   JMP    V7          ; EXIT LIGHT PEN ROUTINE
11F3 B4 10      3154 C   ;----- TRIGGER HAS BEEN SET, READ THE VALUE IN
11F3 B4 10      3155 C
11F3 B4 10      3156 C
11F3 B4 10      3157 C   VTA:  MOV    AH, 16        ; LIGHT PEN REGISTERS
11F3 B4 10      3158 C   ;----- INPUT REGS POINTED TO BY AH, AND CONVERT TO ROW COLUMN IN DX
11F3 B4 10      3159 C
11F3 B4 10      3160 C
11F3 B4 10      3161 C
11F5 BB 16 0463 R 3162 C   INPUT DX, ADDR_6845 ; ADDRESS REGISTER
11F9 8A C4      3163 C   MOV    AL, AH        ; REGISTER TO READ
11FB EE          3164 C   OUT   DX, AL        ; SET IT UP
11FC 5C          3165 C   INC    CH           ; DATA REGISTER
11FD 50          3166 C   PUSH   AX           ; GET THE VALUE
11FE EC          3167 C   IM    AL, DX        ; SAVE IN CX
11FF 8A E8      3168 C   MOV    CH, AL        ; ADDRESS REGISTER
1201 58          3169 C   POP    AX           ; SECOND DATA REGISTER
1202 44          3170 C   DEC    DX           ; POINT TO DATA REGISTER
1203 FF C4      3171 C   INC    AH           ; GET THE 2ND DATA VALUE
1205 8A C4      3172 C   MOV    AL, AH        ; AX HAS INPUT VALUE
1207 EE          3173 C   OUT   DX, AL        ; AX HAS INPUT VALUE
1208 42          3174 C   INC    DX           ; ADDRESS REGISTER
1209 EC          3175 C   IM    AL, DX        ; SCREEN ADDRESS
120A 8A E5      3176 C   MOV    AH, CH        ; DIVIDE BY 2
120A 8A E5      3177 C   ;----- AX HAS THE VALUE READ IN FROM THE 6845
120A 8A E5      3178 C
120C 8A 1E 0449 R 3179 C   MOV    BL, CRT_MODE ; MODE VALUE TO BX
1210 2A FF      3180 C   SUB   BH, BH        ; AMOUNT TO SUBTRACT
1212 2E 8A 9F 11C1 R 3181 C   MOV    BC, CS:V1[BX] ; TELL IT WHERE
1213 2D 01      3182 C   SUB   AX, BX        ; SCREEN ADDRESS
1214 2D 01      3183 C   ADD   AX, AX        ; DIVIDE BY 2
1215 88 1E 044E R 3184 C   MOV    BX, CRT_START ; ADJUST TO ZERO START
121D D1 EB      3185 C   SHR    BX, 1         ; IF POSITIVE, GET MODE
121F 2B C3      3186 C   SUB   AX, BX        ; <0 PLAYS AS 0
1221 79 02      3187 C   JNS    V2           ;----- DETERMINE MODE OF OPERATION
1223 2B C0      3188 C   SUB   AX, AX        ;----- DETERMINE MODE OF OPERATION
1223 2B C0      3189 C
1225 B1 03      3190 C
1227 80 3E 0449 R 04 3191 C
122C 72 4D      3192 C
122E 80 3E 0449 R 07 3193 C   V2:   MOV    CL, 3         ; DETERMINE_MODE
1233 74 46      3194 C   CMP    CRT_MODE, 4 ; SET *8 SHFT COUNT
1234 74 46      3195 C   JB    V4           ; GRAPHICS OR ALPHA
1234 74 46      3196 C   CMP    CRT_MODE, 7 ; ALPHA_PEN
1234 74 46      3197 C   JE    V4           ;----- DETERMINE GRAPHIC ROW POSITION
1234 74 46      3198 C
1235 80 3E 0449 R 06 3199 C   VBX:  MOV    DL, 40        ;----- OLD GRAPHICS MODES
123A 77 28      3200 C   JA    V8           ;----- DIVISOR FOR GRAPHICS
123C 75 02      3201 C   JNE   V8           ;----- ROW(AL) AND COLUMN(AH)
123E D1 E8      3202 C   SHR   AA, 1         ;----- AL RANGE 0-99,
1240 B2 28      3203 C   ;----- BH RANGE 0-39
1242 F6 F2      3204 C
1240 B2 28      3205 C
1242 F6 F2      3206 C   VBX:  MOV    DL, 40        ;----- DIVISOR FOR GRAPHICS
1242 F6 F2      3207 C   DIV   DL, 40        ;----- ROW(AL) AND COLUMN(AH)
1242 F6 F2      3208 C   ;----- AL RANGE 0-99,
1242 F6 F2      3209 C   ;----- BH RANGE 0-39
1242 F6 F2      3210 C
1242 F6 F2      3211 C   ;----- DETERMINE GRAPHIC ROW POSITION
1242 F6 F2      3212 C
124H 8A E8      3213 C   MOV    CH, AL        ;----- SAVE ROW VALUE IN CH
124D 8D DC      3214 C   ADD   CH, CH        ;----- # FOR PVA/DIVIDE FIELD
124B 8A DC      3215 C   MOV    BH, BH        ;----- COLUMN VALUE TO BX
124A 2A FF      3216 C   SUB   BH, BH        ;----- #8 FOR MEDIUM RES
124C 80 3E 0449 R 06 3217 C   CMP    CRT_MODE, 6 ;----- MEDIUM OR HIGH RES
1251 75 04      3218 C   JNE   V3           ;----- NOT HIGH RES
1253 B1 04      3219 C   MOV    CL, 4         ;----- SAVING #1 FOR HIGH RES
1255 D0 E4      3220 C   SAL    AH, 1         ;----- COLUMN VALUE *2 FOR HIGH RES
1257 D3 E3      3221 C   ;----- NOT HIGH RES
1257 D3 E3      3222 C   SHL    BX, CL        ;----- *16 FOR HIGH RES
1257 D3 E3      3223 C
1257 D3 E3      3224 C
1257 D3 E3      3225 C   ;----- DETERMINE ALPHA CHAR POSITION
1257 D3 E3      3226 C   MOV    DL, AH        ;----- COLUMN VALUE FOR RETURN
1258 B4 F0      3227 C   MOV    DH, AL        ;----- ROW VALUE
1259 D0 E4      3228 C   SHR   DH, 1         ;----- DIVIDE BY 4
125F D0 E4      3229 C   ;----- TOTAL VALUE IN 0-24 RANGE
1261 EB 2C 90      3230 C   JMP   V5           ;----- LIGHT_PEN_RETURN_SET
1264          3231 C
1264          3232 C
1264          3233 C
1264          3234 C   ;----- NEW GRAPHICS MODES
1264 99          3235 C   CWD
1265 F7 36 044A R 3236 C   DIV   BYTE PTR CRT_COLS ;----- PREPARE TO DIVIDE
1265 F7 36 044A R 3237 C   MOV    DX, CRT_COLS ;----- AL ROW, DX = COLUMN
1266 89 DA      3238 C   MOV    DH, AL        ;----- SAVE COUNTER
1268 D3 E3      3239 C   SAL    BX, CL        ;----- PEL COLUMN
1260 8B C8      3240 C   MOV    CX, AX        ;----- PEL ROW
1267 52          3241 C   PUSH   AX           ;----- SAVE FROM DIVIDE
1267 09          3242 C   CWD
1271 F7 36 0485 R 3243 C   DIV   BYTES PTR POINTS ;----- PREPARE TO DIVIDE
1275 5A          3244 C   POP    DX           ;----- DIVIDE BY BYTES/CHAR
1276 EB F0      3245 C   MOV    DH, AL        ;----- RECOVER
1278 EB 15 90      3246 C   JMP   V5           ;----- CHARACTER ROW
1278 EB 15 90      3247 C
1278 EB 15 90      3248 C   ;----- ALPHA MODE ON LIGHT PEN
127B F6 36 044A R 3249 C   V4:   DIV   BYTE PTR CRT_COLS ;----- ALPHA_PEN
127F 8A F0      3250 C   MOV    DH, AL        ;----- ROW,COLUMN VALUE
1281 8A D4      3251 C   MOV    DL, AH        ;----- ROW_TO_DH
1283 8A DC      3252 C   MOV    BL, AH        ;----- COLS_TO_DL
1285 32 FF      3253 C   XOR    BH, BH        ;----- COLUMN_VALUE
1287 D1 E3      3254 C   SAL    DL, 1         ;----- TO BX
1289 F6 26 0485 R 3255 C   MUL   BYTES PTR POINTS ;----- LIGHT_PEN_RETURN_SET
128D 8B C8      3256 C
128F B4 01      3257 C   MOV    CX, AX        ;----- LIGHT_PEN_EVENING_SET
1291 52          3258 C   ;----- LIGHT_PEN_RETURN_SET
1291 52          3259 C   PUSH   DX           ;----- SAVE RETURN VALUE
1291 52          3260 C   ;----- (IN CASE
1292 8B 16 0463 R 3261 C   MOV    DX, ADDR_6845 ;----- GET BASE ADDRESS
1296 83 C2 07      3262 C   ADD   DX, 7         ;----- POINT TO RESET PARM
1299 EE          3263 C   OUT   DX, AL        ;----- ADDRESS, NOT DATA,
129A 5A          3264 C   ;----- IS IMPORTANT
129B 5F          3265 C   POP    DX           ;----- RECOVER VALUE
129C 5E          3266 C   ;----- RETURN_NO_RESET
129D 83 C4 06      3267 C   POP    DI           ;----- DISCARD SAVED BX,CX,DX
12A1 07          3268 C   POP    SI           ;----- ADDRESS
12A2 5D          3269 C   ADD   DS, 6         ;----- DS
12A3 CF          3270 C   POP    ES           ;----- ES
12A4          3271 C   ADD   DS, 6         ;----- BP
12A4          3272 C   POP    BP           ;----- BP
12A4          3273 C   POP    DS           ;----- DS
12A4          3274 C   POP    ES           ;----- ES
12A4          3275 C   IRET
12A4          3276 C   READ_LPEN ENDP

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3277 C
3278 C
3279 C ; ACT_DISP_PAGE   SELECT ACTIVE DISPLAY PAGE
3280 C THIS ROUTINE SETS THE ACTIVE DISPLAY PAGE, ALLOWING
3281 C FOR MULTIPLE PAGES OF DISPLAYED VIDEO.
3282 C
3283 C INPUT    AL HAS THE NEW ACTIVE DISPLAY PAGE
3284 C
3285 C OUTPUT   THE CRTC IS RESET TO DISPLAY THAT PAGE
3286 C
3287 C
1244 A2 0462 R
1245 8B 0E 044C R
1247 3288 C AH5: MOV ACTIVE_PAGE,AL ; SAVE ACTIVE PAGE VALUE
1248 98 3289 C MOV CX,CRT_LEN ; GET_SAVED_LENGTH_OF
1249 50 3290 C CBW REGEN_BUFFER
1250 F7 E1 3291 C PUSH AX CONVERT_AL_TO_WORD
1251 3292 C MUL CX SAVE_PAGE_VALUE
1252 D1 3293 C CMP BL,7 DIV_BY_PAGES_TIMES
1253 A3 3294 C JA ADP_1 REGEN_LENGTH
1254 BB 3295 C MOV CRT_START,AX ; SAVE_START_ADDRESS_FOR
1255 C8 3296 C MOV CX,AX LATER_REQUIREMENTS
1256 8A 1E 3297 C MOV BL,CRT_MODE ; START_ADDRESS_TO_CX
1257 0449 R 3298 C
1258 80 FB 07 3299 C
1259 77 02 3300 C
1260 D1 F9 3301 C ADP_2: SAR CX,1 ; / 2 FOR CRTC HANDLING
1261 3302 C ADP_1: MOV AH,C_STRT_HGH ; REG_FOR_START_ADDRESS
1262 BB 0C 3303 C CALL M16
1263 E8 1135 R 3304 C PUSH BX ; RECOVER_PAGE_VALUE
1264 3305 C POP DS ; #2 FOR WORD_OFFSET
1265 0D 3306 C
1266 D1 E3 3307 C SAH DS,1 ; GET_CURSOR_FOR_THIS_PAGE
1267 BB 87 3308 C MOV AX,[BX + OFFSET_CURSOR_POSN]
1268 E8 1175 R 3309 C CALL M18 ; SET_CURSOR_POSITION
1269 E9 219E R 3310 C JMP V_RET
1270 3311 C
1271 3312 C SUBTTL
1272 3313 C INCLUDE VSCROLL.INC
1273 3314 C SUBTTL VSCROLL.INC
1274 PAGE 3315 C
1275 3316 C
1276 3317 C
1277 3318 C FLTA PROC NEAR ; CHECK FOR SCROLL COUNT
1278 3319 C PUSH AX
1279 3320 C MOV AL,DH ; LOWER ROW
1280 3321 C SUB AH,CH ; UPPER ROW
1281 3322 C INC AH ; NUMBER_TO_SCROLL
1282 3323 C CMP AH,AL ; SAME_AS_REQUESTED
1283 3324 C POP AX
1284 3325 C JNE LTA
1285 75 02 3326 C SUB AL,AL ; YES, SET TO 0 FOR BLANK
1286 2A CO 3327 C
1287 C3 3328 C LTA: RET
1288 ENDP
1289 3329 C FLTA
1290 3330 C
1291 3331 C CRANK PROC NEAR ; MOVE_ROWS_OF_PELS_UP
1292 50 3332 C PUSH BX
1293 8A E6 3333 C ASSUME DS:ABSO ; SAVE DATA_SEGMENT
1294 2A E5 3334 C PUSH DS ; SET DATA_SEGMENT
1295 E4 3335 C CALL DDS
1296 3A EO 3336 C MOV BX,CRT_COLS
1297 0B 3337 C POP DS
1298 5F 3338 C
1299 51 3339 C CRANK_A: PUSH CX ; SAVE_MOVE_COUNT
1300 8A CA 3340 C MOV CL,DL ; COLUMN_COUNT
1301 2A ED 3341 C SUB CH,CH ; CLEAR_HIGH_BYTE
1302 56 3342 C PUSH SI ; SAVE_POINTERS
1303 F7 3343 C PUSH DI
1304 F3/ A4 3344 C REP MOVS
1305 5F 3345 C POP DI ; MOVE_THAT_ROW
1306 5E 3346 C POP SI ; RECOVER_POINTERS
1307 03 F3 3347 C ADD DI,BX ; NEXT_ROW
1308 03 FB 3348 C ADD DI,BX ; NEXT_ROW
1309 59 3349 C POP CX ; RECOVER_ROW_COUNT
1310 E2 EE 3350 C LOOP CRANK_A ; DO_MORE
1311 58 3351 C POP BX
1312 63 3352 C RET
1313 3353 C CRANK ENDP ; RETURN_TO_CALLER
1314 3354 C
1315 53 3355 C CRANK_4 PROC NEAR ; MOVE_ROWS_OF_PELS_DOWN
1316 3356 C PUSH BX
1317 3357 C ASSUME DS:ABSO ; SAVE DATA_SEGMENT
1318 3358 C PUSH DS ; SET DATA_SEGMENT
1319 3359 C CALL DDS
1320 3360 C MOV BX,CRT_COLS
1321 1F 3361 C POP DS
1322 3362 C CRANK_B: PUSH CX ; SAVE_MOVE_COUNT
1323 3363 C MOV CL,DL ; COLUMN_COUNT
1324 3364 C SUB CH,CH ; CLEAR_HIGH_BYTE
1325 3365 C PUSH SI ; SAVE_POINTERS
1326 3366 C PUSH DI
1327 3367 C REP MOVS
1328 3368 C POP DI ; MOVE_THAT_ROW
1329 3369 C POP SI ; RECOVER_POINTERS
1330 3370 C SUB SI,BX ; NEXT_ROW
1331 2B F3 3371 C SUB DI,BX ; NEXT_ROW
1332 5B 3372 C POP CX ; RECOVER_ROW_COUNT
1333 52 EE 3373 C LOOP CRANK_B ; DO_MORE
1334 3374 C POP BX
1335 5B C3 3375 C RET
1336 3376 C CRANK_ENDP ; RETURN_TO_CALLER
1337 3377 C
1338 52 3378 C PART_1 PROC NEAR ; FILL_ROW_AFTER_SCROLL
1339 8A CA 3379 C PUSH DX
1340 B6 03 3380 C MOV DH,3
1341 B2 C4 3381 C MOV DX,SEQ_ADDR ; SEQUENCER
1342 0070F 3382 C MOV AX,020FH ; MAP_MASK
1343 E8 0D15 R 3383 C CALL OUT_DX ; ALL_MAPS_ON
1344 5A 3384 C POP DX ; ZERO
1345 2B C0 3385 C SUB AX,AX ; COLUMN_COUNT
1346 8A CA 3386 C MOV DL,DX ; SET_ZEROS
1347 B4 ED 3387 C SUB CH,CH
1348 3388 C PUSH DI
1349 57 3389 C PUSH SI ; SAVE_POINTER
1350 F3/ AA 3390 C REP STOSB ; CLEAR_ONE_ROW_OF_PELS
1351 51 3391 C POP DI ; RECOVER_POINTER
1352 8A G6 3392 C MOV AL,DH ; GET_COLOR_VALUE
1353 52 3393 C PUSH DX
1354 B6 03 3394 C MOV DH,3 ; SEQUENCER
1355 B2 C4 3395 C MOV DX,SEQ_ADDR ; MAP_MASK
1356 0020 3396 C MOV AH,02H ; SET_THE_COLOR
1357 E8 0D15 R 3397 C CALL OUT_DX
1358 5A 3398 C POP DX
1359 B0 FF 3399 C MOV AX,0FFH ; ALL_BITS_ON
1360 8A CA 3400 C MOV DL,DX ; COLUMN_COUNT
1361 57 3401 C PUSH DI ; SAVE_POINTER
1362 F3/ AA 3402 C REP STOSB ; TURN_ON_THOSE_BITS_IN

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13EE          3529      C N7:                                ; BLANK_FIELD
13EE 8A DE    3530      C MOV BL, DH
13F0 EB DC    3531      C JMP N3
13F2          3532      C SCROLL_UP ENDP
13F2          3533      C
13F2          3534      C ;----- HANDLE COMMON SCROLL SET UP HERE
13F2          3535      C
13F2          3536      C SCROLL_POSITION PROC NEAR
13F2          3537      C TEST INFO,4
13F2          3538      C JZ N9
13F2          3539      C
13F2          3540      C ;----- 80X25 COLOR CARD SCROLL
13F2          3541      C
13F9 52       3542      C PUSH DX
13F9 B6 03    3543      C MOV DH, 3
13FC DA       3544      C MOV DL, ODAH
13FE 50       3545      C PUSH AX
13FF          3546      C NB:
13FF EC       3547      C IN AL, DX
1400 00 08    3548      C TEST AL, 8
1402 76 FB    3549      C JZ N9
1404 B0 25    3550      C MOV AL, 25H
1406 B2 D8    3551      C MOV DL, ODH
1408 EE C0    3552      C OUT DX, AL
1409 55       3553      C POP AX
140A 5A       3554      C POP DX
140B          3555      C
140B E8 1146 R 3556      C CALL POSITION
140B          3557      C ADD AX, CRT_START
1412 BB F8    3558      C PUSH DI
1414 BB F0    3559      C MOV SI, AX
1416 B2 D1    3560      C SUB DX, CX
1418 A1 FE 22 3561      C INC DI
141C 32 ED    3562      C XOR DL
141E BB 2E 044A R 3563      C XOR CH, CH
1422 03 ED    3564      C MOV BP, CRT_COLS
1424 00 03    3565      C ADD BP, BP
1426 F6 26 044A R 3566      C MOV AL, BL
142A 03 CO    3567      C MUL BYTE PTR CRT_COLS
142C 06       3568      C ADD AX, AX
142E 80 FB 00 3569      C PUSH ES
1431 C3       3570      C POP DS
1432          3571      C CMP BL, 0
1432          3572      C RET
1432          3573      C SCROLL_POSITION ENDP
1432          3574      C
1432          3575      C ;----- MOVE_ROW
1432          3576      C
1432          3577      C N10: PROC NEAR
1432          3578      C MOV CL, DL
1434 56       3579      C PUSH SI
1435 57       3580      C PUSH DI
1436 F3/ A5   3581      C REP MOVS
1438 5F       3582      C POP DI
1439 5E       3583      C POP SI
143A C3       3584      C RET
143B          3585      C N10 ENDP
1432          3586      C
1432          3587      C ;----- CLEAR_ROW
1432          3588      C N11: PROC NEAR
1432          3589      C MOV CL, DL
1432          3590      C PUSH DI
1432          3591      C REP STOS
1432          3592      C POP DI
1432          3593      C RET
1442          3594      C N11 ENDP
1432          3595      C
1432          3596      C
1432          3597      C
1432          3598      C
1432          3599      C
1432          3599      C ;SCROLL_DOWN
1432          3600      C ; THIS ROUTINE MOVES THE CHARACTERS WITHIN A
1432          3601      C ; DEFINED BLOCK DOWN ON THE SCREEN, FILLING THE
1432          3602      C ; TOP LINES WITH A DEFINED CHARACTER
1432          3603      C ; INPUT
1432          3604      C ; (AH) = CURRENT CRT MODE
1432          3605      C ; (AL) = NUMBER OF LINES TO SCROLL
1432          3606      C ; (CX) = UPPER LEFT CORNER OF REGION
1432          3607      C ; (DX) = LOWER RIGHT CORNER OF REGION
1432          3608      C ; (ES) = CHARACTER
1432          3609      C ; (DS) = DATA SEGMENT
1432          3610      C ; (ES) = REGEN SEGMENT
1432          3611      C ; OUTPUT
1432          3612      C ; NONE -- SCREEN IS SCROLLED
1432          3613      C SCROLL_DOWN PROC NEAR
1432          3614      C STD
1442 FD       3615      C MOV BL, AL
1442          3616      C CALL MK_ES
1442          3617      C PUSH BX
1448 53       3618      C MOV AX, DX
1448          3619      C CALL SCROLL_POSITION
1448          3620      C ADD SI, CX
1450 28 F0    3621      C SUB SI, AX
1452 8A E6    3622      C MOV AH, DH
1454 2A E3    3623      C SUB AH, BL
1456          3624      C
1456 E8 1432 R 3625      C N13: CALL N10
1459 2B F5    3626      C SUB SI, BP
1458 2B FD    3627      C SUB DI, BP
1450 FE CC    3628      C DEC AH
1459 7F       3629      C JNZ N13
1461          3630      C
1461 58       3631      C N14: POP AX, AL
1462 B0 20    3632      C MOV AL, 1
1462          3633      C
1464 E8 1438 R 3634      C N15: CALL N11
1467 2B FD    3635      C SUB DI, BP
1469 FE CB    3636      C DEC BL
1468 75 F7    3637      C JNZ N15
1470 E9 13DA R 3638      C
1470          3639      C N16: MOV BL, DH
1470 8A DE    3640      C JMP N14
1472 EB ED    3641      C
1474          3642      C SCROLL_DOWN ENDP
1474          3643      C
1474          3644      C ;SCROLL_UP
1474          3645      C ; THIS ROUTINE SCROLLS UP THE INFORMATION ON THE CRT
1474          3646      C ; ENTRY
1474          3647      C ; CH, CL = UPPER LEFT CORNER OF REGION TO SCROLL
1474          3648      C ; DH, DL = LOWER RIGHT CORNER OF REGION TO SCROLL
1474          3649      C ; BOTH OF THE ABOVE ARE IN CHARACTER POSITIONS
1474          3650      C ; BH = FULL VALUE FOR BLANKED LINES
1474          3651      C ; AL = # LINES TO SCROLL (AL=0 MEANS BLANK THE ENTIRE
1474          3652      C ; FIELD)
1474          3653      C ; DS = DATA SEGMENT
1474          3654      C

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3655      C ; EXIT      ES = REGEN SEGMENT
3656      C ; NOTHING, THE SCREEN IS SCROLLED
3657      C -----
3658      C ;----- GRAPHICS_UP PROC NEAR
3659      C     MOV    BL, AL          ; SAVE LINE COUNT IN BL
3660      C     MOV    AX, CX          ; GET UPPER LEFT POSITION
3661      C     MOV    AX, CX          ; INTO AX REG
3662      C
3663      C ;----- USE CHARACTER SUBROUTINE FOR POSITIONING
3664      C ;----- ADDRESS RETURNED IS MULTIPLIED BY 2 FROM CORRECT VALUE
3665      C
3666      C     CALL   GRAPH_POS
3667      C     MOV    DI, AX          ; SAVE RESULT AS
3668      C     MOV    DI, AX          ; DESTINATION ADDRESS
3669      C
3670      C ;----- DETERMINE SIZE OF WINDOW
3671      C     SUB    DX, CX
3672      C     ADD    DX, 101H        ; ADJUST VALUES
3673      C     SAL    DH, 1           ; MULTIPLY # ROWS BY 4
3674      C     SAL    DH, 1           ; SINCE 8 VERT DOTS/CHAR
3675      C     SAL    DH, 1           ; AND EVEN/ODD ROWS
3676      C
3677      C ;----- DETERMINE CRT MODE
3678      C     SUB    DX, CX
3679      C     CMP    CRT_MODE, 6
3680      C     JNC    R7             ; TEST FOR MEDIUM RES
3681      C
3682      C ;----- MEDIUM RES UP
3683      C     SAL    DL, 1           ; FIND_SOURCE
3684      C     SAL    DL, 1           ; GET SEGMENTS BOTH
3685      C     SAL    DL, 1           ; POINTING TO REGEN
3686      C     SAL    DL, 1           ; 0 TO HIGH OF COUNT REG
3687      C     SAL    DL, 1           ; NUMBER OF LINES *4
3688      C     SAL    DL, 1           ; IF 0, BLANK ENTIRE FIELD
3689      C     SAL    DL, 1           ; NUMBER OF LINES IN AL
3690      C     SAL    DL, 1           ; 80 BYTES/ROW
3691      C     SAL    DL, 1           ; OFFSET TO SOURCE
3692      C     PUSH   DS             ; SET UP SOURCE
3693      C     POP    DS             ; ADD IN OFFSET TO IT
3694      C     SUB    DI, 1           ; NUMBER OF ROWS IN FIELD
3695      C     SAL    AH, BH          ; DETERMINE NUMBER TO MOVE
3696      C
3697      C ;----- DETERMINE THE SOURCE ADDRESS IN THE BUFFER
3698      C     SUB    DI, 1           ; FIND_SOURCE
3699      C     PUSH   DS             ; GET SEGMENTS BOTH
3700      C     POP    DS             ; POINTING TO REGEN
3701      C     SUB    DI, 1           ; 0 TO HIGH OF COUNT REG
3702      C     SAL    DL, 1           ; NUMBER OF LINES *4
3703      C     SAL    DL, 1           ; IF 0, BLANK ENTIRE FIELD
3704      C     SAL    DL, 1           ; NUMBER OF LINES IN AL
3705      C     SAL    DL, 1           ; 80 BYTES/ROW
3706      C     SAL    DL, 1           ; OFFSET TO SOURCE
3707      C     PUSH   DS             ; SET UP SOURCE
3708      C     POP    DS             ; ADD IN OFFSET TO IT
3709      C     SUB    DI, 1           ; NUMBER OF ROWS IN FIELD
3710      C     SAL    AH, BH          ; DETERMINE NUMBER TO MOVE
3711      C
3712      C ;----- LOOP THROUGH, MOVING ONE ROW AT A TIME, BOTH EVEN AND ODD FIELDS
3713      C     CALL   R7             ; ROW_LOOP
3714      C     SUB    SI, 1           ; MOVE ONE ROW
3715      C     SUB    DI, 2000H-80    ; MOVE TO NEXT ROW
3716      C     DEC    AH             ; NUMBER OF ROWS TO MOVE
3717      C     JNZ    R8             ; CONTINUE TILL ALL MOVED
3718      C
3719      C ;----- FILL IN THE VACATED LINE(S)
3720      C     CALL   R10            ; CLEAR_ENTRY
3721      C     SUB    DI, 2000H-80    ; ATTRIBUTE TO FILL WITH
3722      C     DEC    AH             ; CLEAR THAT ROW
3723      C     JNZ    R10            ; POINT TO NEXT LINE
3724      C     JMP    V_RET          ; NUMBER OF LINES TO FILL
3725      C     REP    STOSB         ; CLEAR_LOOP
3726      C     MOV    BL, DH          ; BLANK_FIELD
3727      C     ADD    SI, 1           ; SET COUNT TO
3728      C     MOV    AH, BH          ; EVERYTHING IN FIELD
3729      C     JMP    R9             ; CLEAR THE FIELD
3730      C     ENDP
3731      C
3732      C ;----- ROUTINE TO MOVE ONE ROW OF INFORMATION
3733      C     PROC   NEAR
3734      C     MOV    CL, DL          ; NUM OF BYTES IN THE ROW
3735      C     PUSH   SI             ; SAVE POINTERS
3736      C     PUSH   DI             ; MOVE THE EVEN FIELD
3737      C     REP    MOVS             ; POINT TO THE ODD FIELD
3738      C     POP    DI             ; SAVE THE POINTERS
3739      C     ADD    DI, 2000H        ; COUNT BACK
3740      C     ADD    DI, 2000H        ; MOVE THE ODD FIELD
3741      C     RET
3742      C     ADD    DI, 2000H        ; POINTERS BACK
3743      C     RET
3744      C     ADD    DI, 2000H        ; RETURN TO CALLER
3745      C
3746      C ;----- CLEAR A SINGLE ROW
3747      C     PROC   NEAR
3748      C     MOV    CL, DL          ; NUMBER OF BYTES IN FIELD
3749      C     PUSH   DI             ; SAVE POINTER
3750      C     REP    STOSB         ; STORE THE NEW VALUE
3751      C     ADD    DI, 2000H        ; POINTER BACK
3752      C     ADD    DI, 2000H        ; POINT TO ODD FIELD
3753      C     RET
3754      C     REP    STOSB         ; FILL THE ODD FIELD
3755      C     ADD    DI, 2000H        ; RETURN TO CALLER
3756      C
3757      C ;----- MEM_DET PROC NEAR
3758      C     ASSUME DS:ABSO
3759      C     PUSH   AX             ; SAVE POINTER
3760      C     PUSH   DS             ; STORE THE NEW VALUE
3761      C     CALL   DS             ; POINTER BACK
3762      C     MOV    AH, INFO          ; POINT TO ODD FIELD
3763      C     AND    AH, 060H        ; FILL THE ODD FIELD
3764      C     RET
3765      C
3766      C ;----- HMIN PROC NEAR
3767      C     ASSUME DS:ABSO
3768      C     PUSH   AX             ; SAVE POINTER
3769      C     PUSH   DS             ; STORE THE NEW VALUE
3770      C     CALL   DS             ; POINTER BACK
3771      C     MOV    AH, INFO          ; MIN
3772      C     AND    AH, 060H        ; STC
3773      C     POP    DS             ; RET
3774      C     POP    AX             ; MIN
3775      C     STC
3776      C     RET
3777      C     HMIN: RET
3778      C     CLC
3779      C     RET
3780      C

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1508          3781 C MEM_DET ENDP
1508          3782 C ;----- SCROLL ACTIVE PAGE UP
1508          3783 C SC_2:
1508 E9 13A3 R 3786 C JMP SCROLL_UP
150E          3787 C
150E          3788 C AH6:
1511 E8 12D1 R 3789 C ASSUME DS:ABSO
1511 BA 26 0449 R 3790 C CALL FILEH
1515 BA FC 07   3791 C MOV AH,CRT_MODE
1518 76 F1     3792 C CMP AH,07H ; GET CURRENT MODE
151A 80 FC 0D   3793 C JBE SC_2 ; ANY OF THE OLD MODES
1511 73 10     3794 C CMP AH,0DH ; NEW GRAPHICS MODES
151F E9 219E R 3795 C JAE GRAPHICS_UP_2 ; NOT A RECOGNIZED MODE
151F           3796 C JMP V_RET
1522          3797 C
1522          3798 C GR_ST_1 PROC NEAR
1525 BA A000   3799 C MOV DX,0A000H ; REGEN BUFFER
1525 BD 0511   3800 C MOV BF,0501H ; GRAPHICS WRITE MODE
1528 BA FC 0F   3801 C CMP AH,OFH
1528 72 08     3802 C JBE SC_2
152D E8 14F7 R 3803 C CALL _MEM_DET
1531 73 08     3804 C JNC VV1
1532 BD 0501   3805 C MOV BF,0501H ; GRAPHICS WRITE MODE
1535           3806 C
1535 C3       3807 C RET
1536           3808 C
1536           3809 C GR_ST_1 ENDP
1536          3810 C GRAPHICS_UP_2 PROC NEAR
1536          3811 C ASSUME DS:ABSO
1537 E8 1522 R 3812 C PUSH DX
1537           3813 C CALL GR_ST_1 ; SET SEGMENT, WRITE MODE
1537           3814 C SRLLOAD ES,DX ; SET REGEN
153A 8E C2     3815 C+
153C 5A       3816 C MOV ES,DX
153D 88 D8     3817 C POP BX
153F BB C1     3818 C MOV BL,AL ; NUMBER OF LINES
1541 53       3819 C PUSH BX ; UPPER LEFT CORNER
1542 BA 3E 0462 R 3820 C MOV BH,ACTION_PAGE ; ACTIVE PAGE FOR SCROLL
1546 E8 16C8 R 3821 C CALL GRX_PSN ; ADDRESS IN REGEN
1547 5B       3822 C POP BX
1548 BB F8     3823 C MOV DI,AX ; SET POINTER
154C 2B D1     3824 C SUB DX,CX ; DETERMINE WINDOW
154E B1 C2 0101 3825 C ADD DX,0101H ; ADJUST
154F 5B       3826 C SUB AX,BH ; ZERO HIGH BYTE
1554 BA C3     3827 C MOV AL,DL ; LINE COUNT
1556 52       3828 C PUSH DX ; BYTES PER CHARACTER
1557 F7 26 0485 R 3829 C MUL POINTS ; COLUMNS
1558 F7 26 04A4 R 3830 C MUL CRT_COLS ; SET UP SOURCE INDEX
155B BB F7     3831 C MOV SI,BH ; SET UP
1561 03 F0     3832 C ADD SI,AX ; ADJUST
1563 06       3833 C ASSUME DS:NOTHING
1564 1F       3834 C PUSH ES
1565 5A       3835 C POP DS ; LOW MEMORY SEGMENT
1566 0A DB     3836 C POP DX
1568 74 3F     3837 C OR BL,BL ; LINE COUNT
156A BA CE     3838 C JZ AR9
156C 2A CB     3839 C MOV CL,DH ; BYTES PER CHAR
156E 2A ED     3840 C SUB CL,BH ; SET THE COUNT
156F           3841 C SUB CH,CH
1570 1E       3842 C
1571 E8 0CFE R 3843 C ASSUME DS:ABSO
1571           3844 C PUSH DS
1574 50       3845 C CALL DDS ; SEQUENCER
1575 52       3846 C PUSH AX ; ENABLE ALL MAPS
1576 BB C1     3847 C PUSH DX
1577 F7 26 0485 R 3848 C MOV AX,CX
157C BB C8     3849 C HUL POINTS ; SET THE COUNT
157E 5A       3850 C MOV CX,AX
157F 58       3851 C POP DX
1580 1F       3852 C POP AX
1581 52       3853 C ASSUME DS:NOTHING
1581           3854 C POP DS ; SCROLL THE SCREEN
1584 BB C5     3855 C
1584 BB C3     3856 C PUSH DX
1584 BB C3     3857 C MOV AX,BP
1584 BB C3     3858 C MOV DH,3
1586 BB CE     3859 C MOV DL,GRAPH_ADDR ; GRAPHICS
1588 E8 0015 R 3860 C CALL OUT_DX ; COLOR MODE
1588 BB C0     3861 C MOV DL,SEQ_ADDR ; SEQUENCER
1589 E8 020F R 3862 C MOV AH,020FH ; ENABLE ALL MAPS
1590 E8 0D15 R 3863 C CALL OUT_DX
1593 5A       3864 C POP DX
1594 E8 12E2 R 3865 C CALL CRANK
1595           3866 C
1597 52       3867 C PUSH DX ; SCROLL ACTIVE DISPLAY PAGE DOWN
1598 4D       3868 C DEC BP
1599 BB C5     3869 C MOV AX,BP ; BLK_N_3
1599 BB C3     3870 C MOV DH,3 ; V_RET
159D BB CE     3871 C MOV DL,GRAPH_ADDR ; BLANK ENTIRE WINDOW
159F E8 0D15 R 3872 C CALL OUT_DX
15A2 5A       3873 C POP DX
15A3           3874 C
15A3 E8 1353 R 3875 C AR10: CALL BLNK_3
15A6 E9 219E R 3876 C JMP V_RET
15A9           3877 C
15A9 BA DE     3878 C MOV BL,DH ; AR9
15A8 EB F6     3879 C JMP AR10
15AD           3880 C GRAPHICS_UP_2 ENDP
15AD          3881 C ;----- SCROLL ACTIVE DISPLAY PAGE DOWN
15AD          3882 C
15AD          3883 C
15AD          3884 C SC_3:
15AD          3885 C JMP SCROLL_DOWN
15AD          3886 C
15AD          3887 C AH7: ASSUME DS:ABSO
1580 E8 12D1 R 3888 C CALL FILEH ; OLD COLOR ALPHA
1583 BA 26 0449 R 3889 C MOV AH,CRT_MODE ; MONOCHROME ALPHA
1587 BA FC 03   3890 C CMP AH,03H ; NEW GRAPHICS MODES
158A 76 F1     3891 C JBE SC_3 ; OLD GRAPHICS MODES
158C BA FC 07   3892 C CMP AH,07H
158F 74 EC     3893 C JNE SC_3
158F           3894 C JE SC_3
1591           3895 C
15C1 80 FC 0D   3896 C CMP AH,0DH ; NEW GRAPHICS MODES
15C4 73 0C 06   3897 C JAE GRAPHICS_DN_2 ; OLD GRAPHICS MODES
15C4 73 0C 06   3898 C CMP AH,06H
15C9 77 04     3899 C JA M_0 ; NEW GRAPHICS MODES
15CB BB 07     3900 C MOV AH,07H ; OLD GRAPHICS MODES
15CD CD 42     3901 C INT 42H
15CF           3902 C
15CF E9 219E R 3903 C M_0: JMP V_RET
1502           3904 C
1502 FD       3905 C GRAPHICS_DN_2 PROC NEAR ; DIRECTION TO DECREMENT
1502 FD       3906 C STD

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1503 8A D8 3907 C MOV BL, AL ; LINE COUNT
1505 52 3908 C PUSH DX ; SAVE LOWER RIGHT
1506 E8 1522 R 3909 C CALL GRX_ST_1 ; SET REGEN SEGMENT
1509 8E C2 3911 C+ MOV ES, DX
1508 5A 3912 C POP DX
1507 8C C2 3913 C MOV AX, DX
150E FE C4 3914 C INC AH ; MOV CHAR ROW UP BY ONE
150E 53 3915 C PUSH BX
15E1 8A 3E 0462 R 3916 C MOV BH, ACTIVE_PAGE ; ADDRESS IN REGEN
15E5 E8 16C6 R 3917 C CALL GRX_PSN ; SET REGEN SEGMENT
15F0 52 3918 C POP DX
15E9 2B 06 044A R 3919 C SUB AX, CRT_COLS ; ONE SCAN OVERSHOOT
15ED 8B F8 3920 C MOV DI, AX ; CALCULATE WINDOW
15EF 2B D1 3921 C SUW DX, CX ; ADJUST COUNT
15F1 2B 00 0101 3922 C ADD DX, 010H
15F5 2A ED 3923 C SUB AH, AH ; SET DS TO
15F7 6A C3 3924 C MOV AL, BL ; THE REGEN SEGMENT
15F9 52 3925 C PUSH DX
15F6 F7 26 0485 R 3926 C MUL POINTS ; BYTES PER CHAR
15F6 F7 26 044A R 3927 HULL CRT_COLS ; BYTES PER ROW
1602 BB F7 3928 C MOV SI, DI
1604 BB F0 3929 C SUB CL, BL
1604 2B F0 3930 C SUB CH, CH ; SET DS:NOTHING
1606 06 3931 C ASSUME DS:NOTHING ; SCROLL COUNT
1607 1F 3932 C PUSH ES ; BLANK ENTIRE WINDOW
1609 5A 3933 C POP DX
1609 0A DB 3934 C OR BL, BL
160D 7A C0 3935 C JZ DXR9 ; SCROLL COUNT
160D 5A CE 3936 C MOV DL, DH
160F 2A CB 3937 C SUB CL, BL
1611 2A ED 3938 C SUB CH, CH ; SET DS:NOTHING
1613 1E 3939 C ASSUME DS:ABSO ; SET DS TO
1614 E8 0CFE R 3940 C PUSH DS ; THE REGEN SEGMENT
1617 50 3941 C CALL DDS ; SCROLL COUNT
1618 52 3942 C PUSH AX ; BLANK ENTIRE WINDOW
1619 BB C1 3943 C PUSH DX
161B F7 26 0485 R 3944 C PUSH AX ; SET DS:NOTHING
161F 8B C8 3945 C NOV AX, CX ; BYTES PER CHAR
1621 5A 3946 C MUL POINTS ; SCROLL COUNT
1622 58 3947 C MOV CX, AX ; BLANK ENTIRE WINDOW
1623 1F 3948 C POP DX
1624 52 3949 C ASSUME DS:NOTHING ; SET DS:NOTHING
1625 BB C5 3950 C POP DS ; SCROLL COUNT
1627 BB 03 3951 C CALL CRANK_4 ; BLANK ENTIRE WINDOW
1629 B2 CE 3952 C PUSH DX
1630 E8 0D15 R 3953 C MOV AH, BP ; GRAPHICS
1632 B2 C4 3954 C MOV DH, 3 ; SEQUENCER
1630 BB 020F 3955 C MOV DL, GRAPH_ADDR ; ENABLE ALL MAPS
1633 E8 0D15 R 3956 C CALL OUT_DX ; SCROLL THE SCREEN
1636 5A 3957 C POP DX
1637 E8 12FE R 3958 C CALL CRANK_4 ; SCROLL THE SCREEN
163A 52 3959 C PUSH DX
163B 4D 3960 C DEC BP
163C 80 C5 3961 C MOV AX, BP ; GRAPHICS
163E BB 03 3962 C MOV DV, BP ; SEQUENCER
1640 B2 CE 3963 C MOV DL, GRAPH_ADDR ; ENABLE ALL MAPS
1642 E8 0D15 R 3964 C CALL OUT_DX ; SCROLL THE SCREEN
1645 5A 3965 C POP DX
1646 E8 137B R 3966 C DXR10: CALL BLNK_4 ; SCROLL THE SCREEN
1649 FC 3967 C CLD ; SCROLL THE SCREEN
164A E9 219E R 3968 C JMP V_RET ; SCROLL THE SCREEN
164D 10 3969 C DXR9: NOV BL, DH ; BLANK ENTIRE WINDOW
164E 8A DE 3970 C JMP DXR10 ; BLANK ENTIRE WINDOW
164F EB F5 3971 C GRAPHICS_DN_2 ENDP ; BLANK ENTIRE WINDOW
1651 00 3972 C
1651 8A CF 3973 C
1651 32 D0 3974 C
1655 BB F1 3975 C
1651 D1 E6 3976 C
1659 BB 84 0450 R 3977 C
165D 33 DB 3978 C
165E 03 00 3979 C
1661 E3 06 3980 C
1661 03 1E 044C R 3981 C
1665 E2 FA 3982 C INCLUDE VGRW.INC ; DISPLAY PAGE TO CX
1667 6A C0 3983 C SUBTTL VGRW.INC ; MOVE TO SI FOR INDEX
1668 00 3984 C PAGE ; *2 FOR WORD OFFSET
1669 00 3985 C
1669 00 3986 C
1651 ASSUME DS:ABSO ; ROW/COLUMN OF THAT PAGE
1651 FIND_POSITION PROC NEAR ; SET START ADDRESS TO 0
1651 3987 C MOV CL, BH ; PAGE_LOOP
1651 3988 C XOR CH, CH ; LENGTH OF BUFFER
1651 3989 C NOV CX ; PAGE_LOOP
1651 3990 C SAL SI, 1 ; LENGTH OF BUFFER
1651 3991 C MOV AX, [SI + OFFSET_CURSOR_POS] ; NO_PAGE
1651 3992 C XOR BX, BX ; DETERMINE LOC IN REGEN
1651 3993 C JCZ P5 ; ADD TO START OF REGEN
1651 3994 C
P4: 3995 C ADD BX, CRT_LEN ; FIND_POSITION
1651 3996 C LOOP P4 ; ENDP
1651 3997 C
P5: 3998 C CALL POSITION ; EXPAND_MED_COLOR
1651 3999 C ADD BX, AX ; THIS ROUTINE EXPANDS THE LOW 2 BITS IN BL TO
1651 4000 C RET ; FILL THE ENTIRE BX REGISTER
1651 4001 C
1651 4002 C
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1651 4007 C
1651 4008 C
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1681 4027 C
1682 4028 C
1660 S19: PROC NEAR ; ISOLATE THE COLOR BITS
1660 4029 C AND BL, 3 ; COPY TO AL
1660 4030 C MOV AL, BL ; SAVE REGISTER
1660 4031 C PUSH CX ; NUMBER OF TIMES
1660 4032 C MOV CX, 3 ; LEFT SHIFT BY 2
1660 S20: SAL AL, 1 ; ANOTHER COLOR VERSION
1660 4033 C SAL AL, 1 ; INTO BL
1660 4034 C OR BL, AL ; FILL ALL OF BL
1660 4035 C LOOP S20 ; FILL ALL OF BL
1660 4036 C POP CX ; REGISTER BACK
1660 4037 C RET ; ALL DONE
1660 S19: ENDP ; EXPAND_BYT
1660 4038 C
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1660 4891 C
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1660 4893 C
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1660 4923 C
1660 4924 C
1660 
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1715 06          4159  C      PUSH   ES
1716 1F          4160  C      POP    DS
1717 74 0B        4161  C      JZ    P3A
1718          4162  C
1719          4163  C
1719          4164  C      ;----- WAIT FOR HORIZONTAL RETRACE
1719          4165  C
1719          4166  C
1719          4167  C      P2:     IN     AL,DX
1719          4168  C      TEST   AL,1
1719          4169  C      JNZ    P2
1719          4170  C      CLI
1719          4171  C
1719          4172  C      P3:     IN     AL,DX
1719          4173  C      TEST   AL,1
1719          4174  C      JZ    P3
1719          4175  C      P3A:   LODSW  JMP    V_RET
1720          4176  C      READ_AC_CURRENT ENDP
1720          4177  C
1720          4178  C
1720          4179  C
1720          4180  C
1720          4181  C
1720          4182  C      MED_READ_BYTE
1720          4183  C      THIS ROUTINE WILL TAKE 2 BYTES FROM THE REGEN
1720          4184  C      BUFFER, COMBINE AGAINST THE CURRENT FOREGROUND
1720          4185  C      COLOR, AND PLACE THE CORRESPONDING ON/OFF BIT
1720          4186  C      PATTERN INTO THE CURRENT POSITION IN THE SAVE
1720          4187  C      AREA
1720          4188  C      ENTRY   S1,DS = POINTER TO REGEN AREA OF INTEREST
1720          4189  C      BX = EXPANDED FOREGROUND COLOR
1720          4190  C      BP = POINTER TO SAVE AREA
1720          4191  C
1720          4192  C      EXIT    BP IS INCREMENT AFTER SAVE
1720          4193  C
1728          4194  C      S23:   PROC   NEAR
1728          4195  C      MOV    AH,[S1]
1728          4196  C      MOV    AL,[S1+1]
1728          4197  C      MOV    CX,0C00H
1728          4198  C
1730          4199  C      S24:   PROC   NEAR
1730          4200  C      MOV    DL,0
1732          4201  C      TEST   AX,CX
1732          4202  C      CLC
1732          4203  C      JZ    S25
1735          4204  C      S25:   RCL    DL,1
1735          4205  C      SHR    CX,1
1735          4206  C      SHR    CX,1
1738          4207  C      S25:   RCL    DL,1
1738          4208  C      SHR    CX,1
1738          4209  C      SHR    CX,1
1738          4210  C      JNC    S24
173E          4211  C      JNC    S24
1740          4212  C
1740          4213  C      MOV    [BP],DL
1740          4214  C      INC    BP
1740          4215  C      RET
1745          4216  C      S23:   ENDP
1745          4217  C
1745          4218  C
1745          4219  C
1745          4220  C      GRAPHICS_READ PROC   NEAR
1745          4221  C      CALL   MK_ES
1745          4222  C      CALL   S26
1745          4223  C      MOV    S1,AX
1745          4224  C      SUB   SP,8
1750          4225  C      MOV    BP,SP
1750          4226  C
1750          4227  C      ;----- DETERMINE GRAPHICS MODES
1752          4228  C
1757          4229  C      CMP    CRT_MODE,6
1757          4230  C      PUSH   ES
1758          4231  C      POP    DS
1759          4232  C      JC    S13P
1759          4233  C      ; POINT TO REGEN SEGMENT
1759          4234  C      ; SAVE IN SI
1759          4235  C      ; ALLOCATE SPACE TO SAVE
1759          4236  C      ; THE REGEN AREA
1759          4237  C      ; POINTER TO SAVE AREA
1758          4238  C
1758          4239  C
1758          4240  C      S12P:  MOV    AL,[S1]
1758          4241  C      MOV    [BP],AL
1762          4242  C      INC    BP
1763          4243  C      MOV    AL,[S1+200H]
1767          4244  C      MOV    [BP],AL
1767          4245  C      INC    BP
1768          4246  C      ADD    DH,4
1768          4247  C      DEC    DH,80
1770          4248  C      JNZ    S12P
1772          4249  C      JMP    S15P
1772          4250  C
1772          4251  C
1772          4252  C      ;----- HIGH RESOLUTION READ
1772          4253  C
1775          4254  C      ;----- GET VALUES FROM REGEN BUFFER AND CONVERT TO CODE POINT
1775          4255  C      S13P:  MOV    DH,4
1775          4256  C      ; NUMBER OF PASSES
1777          4257  C      S14P:  SAL    S1,1
1777          4258  C      MOV    DH,4
1779          4259  C      ;----- MEDIUM RESOLUTION READ
1779          4260  C      S13P:  SAL    S1,1
1779          4261  C      MOV    DH,4
1779          4262  C      ;----- GET PAIR BYTES
1779          4263  C      ADD    S1,200H
1779          4264  C      CALL   S23
1779          4265  C      SUB    S1,200H-80
1779          4266  C      DEC    DH
1779          4267  C      JNZ    S14P
1779          4268  C      ;----- SAVE AREA HAS CHARACTER IN IT, MATCH IT
1788          4269  C
1788          4270  C      S15P:  PUSH   DS
1788          4271  C      CALL   DDS
1788          4272  C      LES    D1,GRX_SET
1793          4273  C      POP    DS
1793          4274  C      SUB    BP,8
1794          4275  C      MOV    S1,BP
1794          4276  C      CLD
1794          4277  C      MOV    AL,0
179C          4278  C      ;----- FIND_CHAR
179C          4279  C      S16P:  PUSH   SS
179C          4280  C      POP    DS
179C          4281  C      MOV    DX,128
179C          4282  C      ;----- ESTABLISH ADDRESSING
179C          4283  C      PUSH   SI
179C          4284  C      PUSH   DI
179C          4285  C      ;----- ADJUST POINTER TO BEGINNING OF SAVE AREA
179C          4286  C      ;----- ENSURE DIRECTION
179C          4287  C      ;----- CODE POINT BEING
179C          4288  C      ;----- MATCHED
179C          4289  C      ;----- ADDRESSING TO STACK
179C          4290  C      ;----- FOR THE STRING COMPARE
179C          4291  C      ;----- NUMBER TO TEST AGAINST
179C          4292  C      ;----- SAVE SAVE AREA POINTER
179C          4293  C      ;----- SAVE CODE POINTER

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17A3 B9 0008          4285    C      MOV    CX,8          ; NUMBER OF BYTES TO MATCH
17A6 F3 / A6          4286    C      REPE   CHPSB        ; COMPARE THE 8 BYTES
17A8 5F               4287    C      POP    DI          ; RECOVER THE POINTERS
17A9 5E               4288    C      POP    SI          ;
17AA 74 1D             4289    C      JZ    S18P         ; IF ZERO FLAG SET,
17AC FE C0             4291    C      INC    AL          ; THEN MATCH OCCURRED
17AE 83 C7 08          4292    C      ADD    DI,8         ; NO MATCH, MOVE TO NEXT
17B1 4A               4293    C      DEC    DX          ; NEXT CODE POINT
17B2 75 ED             4294    C      JNZ    S17P         ; LOOP CONTROL
17B3 4C               4295    C      JZ    S18P         ; DO ALL OF THEM
17B4 3C 00             4296    C      ;----- CHAR NOT MATCHED, MIGHT BE IN USER SUPPLIED SECOND HALF
17B5 4C               4297    C      CMP    AL,0          ; AL < 0 IF ONLY 1ST
17B6 74 11             4298    C      CMP    AL,0          ; HALF SCANNED
17B7 40               4299    C      JE    S18P         ; IF AL=0, THEN ALL HAS
17B8 40               4300    C      ;----- BEEN SCANNED
17B9 40               4301    C      JE    S18P         ;
17B8 EB 00CF E R       4302    C      ASSUME DS:AB50      ;----- CHARACTER IS FOUND ( AL=0 IF NOT FOUND )
17B9 C9 3E 007C R       4303    C      CALL   DS:EXT_PTR    ;----- READ CHARACTER/ATTRIBUTE AT CURRENT CURSOR POSITION
17BF 8C C0             4304    C      LES    DS,D1           ;----- GET POINTER
17C1 0B C7             4305    C      MOV    AX,ES          ;----- SEE IF THE PTRN EXISTS
17C3 74 04             4306    C      OR    AX,DI          ;----- IF ALL 0, DOESN'T EXIST
17C5 74 80             4307    C      JZ    S18P         ;----- NO SENSE LOOKING
17C6 74 80             4308    C      MOV    AL,128         ;----- ORIGIN FOR SECOND HALF
17C7 EB D3             4309    C      JMP    S16P         ;----- GO BACK AND TRY FOR IT
17C8 4310
17C9 83 C4 08          4311    C      ;----- CHARACTER IS FOUND ( AL=0 IF NOT FOUND )
17C9 E9 219E R          4312    C      S18P:          ;----- READ CHARACTER/ATTRIBUTE AT CURRENT CURSOR POSITION
17C9 4313
17C9 4314    ADD    SP,8          ;----- READJUST THE STACK,
17C9 4315    JMP    V_RET         ;----- THROW AWAY SAVE
17CC 4316    GRAPHICS_READ    ENDP
17CF 4317
17CF 4318
17CF 4319
17CF 4320    C      ;----- READ CHARACTER/ATTRIBUTE AT CURRENT CURSOR POSITION
17CF 4321
17CF 4322
17CF 4323    AH8:          JMP    READ_AC_CURRENT
17D2 4324
17D2 4325    AH8:          ASSUME DS:AB50      ;----- GET THE CURRENT MODE
17D2 4326    MOV    AH,CRT_MODE
17D6 4327    CMP    AH,07H
17D9 4328    JE    AH8S
17D9 4329    CMP    AH,03H
17D9 4330    JE    AH8S
17D9 4331    CMP    AH,0FH
17D9 4332    CMP    AH,06H
17E3 4333    JA    Z_1
17E5 4334    JMP    GRAPHICS_READ
17E5 4335    CMP    AH,0FH
17E8 4336    JB    GRX_RD2
17E8 72 52             4337    CALL   MEM_DET
17ED 4338    EB 00CF E R       4339    JC    GRX_RD2
17F0 72 4D             4340    CMP    SH,DS:[GRX_RDI]
17F0 72 4A             4341    CMP    AH,ODH
17F4 80 FC 0D          4342    JAE   GRX_RD2
17F7 73 46             4343    MOV    AL,0
17F9 80 00             4344    JMP    V_RET
17FB 4345    ;----- RANGE TEST
17FE 4346    PROC   NEAR DS:AB50      ;----- FOUR MAP READ
17FE 4347    ASSUME DS:AB50      ;----- REGEN SEGMENT
17FE 4348    SRLOAD ES,QA00H
1801 4349    C+    MOV    DX,QA00H
1801 4350    C+    PUSH  ES,QA00H
1803 4351    C+    CALL   GR_CUR
1806 4352    C+    MOV    SI,AX
1808 4353    C+    MOV    BX,POINTS
180C 2B E3             4354    SUB   SP,BX
180E 4355    C+    MOV    BP,SP
180E 4356    C+    MOV    BP,SP
1810 4357    C+    ;----- GET VALUES FROM REGEN BUFFER AND CONVERT TO CODE POINT
1810 4358    C+    ;----- GET FIRST BYTE
1811 53               4360    PUSH  BX          ;----- SAVE BYTES PER CHARACTER
1811 24 01             4361    AND   AL,1          ;----- ODD OR EVEN BYTE
1811 80 00             4362    MOV    CL,AL          ;----- USE FOR SHIFT
1815 80 05             4363    SHL   AL,2          ;----- COLOR COMPARE VALUE (C0-C2)
1817 02 E0             4364    SHL   AL,CL          ;----- (C1-C3), IF ODD BYTE
1819 80 07             4365    MOV    AH,G_COLOR
1819 80 03             4366    MOV    DH,3
181D 4367    C+    PUSH  DX          ;----- COLOR COMPARE REGISTER
181F E8 0015 R          4368    CALL   DL_GRAPH_ADDR
1822 80 0518 R          4369    MOV    AX,518H
1825 80 0518 R          4370    CALL   OUT_DX
1828 26: 8A 04          4371    C+    CALL   OUT_DX
1828 F6 D0             4372    MOV    AL,ES:[SI]      ;----- GET FIRST BYTE
182D 88 46 00          4373    NOT   AL
182D 4374    MOV    SS:[BP],AL
1831 4375    C+    INRD  BP
1831 03 36 044A R       4376    DEC   BX
1835 4377    C+    S12,_1
1836 75 F0             4378    JNZ    S12,_1
1838 5B               4379    POP   BX
1838 4380    C+    MOV    AX,510H
183C EB 0510             4381    UNPCKL GRA_REGC
183C EB 32 90             4382    JMP    GRA_REGC
183F 4383    C+    ;----- CHAR RECONITION ROUTINE
183F 4384    C+    GRX_RD1 ENDP
183F 4385    C+    ;----- GET VALUES FROM REGEN BUFFER AND CONVERT TO CODE POINT
183F 4386    C+    ASSUME DS:AB50      ;----- REGEN SEGMENT
183F 4387    C+    SRLOAD ES,0A00H
1842 4388    C+    MOV    ES,0A00H
1842 4389    C+    CALL   GR_CUR
1847 8B F0             4390    MOV    SI,AX
1849 8B 1E 0485 R       4391    MOV    BX,POINTS
184D 2B E3             4392    SUB   SP,BX
184F 8B EC             4393    MOV    BP,SP
1850 53               4394    ;----- GET VALUES FROM REGEN BUFFER AND CONVERT TO CODE POINT
1850 4395    C+    ;----- GET COLOR COMPARED BYTE
1850 4396    C+    ;----- ADJUST
1851 B6 03             4397    C+    ;----- SAVE IN STORAGE AREA
1853 B2 CE             4398    C+    ;----- NEXT LOCATION
1855 BB 0508             4399    C+    ;----- POINTER INTO REGEN
1858 E8 0015 R          4400    C+    ;----- LOOP CONTROL
1858 4401    C+    CALL   OUT_DX
185C 53               4402    PUSH  BX
185C 4403    C+    S12:_1
185C 26: 8A 04          4404    C+    MOV    AL,ES:[SI]
185F F6 D0             4405    C+    NOT   AL
1861 88 46 00          4406    C+    MOV    SS:[BP],AL
1861 4407    C+    INRD  BP
1865 03 36 044A R       4408    C+    ADD   S1,CRT_COLS
1869 4409    C+    DEC   BX
186A 75 F0             4410    C      JNZ    S12

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186C 5B ; RECOVER BYTES PER CHAR
186D B8 0500 ; UNDO READ MODE
1870
1870 ;----- SAVE AREA HAS CHARACTER IN IT, MATCH IT
1870 E8 0015 R 4011 C POP BX
1870 C4 3E 010C R 4012 C MOV AX,500H
1870 2B EB 4013 C GRX_RD2 ENDP
1870
1870 4014 C
1870 4015 C
1870 4016 C
1870 4017 C ;----- SAVE AREA HAS CHARACTER IN IT, MATCH IT
1870 4018 C
1870 4019 C CALL OUT_DK
1870 4020 C LES DI,GRX_SET
1870 4021 C SUB BP,BX
1870
1870 4022 C
1870 4023 C MOV SI,BP
1870 4024 C CLD
1870 4025 C MOV AL,0
1870 4026 C PUSH SS
1870 4027 C POP DS
1870 4028 C MOV DX,256D
1870 S17_5: ;----- SET READ MODE BACK
1870 4029 C ; GET FONT DEFINITIONS
1870 4030 C ; ADJUST POINTER TO
1870 4031 C ; BEGINNING OF SAVE AREA
1870 4032 C
1870 4033 C ;----- ENSURE DIRECTION
1870 4034 C ; CODE POINT BEING MATCHED
1870 4035 C ; ADDRESSING TO STACK
1870 4036 C ; FOR THE STRING COMPARE
1870 4037 C ; NUMBER TO TEST AGAINST
1870 4038 C
1870 4039 C ;----- SAVE AREA POINTER
1870 4040 C ; SAVE CODE POINTER
1870 4041 C ; NUMBER OF BYTES TO MATCH
1870 4042 C ; COMPARE THE 8 BYTES
1870 4043 C ; RECOVER THE POINTERS
1870 4044 C
1870 4045 C ;----- IF ZFL SET, THEN MATCH
1870 4046 C ; OCCURRED
1870 4047 C ; NO MATCH ON TO NEXT
1870 4048 C ; NEXT CODE POINT
1870 4049 C ; LOOP CONTROL
1870 4050 C ; DO ALL OF THEM
1870 4051 C ; AL-CHAR, 0 IF NOT FOUND
1870 4052 C ; READJUST THE STACK
1870 4053 C
1870 4054 C ;----- WRITE CHARACTER/ATTRIBUTE AT CURRENT CURSOR POSITION
1870 4055 C
1870 4056 C
1870 4057 C
1870 4058 C
1870 4059 C
1870 4060 C
1870 4061 C
1870 4062 C
1870 4063 C
1870 4064 C
1870 4065 C
1870 4066 C
1870 4067 C
1870 4068 C
1870 4069 C
1870 4070 C
1870 4071 C
1870 4072 C
1870 4073 C
1870 4074 C
1870 4075 C
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1870 4091 C
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1870 4137 C
1870 4138 C
1870 4139 C
1870 4140 C
1870 4141 C
1870 4142 C
1870 4143 C
1870 4144 C
1870 4145 C
1870 4146 C
1870 4147 C
1870 4148 C
1870 4149 C ;----- WRITE_AC_CURRENT
1870 4150 C THIS ROUTINE WRITES THE ATTRIBUTE
1870 4151 C AND CHARACTER AT THE CURRENT CURSOR
1870 4152 C POSITION
1870 4153 C INPUT: ;----- INPUT
1870 4154 C (AH) = CURRENT CRT MODE
1870 4155 C (BH) = DISPLAY PAGE
1870 4156 C (CX) = COUNT OF CHARACTERS TO WRITE
1870 4157 C (AL) = CHAR TO WRITE
1870 4158 C (BL) = ATTRIBUTE OF CHAR TO WRITE
1870 4159 C (DS) = DATA SEGMENT
1870 4160 C (ES) = REGEN SEGMENT
1870 4161 C OUTPUT: ;----- OUTPUT
1870 4162 C NONE
1870 4163 C
1870 A9: ;----- AH9:
1870 4164 C ASSUME DS:ABSO
1870 4165 C CALL DDS
1870 4166 C MOV AH,CRT_MODE
1870 4167 C
1870 4168 C CMP AH,4 ; IS THIS GRAPHICS
1870 4169 C JC P6 ; IS THIS BW CARD
1870 4170 C CMP AH,7
1870 4171 C JE P6
1870 4172 C JMP GRAPHICS_WRITE
1870 P6: ;----- P6:
1870 4173 C CALL MK_ES
1870 4174 C MOV AH,BL ; GET ATTRIBUTE TO AH
1870 4175 C PUSH AX ; SAVE ON STACK
1870 4176 C PUSH AX ; SAVE WRITE COUNT
1870 4177 C CALL FIND_POSITION ;----- CALL DX,BX
1870 4178 C MOV AX,BX ;----- ADDRESS TO DI REGISTER
1870 4179 C ADD DX,BX ;----- WRITE COUNT
1870 4180 C CALL MK_ES ;----- CHARACTER IN BX REG
1870 4181 C POP CX ;----- GET BASE ADDRESS
1870 4182 C POP BX ;----- POINT AT STATUS PORT
1870 4183 C MOV DX,ADDR_6845
1870 4184 C ADD DX,6
1870 4185 C
1870 4186 C ;----- WAIT FOR HORIZONTAL RETRACE
1870 4187 C
1870 4188 C
1870 4189 C P7: TEST INFO,4
1870 4190 C JZ P9A
1870 4191 C
1870 4192 C P8: IN AL,DX ;----- GET STATUS
1870 4193 C TEST AL,1 ;----- IS IT LOW
1870 4194 C JNZ P8 ;----- WAIT UNTIL IT IS
1870 4195 C CLI ;----- NO MORE INTERRUPTS
1870 4196 C
1870 4197 C P9: IN AL,DX ;----- GET STATUS
1870 4198 C TEST AL,1 ;----- IS IT HIGH
1870 4199 C JZ P9 ;----- WAIT UNTIL IT IS
1870 4200 C
1870 4201 C P9A: MOV AX,BX ;----- RECOVER THE CHAR/ATTR
1870 4202 C STOSW ;----- PUT THE CHAR/ATTR
1870 4203 C STI ;----- INTERRUPTS BACK ON
1870 4204 C LOOP P7 ;----- AS MANY TIMES
1870 4205 C JMP V_RET
1870
1870 ;----- WRITE CHARACTER ONLY AT CURRENT CURSOR POSITION
1870 4206 C
1870 4207 C
1870 4208 C
1870 4209 C
1870 4210 C
1870 4211 C
1870 4212 C ;----- WRITE_C_CURRENT
1870 4213 C THIS ROUTINE WRITES THE CHARACTER AT
1870 4214 C THE CURRENT CURSOR POSITION, ATTRIBUTE
1870 4215 C UNCHANGED
1870 4216 C INPUT: ;----- INPUT
1870 4217 C (AH) = CURRENT CRT MODE
1870 4218 C (BH) = DISPLAY PAGE
1870 4219 C (CX) = COUNT OF CHARACTERS TO WRITE
1870 4220 C (AL) = CHAR TO WRITE
1870 4221 C (DS) = DATA SEGMENT
1870 4222 C (ES) = REGEN SEGMENT
1870 4223 C OUTPUT: ;----- OUTPUT
1870 4224 C NONE
1870 4225 C
1870 4226 C AHA: ASSUME DS:ABSO
1870 4227 C CALL DDS
1870 4228 C MOV AH,CRT_MODE
1870 4229 C
1870 4230 C CMP AH,4 ; IS THIS GRAPHICS
1870 4231 C JC P10 ; IS THIS BW CARD
1870 4232 C CMP AH,7
1870 4233 C JE P10
1870 4234 C JMP GRAPHICS_WRITE
1870 P10: ;----- P10:
1870 4235 C CALL MK_ES

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1971 5F          4663   C      POP    DI          ; RECOVER REGEN POINTER
1971 47          4664   C      DI          POINT TO NEXT CHAR POS
1971 E2 E3       4665   C      LOOP   S3          ; MORE CHARS TO WRITE
1975 E9 219E R   4666   C      JMP    V_RET
1978 4667   C      XOR    AL,ES:[DI]
1978 26: 32 05   4668   C      LODSB
1978 AA          4669   C      LODSB
197C AC          4670   C      XOR    AL,ES:[DI+2000H-1]
1970 26: 32 85 1FFF 4671   C      JMP    S5          ; XOR WITH CURRENT
1982 EB EO          4672   C      XOR    AL,ES:[DI+2000H-1]
1982 EB EO          4673   C      JMP    V_RET          ; STORE THE CODE POINT
1982 EB EO          4674   C      ;----- MEDIUM RESOLUTION WRITE
1982 EB EO          4675   C      S6: XOR    AL,ES:[DI]
1984 8A D3       4676   C      LODSB
1986 D1 E7       4677   C      MOV    DL,BL
1986 E8 166D R   4678   C      SAL    D1,1
1986 E8 166D R   4679   C      CALL   S19
1988 4680   C      S8: PUSH   DI
1988 57          4681   C      PUSH   SI
198C 56          4682   C      MOV    DH,4
198F B6 04       4683   C      S9: LODSB
198F 4684   C      CALL   S21
1984 AC          4685   C      AND   AX,BX
1990 E8 1682 R   4686   C      TEST  DL,80H
1993 C3          4687   C      JZ    S10
1995 F6 C2 80     4688   C      XOR    AH,ES:[DI]
1998 74 07       4689   C      XOR    AL,ES:[DI+1]
1998 26: 32 45 01 4690   C      S10: MOV    ES:[DI],AH
1941 4691   C      XOR    ES:[DI+1],AL
1941 26: 88 25     4692   C      CALL   S21
1941 26: 88 45 01 4693   C      AND   AX,BX
1941 4694   C      TEST  DL,80H
1941 23 C3       4695   C      JZ    S11
194E F6 C2 80     4696   C      XOR    ST1
1981 78 0A       4697   C      XOR    AH,ES:[DI+2000H]
1988 26: 32 85 2000 4698   C      XOR    ES:[DI+2000H]
1988 26: 32 85 2001 4699   C      S11: MOV    ES:[DI+2000H],AH
1980 469A   C      ADD   DI,80
1980 26: 88 A5 2000 4700   C      DEC   DI
1980 26: 88 A5 2001 4701   C      JNZ   S9
1980 4702   C      XOR    AH,ES:[DI+2000H]
1980 4703   C      XOR    ES:[DI+2000H]
1980 4704   C      S12: MOV    ES:[DI+2000H],AH
1980 4705   C      ADD   DI,80
1980 4706   C      DEC   DI
1980 4707   C      JNZ   S9
1980 4708   C      XOR    ES:[DI+2000H],AL
1980 4709   C      ADD   DI,80
1980 4710   C      DEC   DI
1980 4711   C      INC   DI
1980 4712   C      INC   DI
1980 4713   C      LOOP  S8
1980 4714   C      JMP    V_RET
1980 4715   C      ;----- GRAPHICS_WRITE
1980 4716   C      ;----- ENTRY
1980 4717   C      AL = CHAR TO WRITE
1980 4718   C      BH = DISPLAY PAGE
1980 4719   C      BL = ATTRIBUTE/COLOR
1980 4720   C      CX = COUNT OF CHARS TO WRITE
1980 4721   C      ;----- GRX_WRT PROC NEAR
1980 4722   C      ASSUME DS:ABSO, ES:NOTHING
1980 4723   C      CMP    AH,0FH
1980 4724   C      JB    A1,ADJ1
1980 4725   C      JC    NO_XOR
1980 4726   C      CALL   NO_XOR
1980 4727   C      XOR    AH,0
1980 4728   C      CALL   NO_XOR
1980 4729   C      XOR    AH,0
1981 E8 E3 85     4730   C      AND   BL,10000010B
1984 8A E3       4731   C      MOV    AH,BL
1984 00 E4       4732   C      SHL   AH,1
1984 00 E4       4733   C      OR    BL,AH
1984 4734   C      NO_XOR: SUB   AH,AH
1984 4735   C      MOVS  MOU_POINTS
1984 4736   C      PUSH  AX
1984 4737   C      CALL   GR_CUR
1984 4738   C      CALL   GR_CUR
1984 4739   C      MOV    DI,AX
1984 4740   C      MOV    BP,PTS
1984 4741   C      XOR    BP,PTS
1984 4742   C      MOVS  MOU_POINTS
1984 4743   C      CALL   GR_XR_SET
1984 4744   C      LDS   SI,GRX_SET
1984 4745   C      MOVS  ES,DX
1984 4746   C      ADD   SI,DX
1984 4747   C      MOVS  SI,AX
1984 4748   C      MOVS  DI,DH
1984 4749   C      S20A: TEST  BL,80H
1984 4750   C      JZ    NO_XOR
1984 4751   C      MOV    DL,GRAPH_ADDR
1984 4752   C      MOV    AX,0318H
1984 4753   C      CALL   OUT_DX
1984 4754   C      JMP    F_2
1984 4755   C      NO_XOR: PUSH  DI
1984 4756   C      MOVS  DL,SEQ_ADDR
1984 4757   C      MOV    AX,020FH
1984 4758   C      CALL   AX,020FH
1984 4759   C      XOR    AX,AX
1984 4760   C      SUB   AX,AX
1984 4761   C      PUSH  CX
1984 4762   C      MOVS  CX,BP
1984 4763   C      PUSH  DS
1984 4764   C      CALL   DDS
1984 4765   C      S13A: STOSB
1984 4766   C      ADD   DI,CRT_COLS
1984 4767   C      DEC   DI
1984 4768   C      DEC   DI
1984 4769   C      LOOP  S13A
1984 4770   C      POP   DS
1984 4771   C      POP   CX
1984 4772   C      POP   DI
1984 4773   C      S13A: RECOVER CHARACTER COUNT
1984 4774   C      MOVS  DL,SEQ_ADDR
1984 4775   C      MOV    AH,02H
1984 4776   C      XOR    AL,BL
1984 4777   C      CALL   OUT_DX
1984 4778   C      PUSH  DI
1984 4779   C      PUSH  BX
1984 4780   C      PUSH  CX
1984 4781   C      MOVS  AX,BP
1984 4782   C      PUSH  DS
1984 4783   C      CALL   DDS
1984 4784   C      ASSUME DS:ABSO
1984 4785   C      MOV    DS,CRT_COLS
1984 4786   C      POP   DS
1984 4787   C      ASSUME DS:NOTHING
1984 4788   C      S1K: ;----- F_2:
1984 4789   C      ;----- ZERO REGEN BYTE
1984 4790   C      ;----- REGEN BYTE OF BOX
1984 4791   C      ;----- ADJUST
1984 4792   C      ;----- NEXT BYTE
1984 4793   C      ;----- SET MAP MASK
1984 4794   C      ;----- FOR COLOR
1984 4795   C      ;----- SET THE CHIP
1984 4796   C      ;----- SAVE OFFSET IN REGEN
1984 4797   C      ;----- SAVE COLOR VALUE
1984 4798   C      ;----- SET COLOR CONTRAST
1984 4799   C      ;----- LOOP CONTROL BYTES/CHAR
1984 4800   C      ;----- SAVE FONT SEGMENT
1984 4801   C      ;----- SET LOW RAM SEGMENT
1984 4802   C      ;----- GET COLUMN COUNT
1984 4803   C      ;----- RESTORE FONT SEGMENT
1984 4804   C      ;----- WRITE OUT THE CHARACTER

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1A9C 8A 04        4789 C      MOV    AL, DS:[SI]
1A9E 26: 8A 25    4790 C      MOV    AH, ES:[DI]
1A91 20: 88 05    4791 C      MOV    ES:[DI], AL
1A54 46          4792 C      INC    SI
1A55 03 F9        4793 C      ADD    DI, CX
1A57 4B          4794 C      DEC    BX
1A58 75 F2        4795 C      JNZ    SI<K
1A59 76          4796 C      POP    CX
1A5B 5B          4797 C      POP    BX
1A5C 20 F5        4798 C      SUB    SI, BP
1A5E EF          4799 C      PUSH   DI
1A5F 47          4800 C      INC    DI
1A60 E2 A6        4802 C      LOOP   S20A
1A62 B2 CE        4804 C      MOV    DL, GRAPH_ADDR
1A64 B8 0300      4805 C      MOV    AX, 1000H
1A67 E8 0D15 R    4806 C      CALL   OUT_DX
1A6A B2 C4        4807 C      MOVL   DL, SEQ_ADDR
1A6C B0 020F      4808 C      MOV    AX, 020FH
1A6D EB 0D15 R    4809 C      CALL   OUT_DX
1A6E 29 19E R    4810 C      JMP    V_RET
1A75              4811 C      GRX_WRT ENDP
1A75              4812 C      SUBTLL
1A75              4813 C      ;----- SET COLOR PALETTE
1A75              4814
1A75              4815
1A75              4816
1A75              4817 AHB:
1A75              4818 ASSUME DS:AB50
1A75              4819 CHR BTYTE PTR ADDR_6845,0B4H
1A75              4820 JE M21_B
1A75              4821 TEST INFO,2
1A75              4822 JZ M21_A
1A75              4823 INT 42H
1A75              4824 M21_B: JMP V_RET
1A75              4825 M21_A: SUB AX, AX
1A75              4826 MOV BP, AX
1A75              4827 LES DI, SAVE_PTR
1A75              4828 ADD DI, 4
1A75              4829 ADD DI, 4
1A75              4830 LES DI, DWORD PTR ES:[DI]
1A75              4831 MOV AX, 0000H
1A75              4832 OR AX, DI
1A75              4833 INC DI
1A75              4834 JZ NOT4AHB
1A75              4835 INC BP
1A75              4836 NOT4AHB: CALL PAL_INIT
1A75              4837 OR BH, BH
1A75              4838 JNZ M20
1A75              4839
1A75              4840
1A75              4841 ;----- HANDLE BH = 0 HERE
1A75              4842 ALPHA MODES => BL = OVERSCAN COLOR
1A75              4843 GRAPHICS => BL = OVERSCAN AND BACKGROUND COLOR
1A75              4844
1A75              4845 ;----- MOVE INTENSITY BIT FROM D3 TO D4 FOR COMPATIBILITY
1A75              4846
1A75              4847 MOV BH, BL
1A75              4848 MOV AL, CRT_PALETTE
1A75              4849 AND AL, 0E0H
1A75              4850 SHL BH, 4
1A75              4851 OR AL, BH
1A75              4852 MOV CRT_PALETTE, AL
1A75              4853 MOV BL, BH
1A75              4854 AND BH, 0BH
1A75              4855 SHL BH, 4
1A75              4856 MOV CH, AL
1A75              4857 AND CH, 0E0H
1A75              4858 OR CH, CH
1A75              4859 SHL BL, 4
1A75              4860 MOV BH, BL
1A75              4861 SHL BL, 1
1A75              4862 AND BL, 010H
1A75              4863 AND BH, 07H
1A75              4864 OR BL, BH
1A75              4865
1A75              4866 MOV AL, CRT_MODE
1A75              4867 CMP AL, 3
1A75              4868 JBE M21
1A75              4869
1A75              4870 ;----- GRAPHICS MODE DONE HERE (SET PALETTE 0 AND OVERSCAN)
1A75              4871
1A75              4872 MOV AH, 0
1A75              4873 MOV AL, BL
1A75              4874 CALL PAL_SET
1A75              4875
1A75              4876 OR BP, BP
1A75              4877 JZ M21
1A75              4878 MOV ES:[DI], BL
1A75              4879
1A75              4880 ;----- ALPHA MODE DONE HERE (SET OVERSCAN REGISTER)
1A75              4881
1A75              4882 M21: CMP CRT_MODE, 3
1A75              4883 JA SET_OVRSC
1A75              4884 CALL BRST_DET
1A75              4885 JC SKIP_OVRSC
1A75              4886 SET_OVRSC: MOV AH, 011H
1A75              4887 MOV AL, BL
1A75              4888 CALL PAL_SET
1A75              4889 ;----- OVERSCAN REGISTER
1A75              4890 SKIP_OVRSC: OR BP, BP
1A75              4891 JZ M21Y
1A75              4892 MOV ES:[DI][160], BL
1A75              4893
1A75              4894 M21Y: MOV BL, CH
1A75              4895 AND BL, 020H
1A75              4896 MOV CL, 5
1A75              4897 SHR BL, CL
1A75              4898
1A75              4899 ;----- HANDLE BH = 1 HERE
1A75              4900 ALPHA MODES => NO EFFECT
1A75              4901 GRAPHICS => PALETTE 0 = BACKGROUND
1A75              4902 ;----- PALETTE 0 = BACKGROUND
1A75              4903 ;----- PALETTE 1 = GREEN
1A75              4904 ;----- PALETTE 2 = RED
1A75              4905 ;----- PALETTE 3 = BROWN
1A75              4906 ;----- PALETTE 0 = BACKGROUND
1A75              4907 ;----- PALETTE 1 = CYAN
1A75              4908 ;----- PALETTE 2 = MAGENTA
1A75              4909 ;----- PALETTE 3 = WHITE
1A75              4910
1A75              4911
1A75              4912
1A75              4913
1A75              4914

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1B09 80 3E 0449 R 03   4915  M20:    CMP    CRT_MODE,3
1B0E 76 4A              4916  JBE    M60_
1B10 A0 0466 R          4917
1B13 24 DF              4918
1B13 80 05 01          4919  MOV    AL,CRT_PALETTE
1B18 74 02              4920  AND    AL,0DFH
1B18 74 02              4921  AND    AL,2
1B1A 0C 20              4922  JZ    M22
1B1A 0C 20              4923  OR    AL,020H
1B1C A2 0466 R          4924
1B1D 80 10              4925  MOV    CRT_PALETTE,AL
1B21 0C 02              4926  AND    AL,010H
1B21 0C 02              4927  OR    AL,2
1B23 0C 08              4928  OR    BL,AL
1B25 B4 01              4929  MOV    AH,1
1B27 8A C3              4930  MOV    AL,BL
1B29 E8 1D9F R          4931  CALL   PAL_SET
1B2C 0B ED              4932
1B2E 74 04              4933  OR    BP,BP
1B30 26: 88 5D 01      4934  JZ    M22Y
1B34 4935  MOV    ES:[DI][1],BL
1B34 FE C3              4936
1B34 FE C3              4937
1B38 B4 02              4938  INC    BL
1B3A 8A C3              4939  INC    BL
1B3C E8 1D9F R          4940  MOV    AH,2
1B42 0C 03              4941  MOV    AL,BL
1B42 0C 03              4942  CALL   PAL_SET
1B43 74 04              4943
1B47 26: 88 5D 02      4944  OR    BP,BP
1B47 0B ED              4945  JZ    M27Y
1B47 74 04              4946  MOV    ES:[DI][2],BL
1B47 4947
1B47 FE C3              4948
1B49 FE C3              4949  INC    BL
1B4B B4 03              4950  INC    BL
1B4D 8A C3              4951  MOV    AH,3
1B4F E8 1D9F R          4952  MOV    AL,BL
1B52 0B ED              4953  CALL   PAL_SET
1B54 74 04              4954
1B56 26: 88 5D 03      4955  OR    BP,BP
1B56 4956
1B5A 1B5A E8 1DB7 R      4957  JZ    M60
1B5D E9 219E R          4958  MOV    ES:[DI][3],BL
1B60 4959
1B60 4960  CALL   PAL_ON
1B60 4961  JMP    V_RET
1B60 4962
1B60 C  INCLUDE    VDOT.INC
1B60 C  SUBTBL VDOT.INC
1B60 C  PAGE
1B60 C  -----
1B60 C  ENTRY
1B60 C  DX = ROW
1B60 C  CX = COLUMN
1B60 C  BH = PAGE
1B60 C  EXIT
1B60 C  BX = OFFSET INTO REGEN
1B60 C  AL = BIT MASK FOR COLUMN BYTE
1B60 C  -----
1B60 C  DOT_SUP_1    PROC  NEAR
1B60 C  -----
1B60 C  ----- OFFSET = PAGE_OFFSET + ROW * BYTES/ROW + COLUMN/B8
1B60 C  -----
1B60 C  4978
1B60 C  4979
1B60 C  4980  MUL    WORD PTR CRT_COLS ; ROW * BYTES/ROW
1B60 C  4981  PUSH   CX ; SAVE COLUMN VALUE
1B60 C  4982  SHR    CX,1 ; DIVIDE BY EIGHT TO
1B60 C  4983  SHR    CX,1 ; DETERMINE THE BYTE THAT
1B60 C  4984  SHR    CX,1 ; THIS DOT IS IN
1B60 C  4985  ADD    AX,CX ; (0-7/8/16)
1B60 C  4986  MOV    BL,BH ; BYTE OFFSET INTO PAGE
1B60 C  4987  SUB    BH,BH ; GET PAGE INTO BL
1B60 C  4988  MOV    CX,BX ; ZERO
1B60 C  4989  MOV    BX,CRT_LEN ; LENGTH OF ONE PAGE
1B60 C  4990  JCXZ  DS_2 ; PAGE ZERO
1B60 C  DS_3:  ADD    AX,BX ; BUMP TO NEXT PAGE
1B60 C  4991  LOOP   DS_3 ; DO FOR THE REST
1B60 C  DS_2:  POP    CX ; RECOVER COLUMN VALUE
1B60 C  4992  MOV    BX,AX ; REGEN OFFSET
1B60 C  4993  AND    CL,07H ; COUNT FOR BIT MASK
1B60 C  4994  MOV    AL,080H ; MASK BIT
1B60 C  4995  SHR    AL,CL ; POSITION MASK BIT
1B60 C  4996  RET
1B60 C  DOT_SUP_1    ENDP
1B60 C  -----
1B60 C  5003
1B60 C  5004  ; THIS SUBROUTINE DETERMINES THE REGEN BYTE LOCATION
1B60 C  5005  ; HE INDICATED ROW/COLUMN VALUE IN GRAPHICS MODE.
1B60 C  5006  ; ENTRY
1B60 C  5007  ; DX = ROW VALUE (0-199)
1B60 C  5008  ; CX = COLUMN VALUE (0-639)
1B60 C  5009  ; EXIT --
1B60 C  5010  ; SI = OFFSET INTO REGEN BUFFER FOR BYTE OF INTEREST
1B60 C  5011  ; BH = MASK TO STRIP OFF THE BITS OF INTEREST
1B60 C  5012  ; CL = BITS TO SHIFT TO RIGHT JUSTIFY THE MASK IN AH
1B60 C  5013  ; DL = # BITS IN RESULT
1B60 C  -----
1B60 C  R3  PROC  NEAR
1B60 C  5014  PUSH   BX ; SAVE BX DURING OPERATION
1B60 C  5015  PUSH   AX ; WILL SAVE AL DURING OPERATION
1B60 C  5016
1B60 C  5017
1B60 C  5018
1B60 C  5019  ; ----- DETERMINE 1ST BYTE IN INDICATED ROW BY MULTIPLYING ROW VALUE BY 40
1B60 C  5020  ; (LOW BIT OF ROW DETERMINES EVEN/ODD, 80 BYTES/ROW)
1B60 C  5021
1B60 C  5022  MOV    AL,40 ; SAVE ROW VALUE
1B60 C  5023  PUSH   DX ; STRIP OFF ODD/EVEN BIT
1B60 C  5024  AND    DL,0FEH ; AX HAS ADDRESS OF 1ST BYTE
1B60 C  5025  MUL    DL,DX ; OF INDICATED ROW
1B60 C  5026  C
1B60 C  5027  POP    DX ; RECOVER IT
1B60 C  5028  TEST   DL,1 ; TEST FOR EVEN/ODD
1B60 C  5029  JZ    R4_ ; JUMP IF EVEN ROW
1B60 C  5030  ADD    AX,2000H ; OFFSET TO LOCATION OF ODD ROWS
1B60 C  5031  R4_:  MOV    SI,AX ; MOVE ADDRESS TO SI
1B60 C  5032  POP    AX ; RECOVER ALL VALUES
1B60 C  5033  MOV    DX,CX ; COLUMN VALUE TO DX
1B60 C  5034
1B60 C  5035
1B60 C  5036  ; ----- DETERMINE GRAPHICS MODE CURRENTLY IN EFFECT
1B60 C  5037
1B60 C  5038
1B60 C  5039  ; SET UP THE REGISTERS ACCORDING TO THE MODE
1B60 C  ; CR = MASK FOR LOW OF COLUMN ADDRESS ( 7/3 FOR HIGH/MED RES ) :

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5041 C : CL = # OF ADDRESS BITS IN COLUMN VALUE ( 3/2 FOR H/M )
5042 C : BL = MASK TO SELECT BITS FROM POINTED BYTE (0FFH/COR FOR H/M) :
5043 C : BH = NUMBER OF VALID BITS IN POINTED BYTE ( 1/2 FOR H/M ) :
5044 C -----
5045 C
5046 C     MOV    BX,20H
5047 C     MOV    CX,302H ; SET PARM FOR MED RES
5048 C     CMP    CRT_MODE,6
5049 C     JC    R5 ; HANDLE IF MED ARES
5050 C     MOV    BX,180H
5051 C     MOV    CX,703H ; SET PARM FOR HIGH RES
5052 C
5053 C ----- DETERMINE BIT OFFSET IN BYTE FROM COLUMN MASK
5054 C
5055 R5: AND    CH,DL ; ADDRESS OF PEL WITHIN BYTE TO CH
5056 C
5057 C ----- DETERMINE BYTE OFFSET FOR THIS LOCATION IN COLUMN
5058 C
5059 C     SHR    DX,CL ; SHIFT BY CORRECT AMOUNT
5060 C     ADD    SI,DX ; INCREMENT THE POINTER
5061 C     MOV    DH,BH ; GET THE # OF BITS IN RESULT TO DH
5062 C
5063 C ----- MULTIPLY BH (VALID BITS IN BYTE) BY CH (BIT OFFSET)
5064 C
5065 R6: SUB    CL,CL ; ZERO INTO STORAGE LOCATION
5066 C     ROR    AL,1 ; LEFT JUSTIFY THE VALUE
5067 C     AL    AL,0F0H ; ADD AL AL FOR WRITE
5068 C     ADD    CL,CH ; ADD IN THE BIT OFFSET VALUE
5069 C     DEC    BH ; LOOP CONTROL
5070 C     JNZ    R6 ; ON EXIT, CL HAS SHIFT COUNT
5071 C     ADD    CL,CL ; TO RESTORE BITS
5072 C     MOV    AH,BL ; GET AL TO DH
5073 C     SHR    AH,CL ; MOVE THE MASK TO CORRECT LOCATION
5074 C     POP    BX ; RECOVER REG
5075 C     RET   ; RETURN WITH EVERYTHING SET UP
5076 C
5077 R3: ENDP
5078 C
5079 C
5080 C ----- READ DOT -- WRITE DOT
5081 C THESE ROUTINES WILL WRITE A DOT, OR READ THE DOT AT
5082 C THE INDICATED LOCATION
5083 C
5084 ENTRY --:
5085 C     DX = ROW (0-199) (THE ACTUAL VALUE DEPENDS ON THE MODE)
5086 C     CX = COLUMN (0-639) (THE VALUES ARE NOT RANGE CHECKED)
5087 C     AL = DATA SEGMENT (AL WILL WRITE 12 OF 16 BITS DEPENDING ON MODE,
5088 C     REQ'D FOR WRITE DOT ONLY, RIGHT JUSTIFIED)
5089 C     BIT 7 OF AL=1 INDICATES XOR THE VALUE INTO THE LOCATION
5090 C
5091 DS = DATA SEGMENT
5092 ES = REGEN SEGMENT
5093 C
5094 EXIT
5095 C     AL = DOT VALUE READ, RIGHT JUSTIFIED, READ ONLY
5096 C
5097 C ----- WRITE DOT
5098 C
5099 AHC: ASSUME DS:ABSO
5100 C     CMP    CRT_MODE,7
5101 C     JA    WRITE_DOT_2
5102 C
5103 C ----- WRITE_DOT PROC NEAR
5104 C
5105 ASSUME DS:ABSO,ES:NOTHING
5106 PUSH DS
5107 SRULOAD ES,0B800H
5108 MOV DX,0B800H
5109 C+
5110 POP DX
5111 PUSH AX
5112 PUSH AX ; SAVE DOT VALUE
5113 CALL R3 ; TWICE
5114 SHR AL,CL ; DETERMINE BYTE POSITION OF THE DOT
5115 AND AL,AH ; SET UP THE BITS FOR OUTPUT
5116 MOV CL,ES:[SI] ; STRIP OFF THE OTHER BITS
5117 POP BX ; GET THE CURRENT BYTE
5118 TEST BL,80H ; RECOVER XOR FLAG
5119 JNZ R2 ; IS IT ON
5120 NOT AH ; YES, XOR THE DOT
5121 AND CL,AH ; SET THE MASK TO REMOVE THE
5122 OR AL,CL ; INDICATED BITS
5123 C+
5124 MOV ES:[SI],AL ; OR IN THE NEW VALUE OF THOSE BITS
5125 POP AX ; FINISH DOT
5126 JMP V_RET ; RESTORE THE BYTE IN MEMORY
5127 C+
5128 XOR AL,CL ; XOR DOT
5129 R1: JMP R1 ; EXCLUSIVE OR THE DOTS
5130 C+
5131 WRITE_DOT ENDP ; FINISH UP THE WRITING
5132 C
5133 C----- WRITE_DOT_2 PROC NEAR
5134 C
5135 CMP CRT_MODE,0FH
5136 JB NO_ADJ2 ; BASE CARD
5137 CALL MEM_DET ; NO ADJ2
5138 AND AL,0000010B ; 85H, XOR C2 CO MASK
5139 MOV AH,AL
5140 SHL AH,1 ; EXPAND CO TO C1, C2 TO C3
5141 OR AL,AH ; BUILD ?(80H) + (0,3,C,F)
5142 C+
5143 PUSH AX ; NO ADJ2
5144 MOV AX,DX ; ROW VALUE
5145 CALL DOT_SUP_1 ; BX=OFFSET, AL=BIT MASK
5146 MOV DH,DX ; ROW
5147 C+
5148 MOV DH,GRAPH_ADDR ; GRAPHICS CHIP
5149 MOV AH,G_B1T_MASK ; BIT MASK REGISTER
5150 PUSH DX ; SET BIT MASK
5151 SRULOAD DS,0A000H ; REGEN SEGMENT
5152 C+
5153 MOV DX,0A000H ; RECOVER COLOR
5154 POP DX ; SAVE COLOR
5155 C+
5156 MOV CH,AL ; SEE IF XOR
5157 TEST CH,0B0H ; XOR FUNCTION
5158 JZ WD_A ; SET THE REGISTER
5159 MOV AH,01H ; SKIP THE BLANK
5160 CALL OUT_DX ; BLANK THE DOT
5161 JMP WD_B ; REGENERATOR
5162 C+
5163 MOV DL,SEQADDR ; MAP MASK
5164 MOV AH,S_MAP ; ENABLE ALL MAPS
5165 MOV AL,0FFH ; SET THE REGISTER
5166 C

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1C41 26: 8A 07      5167 C    MOV AL, ES:[BX]          ; LATCH DATA
1C44 2A C0      5168 C    SUB AL, AL          ; ZERO
1C46 26: 88 07      5169 C    MOV ES:[BX], AL      ; BLANK THE DOT
1C49 26: 88        5170 C    ;----- SET THE COLOR MAP MASK
1C49 B2 C4      5171 C    MOV DL, SEQ_ADDR      ; SEQUENCER
1C4B B4 02      5172 C    MOV AH, S_MAP          ; MAP MASK REGISTER
1C4D B4 C0      5173 C    MOV AL, CH          ; COLOR VALUE
1C4F 2E 0F      5174 C    AND AL, DH          ; VALUE 0-15
1C51 E8 0015 R    5175 C    CALL OUT_DX          ; SET IT
1C54 26: 8A 07      5176 C    MOV AL, ES:[BX]      ; LATCH DATA
1C57 B0 FF      5177 C    MOV AL, OFFH          ; WRITE VALUE
1C59 26: 88 07      5178 C    MOV ES:[BX], AL      ; SET THE DOT
1C59 26: 89        5179 C    ;----- NORMALIZE THE ENVIRONMENT
1C5C E8 0015 R    5180 C    ;----- ; ALL MAPS ON
1C5F B2 C6      5182 C    CALL OUT_DX          ; GRAPHICS CHIPS
1C61 B4 03      5183 C    MOV DL, GRAPH_ADDR      ; XOR REGISTER
1C63 2A C0      5184 C    MOV AH, G_DATA_ROT      ; NORMAL WRITES
1C65 E8 0015 R    5185 C    SUB AL, AL          ; SET IT
1C66 B0 FF      5186 C    CALL OUT_DX          ; BIT MASK
1C66 B0 FF      5187 C    MOV AH, OUT_MASK          ; ALL BITS ON
1C66 B0 FF      5188 C    MOV AL, OFFH          ; SET IT
1C66 B0 FF      5189 C    CALL OUT_DX          ; WRITE DOT DONE
1C67 E8 0015 R    5190 C    JMP V_RET
1C6F E9 219E R    5191 C    ;----- WRITE_DOT_2
1C72 50          5192 C    RD_S PROC NEAR
1C73 50          5193 C    ASSUME DS:ABSO
1C74 BA A000      5194 C    PUSH AX
1C77 B6 C2      5195 C    PUSH DX
1C79 9A          5196 C    SRL/LOAD ES, D4000H
1C7A 26: 88        5197 C    MOV DX, D4000H
1C7B B8 C2      5198 C    MOV AH, OUT_MASK
1C7D E8 1B60 R    5199 C    MOV ES, DX
1C80 B5 07      5200 C    POP DX
1C82 26: 88        5201 C    POP AX
1C84 2B D2      5202 C    MOV AX, DX
1C86 B0 00      5203 C    CALL DOT_SUP_1
1C88 C3          5204 C    MOV CH, 7
1C89 26: 88        5205 C    SUB CH, CL
1C89 26: 88        5206 C    MOV DX, DX
1C89 26: 88        5207 C    MOV AL, 0
1C89 26: 88        5208 C    RET
1C89 26: 88        5209 C    RD_S ENDP
1C89 26: 88        5210 C    ;----- RD_IS
1C89 26: 88        5211 C    RD_IS PROC NEAR
1C89 26: 88        5212 C    ASSUME DS:ABSO
1C88 26: 04      5213 C    MOV CL, CH
1C89 26: 04      5214 C    MOV AH, 4
1C8E B6 03      5215 C    PUSH DX
1C90 B2 C6      5216 C    SRL/LOAD ES, D800H
1C92 E8 0015 R    5217 C    MOV DX, D800H
1C92 E8 0015 R    5218 C    CALL OUT_DX
1C96 26: 8A 27      5219 C    POP DX
1C99 D2 EC      5220 C    MOV AH, ES:[BX]
1C9B 80 E4 01      5221 C    SHR AH, CL
1C9E C3          5222 C    AND AH, 1
1C9F 26: 88        5223 C    RET
1C9F 26: 88        5224 C    RD_IS ENDP
1C9F 26: 88        5225 C    ;----- READ DOT
1C9F 26: 88        5226 C    AHD: ; DETERMINE BYTE POSITION OF DOT
1C9F 26: 88        5227 C    GET THE BYTE
1C9F 26: 88 07      5228 C    ASSUME DS:ABSO, CRT_MODE, 7 ; MASK THE OTHER BITS IN THE BYTE
1C9A 77 18          5229 C    CMP CRT_MODE, 7 ; LEFT JUSTIFY THE VALUE
1C9A 77 18          5230 C    JA R_1 ; GET NUMBER OF BITS IN RESULT
1CA6 52          5231 C    ;----- ; RIGHT JUSTIFY THE RESULT
1CA6 52          5232 C    READ_DOT PROC NEAR
1CA6 52          5233 C    ASSUME DS:ABSO, ES:NOTHING
1CA6 52          5234 C    PUSH AX
1CA7 BA B800      5235 C    PUSH DX
1CAA B6 C2      5236 C    SRL/LOAD ES, DB800H
1CAC 5A          5237 C    MOV DX, DB800H
1CAC 5A          5238 C    POP DX
1CAC 5A          5239 C    CALL R3
1CAC 5A          5240 C    MOV AL, ES:[SI]
1CAC 5A          5241 C    AND AL, DH
1CAC 5A          5242 C    SHL AL, CL
1CAC 5A          5243 C    MOV CL, DH
1CAC 5A          5244 C    ROL AL, CL
1CAC 5A          5245 C    JMP V_RET
1CAC 5A          5246 C    READ_DOT ENDP
1CBE 26: 88        5247 C    ;----- R_1:
1CBE 26: 88 07      5248 C    CMP CRT_MODE, OFH ; 4 MAPS
1CC2 72 25          5249 C    JB READ_DOT_2
1CC5 E8 14F7 R    5250 C    CALL MEM_DET
1CC8 72 20          5251 C    JC READ_DOT_2
1CCA 26: 88        5252 C    ;----- ; 2 MAPS
1CCA 26: 88        5253 C    READ_DOT_1 PROC NEAR
1CCA 26: 88        5254 C    ASSUME DS:ABSO, ES:NOTHING
1CCA 26: 88        5255 C    CALL RD_S
1CCA 26: 88        5256 C    CALL RD_IS
1CCA 26: 88        5257 C    OR DL, AH
1CCA 26: 88        5258 C    SHL AH, 1
1CCA 26: 88        5259 C    OR DL, AH
1CCA 26: 88        5260 C    SHL AH, 1
1CCA 26: 88        5261 C    OR DL, AH
1CCA 26: 88        5262 C    MOV AL, 2
1CCA 26: 88        5263 C    CALL RD_IS
1CCA 26: 88        5264 C    SHL AH, 1
1CCA 26: 88        5265 C    OR DL, AH
1CCA 26: 88        5266 C    SHL AH, 1
1CCA 26: 88        5267 C    OR DL, AH
1CCA 26: 88        5268 C    SHL AH, 1
1CCA 26: 88        5269 C    OR DL, AH
1CCA 26: 88        5270 C    JMP V_RET
1CCA 26: 88        5271 C    READ_DOT_1 ENDP
1CEA 26: 88        5272 C    ;----- READ_DOT_2 PROC NEAR
1CEA 26: 88        5273 C    ASSUME DS:ABSO, ES:NOTHING ; 4 MAPS
1CEA 26: 88        5274 C    RD_2A: CALL RD_S
1CEA 26: 88        5275 C    CALL RD_IS
1CEA 26: 88        5276 C    MOV CL, AL
1CEA 26: 88        5277 C    SHL AH, CL
1CEA 26: 88        5278 C    OR DL, AH
1CEA 26: 88        5279 C    INC AL
1CEA 26: 88        5280 C    JBE RD_2A
1CEA 26: 88        5281 C    SHL AH, 3
1CEA 26: 88        5282 C    OR DL, AH
1CEA 26: 88        5283 C    MOV AL, DL
1CEA 26: 88        5284 C    JMP V_RET
1CEA 26: 88        5285 C    READ_DOT_2 ENDP
1CEA 26: 88        5286 C    ;----- ; WRITE_TTY : WRITE TELETYPE TO ACTIVE PAGE
1CEA 26: 88        5287 C    ;----- ; THIS INTERFACE PROVIDES A TELETYPE-LIKE INTERFACE TO THE VIDEO
1CEA 26: 88        5288 C    ;----- ; CARD. THE INPUT CHARACTER IS WRITTEN TO THE CURRENT CURSOR
1CEA 26: 88        5289 C    ;----- ; POSITION, AND THE CURSOR IS MOVED TO THE NEXT POSITION. IF THE CURSOR
1CEA 26: 88        5290 C    ;----- ; LEAVES THE LAST COLUMN OF THE FIELD, THE COLUMN IS SET
1CEA 26: 88        5291 C    ;----- ; CURSOR
1CEA 26: 88        5292 C    ;----- ; POSITION

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5293 C : TO ZERO, AND THE ROW VALUE IS INCREMENTED. IF THE ROW VALUE
5294 C : LIVES, THE ENTIRE SCREEN IS SCROLLED UP ONE LINE. WHEN
5295 C : COLUMN AND THE ENTIRE SCREEN IS SCROLLED UP ONE LINE. WHEN
5296 C : THE SCREEN IS SCROLLED UP, THE ATTRIBUTE FOR FILLING THE NEWLY
5297 C : BLANKED LINE IS READ FROM THE CURSOR POSITION ON THE PREVIOUS
5298 C : LINE BEFORE THE SCROLL, IN CHARACTER MODE. IN GRAPHICS MODE,
5299 C : THE O COLOR IS USED.
5300 C : ENTRY
5301 C : (AH) = CURRENT CRT MODE
5302 C : (AL) = CHARACTER TO BE WRITTEN
5303 C : NOV - THE BELL SPACE, CAR RET, BELL AND LINE FEED ARE HANDLED
5304 C : AS COMMANDS RATHER THAN AS DISPLAYABLE GRAPHICS
5305 C : (BL) = FOREGROUND COLOR FOR CHAR WRITE IF CURRENTLY IN A
5306 C : GRAPHICS MODE
5307 C : EXIT
5308 C : ALL REGISTERS SAVED
5309 C :----- AH:----- AHE:
5311 C : ASSUME CS:CODE, DS:ABSO
5312 C : PUSH AX
5313 C : MOV BH,ACTIVE_PAGE ; SAVE REGISTERS
5314 C : PUSH BX ; GET THE ACTIVE PAGE
5315 C : MOV BL,BH ; SAVE
5316 C : XOR BH,BH ; GET PAGE TO BL
5317 C : SAL BL,1 ; CLEAR HIGH BYTE
5318 C : MOV DX,[BX + OFFSET_CURSOR_POSN] ; WORD_OFFSET
5319 C : POP BX ; CURSOR, ACTIVE PAGE
5320 C : RECOVER ; RECOVER
5321 C :----- DX NOW HAS THE CURRENT CURSOR POSITION
5322 C :----- CMP AL,0DH ; IS IT CARRIAGE RETURN
5323 C : JE U9 ; CARRET
5324 C : CMP AL,0AH ; IS IT A LINE FEED
5325 C : JNZ U10 ; LINE_FEED
5326 C : JE U11 ; BACKSPACE
5327 C : CMP AL,08H ; BACK_SPACE
5328 C : JE U8 ; BELL
5329 C : CMP AL,07H ; IS IT A BELL
5330 C : JE U11 ; BELL
5331 C :----- WRITE THE CHAR TO THE SCREEN
5332 C :----- MOV AH,10 ; WRITE CHAR ONLY
5333 C : MOV CX,1 ; ONLY ONE CHAR
5334 C : INT 10H ; WRITE THE CHAR
5335 C :----- POSITION THE CURSOR FOR NEXT CHAR
5336 C :----- INC DL ; TEST FOR COLUMN OVERFLOW
5337 C :----- CMP DL,BYTE PTR CRT_COLS ; SET_CURSOR
5338 C :----- JNZ U7 ; COLUMN FOR CURSOR
5339 C :----- SUB DL,DL ; SET_CURSOR_INC
5340 C :----- CMP DH,ROWS ; SET_CURSOR
5341 C :----- JNZ U6 ; SET_CURSOR
5342 C :----- INC DH ; SET_CURSOR
5343 C :----- SUB DH,DH ; SET_CURSOR
5344 C :----- CMP DH,ROWS ; SET_CURSOR
5345 C :----- JNZ U6 ; SET_CURSOR
5346 C :----- INC DH ; SET_CURSOR
5347 C :----- SCROLL REQUIRED
5348 C :----- U1:----- CALL SET_CPOS ; SET THE CURSOR
5349 C :----- U1:----- INC AL,CRT_MODE ; GET THE CURRENT MODE
5350 C :----- U1:----- CMP AL,4 ; READ-CURSOR
5351 C :----- U1:----- JNE U3 ; FILL WITH BACKGROUND
5352 C :----- U1:----- SUB BH,BH ; SCROLL-UP
5353 C :----- U1:----- CMP AL,7 ; READ-CURSOR
5354 C :----- U1:----- JNE U3 ; SCROLL-UP
5355 C :----- U1:----- INT 10H ; READ CHAR/ATTR
5356 C :----- U1:----- MOV BH,AH ; STORE IN BH
5357 C :----- U1:----- INT 10H ; SCROLL-UP
5358 C :----- U1:----- MOV BH,AH ; SCROLL ONE LINE
5359 C :----- U1:----- INC DH ; READ-CURSOR
5360 C :----- U1:----- SUB CX,CX ; FILL WITH CARRIER
5361 C :----- U1:----- MOV DH,ROWS ; LOWER RIGHT ROW
5362 C :----- U1:----- DEC DL ; LOWER RIGHT COLUMN
5363 C :----- U1:----- CALL SET_CPOS ; VIDEO-CALL-RETURN
5364 C :----- U1:----- INC DH ; SCROLL UP THE SCREEN
5365 C :----- U1:----- SUB CX,CX ; TTY-RETURN
5366 C :----- U1:----- MOV DH,ROWS ; RESTORE THE CHARACTER
5367 C :----- U1:----- DEC DL ; RETURN TO CALLER
5368 C :----- U1:----- CALL SET_CPOS ; SET_CURSOR-INC
5369 C :----- U1:----- INC DH ; NEXT ROW
5370 C :----- U1:----- SUB CX,CX ; SET-CURSOR
5371 C :----- U1:----- INT 10H ; ESTABLISH THE NEW CURSOR
5372 C :----- U5:----- POP AX ; SCROLL UP THE SCREEN
5373 C :----- U5:----- JMP V_RET ; TTY-RETURN
5374 C :----- U6:----- INC DH ; RESTORE THE CHARACTER
5375 C :----- U6:----- SUB CX,CX ; RETURN TO CALLER
5376 C :----- U6:----- INC DH ; SET_CURSOR-INC
5377 C :----- U6:----- MOV AH,2 ; NEXT ROW
5378 C :----- U6:----- INT 10H ; SET-CURSOR
5379 C :----- U6:----- MOV BH,U4 ; ESTABLISH THE NEW CURSOR
5380 C :----- U6:----- INT 10H ; SCROLL UP THE SCREEN
5381 C :----- U6:----- MOV BH,U4 ; TTY-RETURN
5382 C :----- U8:----- OR DL,DL ; ALREADY AT END OF LINE
5383 C :----- U8:----- JZ U7 ; SET_CURSOR
5384 C :----- U8:----- DEC DL ; NO -- JUST MOVE IT BACK
5385 C :----- U8:----- JMP U7 ; SET_CURSOR
5386 C :----- U8:----- INC DH ; SET_CURSOR
5387 C :----- U8:----- DEC DL ; SET_CURSOR
5388 C :----- U8:----- JMP U7 ; SET_CURSOR
5389 C :----- U8:----- INC DH ; SET_CURSOR
5390 C :----- U9:----- SUB DL,DL ; MOVE TO FIRST COLUMN
5391 C :----- U9:----- JMP U7 ; SET_CURSOR
5392 C :----- U9:----- INC DH ; SET_CURSOR
5393 C :----- U9:----- SUB DL,DL ; MOVE TO FIRST COLUMN
5394 C :----- U9:----- JNE U6 ; SET_CURSOR
5395 C :----- U9:----- INC DH ; YES, SCROLL THE SCREEN
5396 C :----- U9:----- JNE U6 ; NO, JUST SET THE CURSOR
5397 C :----- U10:----- CMP DH,ROWS ; BOTTOM OF SCREEN
5398 C :----- U10:----- JNE U6 ; YES, SCROLL THE SCREEN
5399 C :----- U10:----- INC DH ; NO, JUST SET THE CURSOR
5400 C :----- U10:----- SUB CX,CX ; SCROLL UP THE SCREEN
5401 C :----- U10:----- INC DH ; SET_CURSOR
5402 C :----- U10:----- INC DH ; SCROLL UP THE SCREEN
5403 C :----- U11:----- MOV BL,2 ; SET UP COUNT FOR BEEP
5404 C :----- U11:----- CALL BEEP ; SOUND THE POD BELL
5405 C :----- U11:----- JHP U5 ; TTY_RETURN
5406 C :----- U11:----- INC DH ; SET_CURSOR
5407 C :----- U11:----- SUB CX,CX ; SCROLL UP THE SCREEN
5408 C :----- U11:----- INC DH ; SET_CURSOR
5409 C :----- U11:----- SUB CX,CX ; SCROLL UP THE SCREEN
5410 C :----- U11:----- INC DH ; SET_CURSOR
5411 C :----- U11:----- SUB CX,CX ; SCROLL UP THE SCREEN
5412 C :----- U11:----- INC DH ; SET_CURSOR
5413 C :----- AHF:----- ASSUME DS:ABSO ; GET NUMBER OF COLUMNS
5414 C :----- MOV AH,BYTE PTR CRT_COLS ; GET NUMBER OF COLUMNS
5415 C :----- MOV BH,ACTIVE_PAGE ; GET NUMBER OF COLUMNS
5416 C :----- MOV AL,INFO ; GET NUMBER OF COLUMNS
5417 C :----- AND AL,080H ; GET NUMBER OF COLUMNS
5418 C :----- OR AL,CRT_MODE ; GET NUMBER OF COLUMNS

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1096 5F
1097 5E
1098 59
1099 59
109A 5A
109B 1F
109C 07
109D 5D
109E CF

5419 C POP DI
5420 C C POP SI
5421 C C POP CX
5422 C C POP DX
5423 C C POP DS
5424 C C POP ES
5425 C C POP BP
5426 C C IRET
5427 C
5428 C SUBTLL
5429 C
5430 C
5431 C
5432 C PAL_SET PROC NEAR
5433 C PUSH AX
5434 C CALL WHAT_BASE
5435 C CLI
5436 C VR: IN AL,DX
5437 C TEST AL,0BH ; VERTICAL RETRACE
5438 C JZ VR
5439 C POP AX
5440 C MOV DL,ATTR_WRITE
5441 C MOV AL,AH
5442 C XCHG AL,AH
5443 C OUT DX,AL
5444 C XCHG AL,AH
5445 C OUT DX,AH
5446 C MOV AL,020H
5447 C OUT DX,AL
5448 C STI
5449 C RET
5450 C PAL_SET ENDP
5451 C
5452 C PAL_ON PROC NEAR
5453 C CALL PAL_INIT
5454 C MOV DL,ATTR_WRITE
5455 C MOV AL,020H
5456 C OUT DX,AL
5457 C RET
5458 C PAL_ON ENDP
5459 C
5460 C PAL_INIT PROC NEAR
5461 C CALL WHAT_BASE
5462 C IN AL,DX
5463 C RET
5464 C PAL_INIT ENDP
5465 C
5466 C ;----- SET PALETTE REGISTERS
5467 C AH10:
5468 C ASSUME DS:ABSO
5469 C TEST INFO_2
5470 C JNZ BM_OK ; IN MONOCHROME MODE
5471 C
5472 C
5473 C ;----- HERE THE EGA IS IN A COLOR MODE
5474 C
5475 C CMP BYTE PTR ADDR_6845,0B4H
5476 C JE BM_OUT
5477 C
5478 C MOV AH,AL
5479 C OR AH,AH
5480 C JNZ BM_1
5481 C
5482 C ;----- SET INDIVIDUAL REGISTER
5483 C
5484 C SUB BP,BP
5485 C LES DI,SAVE_PTR
5486 C ADD DI,4_SAVE_PTR
5487 C LES DI,DWORD PTR ES:[DI]
5488 C MOV AX,ES
5489 C OR AX,DI
5490 C JZ TLO_1
5491 C INC BP
5492 C TLO_1:
5493 C
5494 C CALL PAL_INIT
5495 C MOV AH,BL
5496 C MOV AL,BH
5497 C CALL PAL_SET
5498 C OR BP,BP
5499 C JZ BM_OUT
5500 C MOV AL,BH
5501 C SHL BH,1
5502 C ADD DI,BX
5503 C MOV ES:[DI],AL
5504 C
5505 C BM_OUT: JMP V_RET
5506 C
5507 C
5508 C BM_1: DEC AH
5509 C JNZ BM_2
5510 C
5511 C
5512 C SUB BP,BP
5513 C LES DI,SAVE_PTR
5514 C ADD DI,4_SAVE_PTR
5515 C LES DI,DWORD PTR ES:[DI]
5516 C MOV AX,ES
5517 C OR AX,DI
5518 C JZ TLO_2
5519 C INC BP
5520 C
5521 C
5522 C ;----- SET OVERSCAN REGISTER
5523 C
5524 C CALL PAL_INIT
5525 C MOV AH,011H
5526 C MOV AL,BH
5527 C CALL PAL_SET
5528 C CALL PAL_ON
5529 C
5530 C OR BP,BP
5531 C JZ BM_OUT
5532 C ADD DI,011H,BH
5533 C MOV ES:[DI],BH
5534 C
5535 C
5536 C
5537 C
5538 C
5539 C
5540 C
5541 C
5542 C
5543 C PUSH DS
5544 C PUSH ES
109F
109F 50
10A0 E8 0DD05 R
10A1 FA
10A4
10A4 EC
10A5 A8 08
10A7 78 FB
10A9 58
10AA B2 C0
10AC 86 C4
10AE EE 04
10AF C4
10B1 EE
10B2 B0 20
10B4 EE
10B5 FB
10B6 C3
10B7
10B7 E8 1DC0 R
10B8 B2 C0
10B9 B0 20
10BDB EE
10BFC C3
10C0
10C0 E8 0DD05 R
10C3 EC
10C4 C3
10C5
10C5
10C5 F6 06 0487 R 02
10CA 75 07
10D0 80 3E 0463 R B4
10D1 74 33
10D3
10D3 8A E0
10D5 0A E4
10D7 75 30
10D9 28 ED
10DB C4 3E 04AB R
10DF 83 C7 04
10DE 26: C4 3D
10D9 8C 04
10D7 8C C7
10D9 74 01
10E0 45
10EC
10E1 26: 88 05
10E0 E9 219E R
10E0 75 2D
10E0 28 ED
10EF C4 3E 04AB R
10E1 83 C7 04
10E1 8C: C4 3D
10E1 8C C0
10E1 0B C7
10E1 74 01
10E1 45
10E20
10E20 E8 1DC0 R
10E23 B4 11
10E25 8A C7
10E27 E8 109F R
10E2A E8 1DB7 R
10E2D 0B ED
10E2F 74 05
10E31 83 C7 11
10E34 26: 88 3D
10E37 E9 219E R
10E3A FE CC
10E3C 75 40
10E3E 1E
10E3F 06

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1E40 C4 3E 04A8 R      5545          LES    D1_SAVE_PTR
1E44 B3 C7 04            5546          ADD    D1,4
1E47 26: C4 3D          5547          LES    D1,DWORD PTR ES:[D1] ; ES:DI PTR TO PAL SAVE AREA
1E4A 8C C0              5548          MOV    AX,ES
1E4C 0B C7              5549          OR    AX,D1
1E4E 74 09              5550          JZ    TL0_3
1E4F 5551
1E50 1F                5552          5553          POP   DS
1E51 E1                5554          PUSH  DS
1E52 BB F2              5555          MOV    SI,DX
1E54 B9 0011            5556          MOV    CX,17D
1E57 F3/A4              5557          REP   MOVSBD
1E58 5558
1E59 07                5559          TLO_3:    POP   ES
1E5A 1F                5560          POP   DS
1E5B BB DA              5561          5562          MOV    BX,DX
1E5D E8 1000 R          5563          CALL  PAL_INIT
1E60 2A E4              5564          SUB   AH,AH
1E62 5565
1E62 26: BA 07          5566          BM_2A:    MOV    AL,ES:[BX]
1E63 E8 109F R          5567          CALL  PAL_SET
1E64 FE C4              5568          INC   AH
1E64 43                5569          CMP   AH,010H
1E66 80 FC 10          5570          INC   BX
1E67 F2 F2              5571          BM_2A:    INC   AH
1E70 F4 HN              5572          INC   AH
1E72 26: BA 07          5573          MOV    AL,ES:[BX]
1E75 E8 109F R          5574          CALL  PAL_SET
1E78 E8 10B7 R          5575          CALL  PAL_ON
1E7B E9 219E R          5576          CALL  V_RET
1E7B 5577
1E7E FE CC              5578          BM_3:    DEC   AH
1E80 75 29              5579          JNZ   BM_4
1E82 5580
1E82 5581
1E82 5582
1E82 5583          ;----- TOGGLE INTENSIFY/BLINKING BIT
1E82 5584
1E83 53                5585          PUSH  BX
1E83 E8 005A R          5586          CALL  MAKE_BASE
1E86 83 C3 33          5587          ADD   AL,010H + LN_4
1E89 26: BA 07          5588          MOV   AL,ES:[BX]
1EBC 5B                5589          POP   BX
1E8D 0A DB              5590          OR    BL,BL
1E8F 75 0A              5591          JNZ   BM_6
1E8F 5592
1E8F 5593
1E8F 5594
1E8F 5595
1E8F 5596          ;----- ENABLE INTENSIFY
1E91 80 26 0465 R DF    5597          AND   CRT_MODE_SET,11011111B
1E96 24 F7              5598          AND   AL,0F7H
1E98 EB OC 90          5599          JMP   BM_7
1E99 559A
1E99 FE CB              5600          BM_6:    DEC   BL
1E9D 75 07              5601          JNZ   BM_7
1E9D 5602
1E9D 5603
1E9D 5604
1E9D 5605          ;----- ENABLE BLINK
1E9D 5606          OR    CRT_MODE_SET,020H
1EAD 0C 0B              5607          OR    AL,08H
1EAD 5608
1EAD B4 10              5609          BM_7:    MOV   AH,P_MODE
1EAD E8 109F R          5610          CALL  PAL_SET
1EAB E9 219E R          5611          BM_8:    JMP   V_RET
1EAD 5612
1EAD 5613
1EAD 5614          INCLUDE VCHGEN.INC
1EAD 5615          SUBTLL VCHGEN.INC
1EAD 5616          PAGE
1EAD 5617          ;----- ENTRY
1EAD 5618          AL = 0 USER SPECIFIED FONT
1EAD 5619          1 8 X 14 FONT
1EAD 5620          2 8 X 8 DOUBLE DOT
1EAD 5621          BL = BLOCK TO LOAD
1EAD 5622
1EAD 5623          ;----- CH_GEN:
1EAE 50                5624          PUSH  AX          ; SAVE THE INVOLVED REGS
1EAF 55                5625          PUSH  BP
1EAB 53                5626          PUSH  BX
1EAB 51                5627          PUSH  CX
1EAB 52                5628          PUSH  DX
1EAB 06                5629          PUSH  ES
1EAD 5630
1EAD 5631          ASSUME DS:ABSO
1EAD 5632          CALL  DDS
1EAD 5633          MOV   AL,CRT_MODE
1EAD 5634          PUSH  AX          ; SET DATA SEGMENT
1EAD 5635          PUSH  AL,0000          ; GET THE CURRENT MODE
1EAD 5636          CMP   AL,7          ; SAVE IT
1EAD 5637          JE    H14          ; THIS IS MONOCHROME
1EAD 5638          MOV   CRT_MODE,0BH
1EAD 5639          JMP   SHORT H15          ; MONOCHROME VALUES
1EAD 5640          H14:    MOV   CRT_MODE,OCH          ; COLOR VALUES
1EAD 5641          H15:    MOV   CRT_MODE,0CH          ; SKIP
1EAD 5642          CALL  SET_REGS
1EAD 5643          CALL  DDS
1EAD 5644          POP   AX          ; RESTORE THE DATA SEGMENT
1EAD 5645          MOV   CRT_MODE,AL          ; RECOVER OLD MODE VALUE
1EAD 5646          H15:    MOV   CRT_MODE,AL          ; RETURN TO LOW MEMORY
1EAD 5647          POP   ES          ; RESTORE REGS THAT WERE
1EAD 5648          POP   DX          ; USED BY THE MODE SET
1EAD 5649          POP   BX          ; ROUTINES
1EAD 5650          POP   BP
1EAD 5651          POP   BP
1EAD 5652          POP   AX
1EAD 5653          OR    AL,AL          ; SET FLAGS
1EAD 5654          JZ    DO_MAP2
1EAD 5655          PUSH  CS          ; USER SPECIFIED FONT
1EAD 5656          PUSH  ES          ; SET SEGMENT TO
1EAD 5657          POP   ES          ; THIS MODE
1EAD 5658          SUB   BX,DX          ; ZERO BASE START OFFSET
1EAD 5659          MOV   CX,0256D          ; CHAR COUNT (FULL SET)
1EAD 5660          DEC   AL          ; WHICH PARAMETER
1EAD 5661          JNC   H17          ; MUST BE ON
1EAD 5662          MOV   BH,010D          ; DISPLAY CHARACTER
1EAD 5663          MOV   BP,OFFSET CGMN          ; 8 X 14 TABLE OFFSET
1EAD 5664          JMP   SHORT DO_MAP2          ; STORE IT
1EAD 5665          H7:    MOV   BH,8          ; 8 X 8 FONT
1EAD 5666          MOV   BP,OFFSET CGDDOT          ; ROM 8 X 8 DOUBLE DOT
1EAD 5667
1EAD 5668
1EAD 5669          ;----- ALPHA CHARACTER GENERATOR LOAD
1EAD 5670

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5671 C : ENTRY
5672 C   ES:BP = POINTER TO TABLE
5673 C   CX = COUNT OF CHARACTERS
5674 C   DX = NUMBER OF OFFSET INTO MAP 2
5675 C   AX = NUMBER OF BYTES PER CHARACTER
5676 C   BH = BYTES PER CHARACTER
5677 C   BL = MAP 2 BLOCK TO LOAD
5678 C-----DO_MAP2-----C
5679 C
5680 C   PUSH ES ; FONT TABLE SEGMENT
5681 C   POP DS ; ADDRESSING TO TABLE
5682 C   PUSH DX ; SAVE REGISTER
5683 C   SRLLOAD ES,0A000H ; ADDRESSING TO MAP 2
5684 C   PSHL DS,0A000H
5685 C   MOV ES,DX ; RECOVER REGISTER
5686 C   POP DX ; MULTIPLY BY 020H SINCE
5687 C   PUSH CX ; MAX BYTES PER
5688 C   MOV AH,00H ; CHARACTER IS 32D-020H
5689 C   SHL DX,CL ; RECOVER
5690 C   POP CX ; WHICH 16K BLOCK TO LOAD
5691 C   OR BL,BL ; BLOCK ZERO
5692 C   JZ H3
5693 C-----H4-----C
5694 C   ADD DX,04000H ; INCREMENT TO NEXT BLOCK
5695 C   DEC BL ; ANY MORE
5696 C   JNZ H4 ; DO ANOTHER
5697 C-----H3-----C
5698 C   MOV AL,BH ; BYTES PER CHARACTER
5699 C   SUB AH,AH ; ZERO
5700 C   MOV DI,DX ; OFFSET INTO MAP
5701 C   MOV SI,BP ; OFFSET INTO TABLE
5702 C   JCXZ LD_OVER ; CHARACTER COUNT
5703 C
5704 C   PUSH CX ; SAVE CHARACTER COUNT
5705 C   MOV CX,AX ; ONE ENTIRE CHARACTER
5706 C   MUL DI,DX ; AT A TIME
5707 C   SUB DI,AX ; ADJUST OFFSET
5708 C   ADD DI,020H ; NEXT CHARACTER POSITION
5709 C   POP CX ; RECOVER CHARACTER COUNT
5710 C   LOOP H4 ; DO THE REST
5711 C-----LD_OVER-----C
5712 C   RET
5713 C
5714 C-----BRK_1-----C
5715 C   ASSUME DS:ABSO ; SET LOW MEMORY SEGMENT
5716 C   CALL DDS ; GET BYTES/CHARACTER
5717 C   MOV POINTS,AX ; CRTC REGISTER
5718 C   MOV DX,ADDR_6845 ; CRTC MODE,7
5719 C   IN AL,H11 ; R14H
5720 C   MOV AH,C_UNDERLN_LOC ; SET THE UNDERLINE LOC
5721 C   CALL OUT_DX ; SET THE CURSOR
5722 C-----H11A-----C
5723 C   DEC AL ; POINTS - 1
5724 C   MOV AH,C_MAX_SCAN_LN ; RO9H
5725 C   CALL OUT_DX ; SET THE CHARACTER HEIGHT
5726 C   DEC AL ; POINTS - 2
5727 C
5728 C   MOV CH,AL ; CURSOR START
5729 C   MOV CL,AL ; CURSOR END
5730 C   INC CL ; ADJUST END
5731 C   MOV AH,1 ; SET C_TYPE BIOS CALL
5732 C   INT 10H ; SET THE CURSOR
5733 C
5734 C   MOV BL,CRT_MODE ; GET THE CURRENT MODE
5735 C   MOV AX,350D ; MAX SCANS ON SCREEN
5736 C   CMP BH,3 ; 640x200 ALPA MODES
5737 C   JA H11 ; MUST BE 350
5738 C   CALL BRST_DET ; ROWS*BYTES/CHAR
5739 C   JC H11 ; ADJUST CUR
5740 C   MOV AX,200D ; CRT ADDRESS
5741 C
5742 C-----H11-----C
5743 C   CWD ; PREPARE TO DIVIDE
5744 C   DIV POINTS ; MAX ROWS ON SCREEN
5745 C   DEC AX ; ADJUST
5746 C   MOV BX,AL ; SET ROWS
5747 C   INC AL ; READJUST
5748 C   SUB AH,AH ; CLEAR
5749 C   MUL POINTS ; ROWS*BYTES/CHAR
5750 C   DEC AX ; ADJUST
5751 C   MOV DX,ADDR_6845 ; CRT ADDRESS
5752 C   MOV AH,C_VRT_DSP_END ; SCANS DISPLAYED
5753 C   CALL OUT_DX ; SET IT
5754 C   MOV AL,ROWS ; GET CHARACTER ROWS
5755 C   INC AL ; ADJUST
5756 C   MUL BYTE PTR CRT_COLS ; ROWS*COLS
5757 C   SHL AX,1 ; #2 FOR ALPA MODE
5758 C   ADD AX,256D ; SPACE BETWEEN PAGES
5759 C   MOV CRT_END,AX ; BYTES PER PAGE
5760 C   CALL PR_5 ; VIDEO ON
5761 C   JMP V_RET ; RETURN TO CALLER
5762 C
5763 C-----LOADABLE CHARACTER GENERATOR ROUTINES-----C
5764 C
5765 C-----AH11-----C
5766 C   CMP AL,010H ; CHECK PARAMETER
5767 C   JAE AH11_ALPHA1 ; NEXT STAGE
5768 C
5769 C-----ALPHA MODE ACTIVITY HERE-----C
5770 C
5771 C   CMP AL,03H ; RANGE CHECK
5772 C   JAE H11_ALPHA1 ; NEXT STAGE
5773 C   CALL GH_GEN ; SET THE CHAR GEN
5774 C   CALL SET_REGS ; VIDEO ON
5775 C
5776 C   ASSUM DS:ABSO ; SET THE DATA SEGMENT
5777 C   CALL DDS ; GET THE MODE
5778 C   MOV CX,CURSOR_MODE ; SET C_TYPE
5779 C   MOV AH,1 ; EMULATE CORRECT CURSOR
5780 C   INT 10H ; RETURN TO CALLER
5781 C
5782 C-----SET THE CHARACTER GENERATOR BLOCK SELECT REGISTER-----C
5783 C
5784 C-----H1-----C
5785 C
5786 C   JNE H2 ; NOT IN RANGE
5787 C   MOV DH,3 ; SEQUENCER
5788 C   MOV DL,SEQ_ADDR ; SEQUENCER
5789 C
5790 C   MOV AX,1 ; AH=S_RESET, AL=1
5791 C   CALL OUT_DX ; AH=BL
5792 C
5793 C   MOV AH,S_CGEN ; CHAR BLOCK REGISTER
5794 C   MOV AL,BL ; GET THE VALUE
5795 C   CALL OUT_DX ; SET IT
5796 C

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1FCA B8 0003      5797 C    MOV    AX, 3          ; AH=S_RESET, AL=3
1FCF E8 0D15 R     5798 C    CALL   OUT_DX
1FD0 5999 C        H2:   JMP    V_RET           ; RETURN TO CALLER
1FD0 E9 219E R     5800 C
1FD3 5A02 C
1FD3 3C 20         5801 C
1FD5 73 26         5802 C
1FD7 5996 C
1FD7 5807 C
1FD7 2C 10         5808 C
1FD9 3C 02         5809 C    SUB    AL, 010H
1FDB 57 F3         5810 C    CMP    AL, 02H
1FD0 50             5811 C    JNC    H2
1FD7 53             5812 C    PUSH   AX
1FD7 E8 1EAE R     5813 C    PUSH   BX
1FD2 5992 C
1FE5 5B             5814 C    CALL   CH_GEN
1FE6 58             5815 C    CALL   STL_REGS
1FE7 8A E0         5816 C    POP    AX
1FE8 09 E4         5817 C    POP    AX
1FE8 C7             5818 C    MOV    AH, AL
1FED 74 09         5819 C    OR    AH, AH
1FEE 80 08         5820 C    MOV    AL, BH
1FF1 80 FC 01       5821 C    JZ    H1
1FF1 75 02         5822 C    MOV    AL, 8
1FF6 80 0E         5823 C    CMP    AH, 1
1FF8 2A E4         5824 C    JNE    H13
1FFA E9 1F29 R     5825 C    MOV    AH, 14D
1FF9 5999 C
1FFA 5826 C
1FFB 5827 C
1FFB 5828 C    H13:  SUB    AH, AH
1FFB 5829 C    JMP    BRK_1           ; CLEAR UPPER BYTE
1FFB 5830 C
1FFC 5831 C
1FFD 5832 C
1FFD 3C 30         5833 C    ASSUME DS:ABS0
1FFF 73 6A         5834 C    CMW   AL, 030H
2001 2C 20         5835 C    JAE    AH11_INFORM
2003 75 11         5836 C    SUE    AL, 020H
2003 5998 C
2005 5837 C
2005 5838 C
2005 5839 C
2005 5840 C
2005 5841 C    ;----- GRAPHICS MODE ACTIVITY HERE
2005 5842 C
2005 5843 C    AH11_GRAPHICS: ASSUME DS:ABS0
2005 5844 C    SRLOAD DS, 0
2005 5845 C    SUB   DX, DX
2005 5846 C    CLI
2005 5847 C    MOV   WORD PTR EXT_PTR + BP
2005 5848 C    MOV   WORD PTR EXT_PTR + 2, ES
2005 5849 C
2005 5850 C
2005 5851 C
2005 5852 C    F11:  JMP    V_RET
2005 5853 C    F10:  ASSUME DS:ABS0
2005 5854 C    PUSH   DX
2005 5855 C    SRLOAD DS, 0
2005 5856 C    SUB   DX, DX
2005 5857 C    MOV   DX, DX
2005 5858 C    CMP   AL, 03H
2005 5859 C    JA    F11
2005 5860 C    DEC   AL
2005 5861 C    JZ    F19
2005 5862 C    PUSH   CS
2005 5863 C    POP    ES
2005 5864 C    DEC   AL
2005 5865 C    JNZ   F13
2005 5866 C    MOV   CX, 14D
2005 5867 C    MOV   BP, OFFSET COMM
2005 5868 C    JMP   SHORT F19
2005 5869 C
2005 5870 C    F13:  MOV   CX, 8
2005 5871 C    MOV   BP, OFFSET GDDOT
2005 5872 C    F19:  CL
2005 5873 C    MOV   DR
2005 5874 C    WORD PTR GRX_SET + BP
2005 5875 C    WORD PTR GRX_SET + 2, ES
2005 5876 C    STI
2005 5877 C    ASSUME DS:ABS0
2005 5878 C    CALL   DR
2005 5879 C    MOV   POINTS, CX
2005 5880 C    MOV   AL, BL
2005 5881 C    MOV   BX, OFFSET RT
2005 5882 C    OR    AL, AL
2005 5883 C    JNZ   DR
2005 5884 C    MOV   AL, DL
2005 5885 C    JMP   DR_1
2005 5886 C
2005 5887 C    DR_3:  CMP   AL, 3
2005 5888 C    JBE   DR_2
2005 5889 C    MOV   AL, 2
2005 5890 C
2005 5891 C    DR_2:  XLAT   CS:RT
2005 5892 C    DR_1:  DEC    AL
2005 5893 C    MOV   ROWS, AL
2005 5894 C    JMP   V_RET
2005 5895 C
2005 5896 C
2005 5897 C
2005 5898 C    RT    LABEL  BYTE
2005 5899 C    DB    00D, 14D, 25D, 43D
2005 5900 C
2005 5901 C
2005 5902 C
2005 5903 C
2005 5904 C
2005 5905 C    ;----- INFORMATION RETURN DONE HERE
2005 5906 C
2005 5907 C    F5:   JMP    V_RET
2005 5908 C
2005 5909 C
2005 5910 C    F6:   MOV    CX, POINTS
2005 5911 C    MOV    DL, ROWS
2005 5912 C    CMP    BH, 7
2005 5913 C    JA    F5
2005 5914 C    CMP    BH, 1
2005 5915 C    JA    F7
2005 5916 C
2005 5917 C    ASSUME DS:ABS0
2005 5918 C    PUSH   DX
2005 5919 C    SRLOAD DS, 0
2005 5920 C    SUB   DX, DX
2005 5921 C    MOV   DS, DX
2005 5922 C    POP   DX

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208A 0A FF      5923 C   OR    BH,BH
208C 75 07      5924 C   JNZ   F9'
208E C4 2E 007C R 5925 C   LES   BP,EXT_PTR
2092 EB 1A 90    5926 C   JMP   INFORM_OUT
2094          F9: 5927 C   LESS  BP,GRX_SET
2095 C4 2E 010C R 5928 C   JMP   INFORM_OUT
2099 EB 13 90    5929 C
209D          F9: 5930 C ;----- HANDLE BH = 2 THRU BH = 5 HERE RETURN ROM TABLE POINTERS
209E          F9: 5931 C
209F          F9: 5932 C
209C          F7: 5933 C
209C          F7: 5934 C   ASSUME DS:ABSO
209C          F7: 5935 C   SUB   BH,2
209C          F7: 5936 C   MOVV  BL,BH
20A1 2A FF      5937 C   SUB   BH,BH
20A3 D1 E3      5938 C   SAL   BX,1
20A5 81 C3 20B7 R 5939 C   ADD   BX,OFFSET TBL_5
20A9 2E BB 2F    5940 C   MOV   BP,CS:[BX]
20A9 0E          5941 C   PUSH  CS
20AD 07          5942 C   POP   ES
20AF          F9: 5943 C
20AF          F9: 5944 C   INFORM_OUT:
20AF          F9: 5945 C   POP   DI
20AF 5F          5946 C   POP   SI
20B0 5B          5947 C   POP   BX
20B1 58          5948 C   POP   AX
20B2 58          5949 C   POP   AX
20B3 11          5950 C   POP   DS
20B4 58          5951 C   POP   AX
20B5 58          5952 C   POP   AX
20B6 CF          5953 C   IRET
20B6          F9: 5954 C
20B6          F9: 5955 C ;----- TABLE OF CHARACTER GENERATOR OFFSETS
20B7          F9: 5956 C
20B7          F9: 5957 C   TBL_5 LABEL WORD
20B9 0000 E     5958 C   DW   OFFSET COMM
20B8 0000 E     5959 C   DW   OFFSET CGDDOT
20BD 0000 E     5960 C   DW   OFFSET INT_1F_1
20BD 0000 E     5961 C   DW   OFFSET CGMR_FDG
20B6          F9: 5962 C
20B6          F9: 5963 C   SUBTLL
20B6          F9: 5964 C
20B6          F9: 5965 C ;----- ALTERNATE SELECT
20B6          F9: 5966 C
20B6          F9: 5967 AH12:
20B6          F9: 5968 C   ASSUME DS:ABSO
20B6          F9: 5969 CMP   BL,010H
20B6          F9: 5970 JB    ACT_1
20C4 74 51      5971 JE    ACT_3
20B6          F9: 5972 CMP   BL,020H
20C9 74 03      5973 JE    ACT_2
20CB E9 219E R   5974 JMP   V_RET
20CE          F9: 5975 ACT_2:
20CE          F9: 5976 SRLOAD DS,0
20D0 2B D2      5977 +   SUB   DX,DX
20D0 8E DA      5978 +   MOV   DS,DX
20D2 FA          5979 CL1
20D3 07 06 0014 R 21A7 R 5980 MOV   WORD PTR INT5_PTR,OFFSET PRINT_SCREEN
20D9 8C 0E 0016 R 5981 MOV   WORD PTR INT5_PTR+2,CS
20D9 FB          5982 STI
20D9          F9: 5983 JMP   V_RET
20E1          F9: 5984 ACT_3:
20E1 8A 3E 0487 R 5985 MOV   BH,INFO
20E5 80 E7 02    5986 AND   BH,1
20E8 DO EF      5987 SHR   BH,1
20E8          F9: 5988
20E4 0A 0487 R   5989 MOV   CL,INFO_3
20E0 24 60      5990 AND   AL,01100000B
20EF B1 05      5991 MOV   CL,2
20F1 D2 E8      5992 SHR   CH,1
20F3 8A 08       5993 MOV   AL,CL
20F6 80 E5 0F      5994 MOV   BL,AL
20F6          F9: 5995 AND   CH,0FH
2109 5F          6004 POP   DI
210A 5E          6005 POP   SI
210B 5A          6006 POP   DX
210D 5A          6007 POP   DS
210D 5A          6008 POP   DX
210E 1F          6009 POP   DS
210F 07          6010 POP   ES
2111 5D          6011 POP   BP
2111 CF          6012 IRET
2112          F9: 6013 AH12_X:
2112          F9: 6014 JMP   V_RET
2115          F9: 6015 ACT_1:
2115          F9: 6016 STR_OUTZ:
2115          F9: 6017 JMP   V_RET
2118          F9: 6018 ;----- WRITE STRING
2118          F9: 6019
2118          F9: 6020
2118          F9: 6021
2118 3C 04      6022 CMP   AL,04
211A 73 F9      6023 JAE   STR_OUTZ
211C E3 F7      6024 JCXZ STR_OUTZ
211D 55          6025 PUSH  BX
211F 8A DF      6026 MOV   BL,BH
2121 2A FF      6027 SUB   BH,BH
2123 D1 E3      6028 SAL   BX,1
2123 80          6029 MOV   SI,[BX + OFFSET CURSOR_POSN]
2129 5B          6030 POP   SI
212A 56          6031 PUSH  SI
212B 50          6032 PUSH  CX
212C 88 0200    6033 PUSH  BX
212F CD 10      6034 MOV   AX,0200H
2131 58          6035 INT   10H
2131          F9: 6036 POP   AX
2132          F9: 6037 STR_1:
2132          F9: 6038 PUSH  CX
2133 51          6039 PUSH  BX
2134 50          6040 PUSH  AX
2135 86 E0      6041 XCHG AH,AL
2135 20 20: 8A 46 00 6042 MOV   BP,ES:[BP]
2138 A5          6043 INC   BP
213C 3C 00      6044 CMP   AL,0DH
213E 74 3D      6045 JE    STR_CR_LF
2140 3C 0A      6046 CMP   AL,0AH
2140 70 39      6047 JE    STR_CR_LF
2144 3C 08      6048 CMP   AL,0BH
2118          F9: ;----- RANGE CHECK
2118 73 F9      6022 ;----- INVALID PARAMETER
211C E3 F7      6024 ;----- GET REGISTER
211D 55          6025 ;----- GET PAGE TO LOW BYTE
211F 8A DF      6026 ;----- *2 FOR WORD OFFSET
2121 2A FF      6027 ;----- GET CURSOR POSITION
2123 D1 E3      6028 ;----- TEST FOR EQUAL
2123 80          6029 ;----- CURRENT VALUE ON STACK
2129 5B          6030 ;----- SET THE CURSOR POSITION
212A 56          6031 ;----- GET THE CHAR TO WRITE
2135 86 E0      6041 ;----- CARRIAGE RETURN
2135 20 20: 8A 46 00 6042 ;----- LINE FEED
2138 A5          6043 ;----- BACKSPACE
213C 3C 00      6044 ;----- CARRIAGE RETURN
213E 74 3D      6045 ;----- LINE FEED
2140 3C 0A      6046 ;----- BACKSPACE
2140 70 39      6047 ;----- CARRIAGE RETURN
2144 3C 08      6048 ;----- BACKSPACE

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2146 74 35          609    JE     STR_CR_LF
214A 3C 07          609    CMP    AL,07H
214C 74 31          6051   JE     STR_CR_LF
214C B9 0001        6052   MOV    CX,T
214D B8 FC 02        6053   CMP    AH,2
2152 72 02          6054   JB    DO_STR
2154 26: 8A 5E 00    6055   MOV    BL,ES:[BP]
2158 45              6056   INC    BP
2159          6057   DO_STR: ; BELL
2159 B4 09          6058   MOV    AH,09H
215B CD 10          6059   INT    IOH
215D FE C2          6060   INC    DL
215F 3A 16 044A R   6061   CMP    DL_BYT PTR CRT_COLS
2160 72 11          6062   JB    STR_2
2165 45: 8A 0484 R   6063   CMP    DH
2169 75 07          6064   JNE    STR_3
216B B8 0E0A        6065   MOV    AX,0E0AH
216E CD 10          6066   INT    10H
2171 FE CE          6067   DEC    DH
2172          6068   STR_3: ; WRITE THE CHAR/ATTR
2174 2A D2          6069   INC    DH
2174          6070   SUB    DL,DL
2176 B8 0200        6071   STR_2: ; NEXT CURSOR POSITION
2179 CD 10          6072   MOV    AX,0200H
217B EB 0E          6073   INT    10H
217D          6074   JMP    SHORT STR_4 ; COLUMN OVERFLOW
217E          6075   STR_CR_LF: ; NOT YET
217F B8 0E          6076   MOV    AH,0EH
217F CD 10          6077   INT    10H
2181 8A DF          6078   MOV    BL,BH
2183 2A FF          6079   SUB    BH,BH
2185 D1 E3          6080   SAL    BX,1
2189 8B 97 0450 R   6081   MOV    DX,[BX + OFFSET_CURSOR_POSN] ; *2 FOR WORD OFFSET
218B          6082   STR_4: ; GET CURSOR POSITION
218B 58              6083   POP    AX
218C B8              6084   POP    BX
218D 59              6085   POP    CX
218E E2 A2          6086   LOOP   STR_1
218F          6087   STR_OUT: ; RECOVER CURSOR POSITION
2190 5A              6088   POP    DX
2191 3C 01          6089   POP    DX
2193 74 09          6090   CMP    AL,1
2195 3C 03          6091   JE     STR_OUT
2197 74 05          6092   CMP    AL,3
2199 B8 0200        6093   JE     STR_OUT
219C CD 10          6094   MOV    AX,0200H
219E          6095   INT    10H
219F          6096   STR_OUT: ; SET CURSOR POSITION
2199          6097   ; ALLOW FALL THROUGH
2199          6098
2199          6099
219E          6100   V_RET: ; VIDEO BIOS RETURN
219E 5F              6101   PROC   NEAR
219F SE              6102   POP    DI
21A0 5B              6103   POP    SI
21A1 59              6104   POP    BX
21A2 5A              6105   POP    CX
21A3 1F              6106   POP    DS
21A4 07              6107   POP    ES
21A5 30              6108   POP    BP
21A6 CF              6109   IRET
21A7          6110   V_RET: ENDP
21A7          6111
21A7 COMBO_VIDEO: ENDP
21A7          6112
21A7          6113
21A7          6114   INCLUDE  VPRSC.INC
21A7          6115   SUBTL  VPRSC.INC
21A7          6116   PAGE
21A7          6117
21A7          6118   C----- INTERRUPT 5
21A7          6119   THIS LOGIC WILL BE INVOKED BY INTERRUPT 05H TO PRINT THE
21A7          6120   SCREEN. THE CURSOR POSITION AT THE TIME THIS ROUTINE IS INVOKED
21A7          6121   WILL BE PRESERVED AND RESTORED UPON RETURN. THIS ROUTINE IS
21A7          6122   INTENDED TO RUN WITH INTERRUPTS ENABLED. IF A SUBSEQUENT
21A7          6123   'PRINT SCREEN' KEY IS DEPRESSED DURING THE TIME THIS ROUTINE
21A7          6124   IS PRINTING IT WILL BE IGNORED.
21A7          6125   ADDRESS 50:0 CONTAINS THE STATUS OF THE PRINT SCREEN:
21A7          6126          50:0 =0    EITHER PRINT SCREEN HAS NOT BEEN CALLED
21A7          6127          OR UPON RETURN FROM A CALL THIS INDICATES
21A7          6128          A SUCCESSFUL OPERATION.
21A7          6129          =1    PRINT SCREEN IS IN PROGRESS
21A7          6130          =255   ERROR ENCOUNTERED DURING PRINTING
21A7          6132
21A7          6133   C----- ASSUME CS:CODE,DS:AB50
21A7          6134   PRINT_SCREEN: PROC FAR
21A7          6135   STI
21A8 1E              6136   PUSH   DS      ; MUST RUN WITH INTS ENABLED
21A9 50              6137   PUSH   AX      ; MUST USE 50:0 FOR DATA
21A9 53              6138   PUSH   BX      ; AREA STORAGE
21A9 21              6139   PUSH   CX      ; USE THIS LATER FOR CURSOR LIMITS
21AC          6140   PUSH   DX      ; WILL HOLD CURRENT CURSOR POS
21AD E8 OCFF R       6141   CALL   DDS
21B0 80 3E 0500 R 01 6142   CMP    STATUS_BYTE,1 ; SEE IF PRINT ALREADY IN PROGRESS
21B0          6143   INT    10H   ; JUMP IF PRINT IN PROGRESS
21B7 C6 06 0500 R 01 6144   MOV    STATUS_BYTE,1 ; INDICATE PRINT NOW IN PROGRESS
21BC B4 0F          6145   MOV    AH,15 ; WILL REQUEST THE CURRENT MODE
21BE CD 10          6146   INT    10H   ; [AL]=MODE (NOT USED)
21BE          6147   ; [AH]=NUMBER COLUMNS/LINE
21BE          6148   ; [BH]=VISUAL PAGE
21A7          6149
21A7          6150   C----- AT THIS POINT WE KNOW THE COLUMNS/LINE ARE IN
21A7          6151   [AX] AND THE PAGE IF APPLICABLE IS IN [BH]. THE STACK
21A7          6152   HAS DS,AX,BX,CX,DX PUSHED. [AL] HAS VIDEO MODE
21A7          6153
21C0 8A CC          6154   MOV    CL,AH ; WILL MAKE USE OF [CX] REG TO
21C2 8A 2E 0484 R   6155   MOV    CH,ROWS ; CONTROL ROW & COLUMNS
21C6 C5 00          6156   INC    CH
21C8 E8 2220 R       6157   CALL   CHRLF ; CAR RETURN LINE FEED ROUTINE
21CB 51              6158   PUSH   CX ; SAVE SCREEN BOUNDS
21CC B4 03          6159   MOV    AH,3 ; WILL NOW READ THE CURSOR,
21CE CD 10          6160   INT    10H ; AND PRESERVE THE POSITION
21D0 29              6161   POP    CX ; RECALCULATE SCREEN BOUNDS
21D1 52              6162   PUSH   DX ; RECALL [BH]-VISUAL PAGE
21D2 33 D2          6163   XOR    DX,DX ; SET CURSOR POSITION TO [0,0]
21D2          6164
21D2          6165   C----- THE LOOP FROM PRI10 TO THE INSTRUCTION PRIOR TO PRI20
21D2          6166   ; IS THE LOOP TO READ EACH CURSOR POSITION FROM THE
21D2          6167   ; SCREEN AND PRINT.
21D2          6168
21D2          6169   PRI10: MOV    AH,2 ; TO INDICATE CURSOR SET REQUEST
21D2          6170   INT    10H ; NEW CURSOR POS ESTABLISHED
21D2          6171   MOV    AH,8 ; TO INDICATE READ CHARACTER
21D2          6172   INT    10H ; CHARACTER NOW IN [AL]
21D2          6173   OR    AL,AL ; SEE IF VALID CHAR
21D2          6174 C

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21DE 75 02          6175   C     JNZ    PRI15,           ; JUMP IF VALID CHAR
21E0 B0 20          6176   C     MOV    AL, ' ',        ; MAKE A BLANK
21E2 52             6177   C     PRI15:          ; INDICATE POSITION
21E3 32 00          6178   C     PUSH   DX,           ; TO INDICATE PRINT CHAR IN [AL]
21E5 32 E4          6179   C     XOR    AH,DX         ; PRINT THE CHARACTER
21E7 CD 17          6180   C     XOR    AH,AH         ; RECALL CURSOR POSITION
21E9 5A             6181   C     INT    17H           ; TEST FOR INTERRUPT ERROR
21E9 F0 00 29          6182   C     POP    DX,           ; JUMP IF ERROR DETECTED
21ED F5 21          6183   C     TEST   AL,029H       ; ADVANCE TO NEXT COLUMN
21EF FE 2E          6184   C     INC    DL,           ; SEE IF AT END OF LINE
21F1 3A CA          6185   C     CMP    CL,DL         ; BACK TO COLUMN 0
21F3 75 DF          6186   C     JNZ    PRI10,        ; SAVE NEW CURSOR POSITION
21F4 3C 02          6187   C     CMP    CH,DH         ; LINE FEED/CARRIAGE RETURN
21F7 8A E2          6188   C     MOV    AH,DL         ; RECALL CURSOR POSITION
21F9 8A E2          6189   C     MOV    AH,DL         ; ADVANCE TO NEXT LINE
21FA E8 2220 R      6190   C     PUSH   DX,           ; FINISHED?
21FB 24 00          6191   C     CALL   CRLF,        ; IF NOT CONTINUE
21FE 1E C6          6192   C     INT    0DH          ; BACK TO COLUMN 0
2200 3A EE          6193   C     INC    DH,           ; SAVE NEW CURSOR POSITION
2202 75 00          6194   C     CMP    CH,DH         ; LINE FEED/CARRIAGE RETURN
2202 75 00          6195   C     JNZ    PRI10,        ; RECALL CURSOR POSITION
2204 5A             6196   C     POP    DX,           ; TO INDICATE CURSOR SET REQUEST
2205 B4 02          6197   C     MOV    AH,2          ; CURSOR POSITION RESTORED
2207 CD 10          6199   C     INT    10H           ; INDICATE FINISHED
2208 C6 06 0500 R 00 6200   C     MOV    STATUS_BYTETO,0 ; EXIT THE ROUTINE
2208 EB 00          6201   C     JMP    SHORT_EXIT,    ; RECALL CURSOR POSITION
2210 5A             6202   C     ERR10:          ; TO REQUEST CURSOR SET
2211 B4 02          6203   C     POP    DX,           ; CURSOR POSITION RESTORED
2212 CD 10          6204   C     MOV    AH,2          ; INDICATE ERROR
2213 C6 06 0500 R FF 6205   C     INT    10H           ; GET CURSOR POSITION
2214 5A             6206   C     MOV    STATUS_BYTETO,0 ; TO REQUEST CURSOR SET
2215 5A             6207   C     EXIT:           ; CURSOR POSITION RESTORED
2216 59             6208   C     POP    DX,           ; INDICATE ERROR
2217 5B             6209   C     POP    CX,           ; RESTORE ALL THE REGISTERS USED
2218 5B             6210   C     POP    DX,           ; PRINT_SCREEN ENDP
2219 5B             6211   C     POP    AX,           ; WILL NOW SEND INITIAL CR, LF
221E 1F             6212   C     POP    DS,           ; TO PRINTER
221F CF             6213   C     IRET              ; SEND THE LINE FEED
2220 64             6214   C     PRINT_SCREEN,    ; NOW FOR THE CR
2221 64             6215   C     RET               ; LF
2222 64             6216   C     CARRIAGE_RETURN, ; SEND THE CARRIAGE RETURN
2223 64             6217   C     LINE_FEED,       ; -----
2224 33 D2          6218   C     CRLF,            ; -----
2225 32 E4          6219   C     PROC   NEAR,        ; -----
2226 64             6220   C     XOR    DX,DX         ; -----
2227 64             6221   C     XOR    AH,AH         ; -----
2228 B0 0D          6222   C     MOV    AL,0DH         ; -----
2229 7E 17          6223   C     INT    17H           ; -----
222A 32 E4          6224   C     XOR    AH,AH         ; -----
222B B0 0A          6225   C     MOV    AL,0AH         ; -----
222C CD 17          6226   C     INT    17H           ; -----
222E C3             6227   C     RET               ; -----
222F 64             6228   C     CRLF,            ; -----
222F 64             6229   C     ENDP              ; -----
222F 64             6230   C     SUBTTL,          ; -----
222F 64             6231   C     CODE   ENDS,        ; -----
222F 64             6232   C     END               ; -----
222F 64             6233   C     END                ; -----
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SUBTTL MONOCHROME CHARACTER GENERATOR  
CODE SEGMENT PUBLIC  
PUBLIC CMN  
CMN LABEL BYTE

0000 00 00 00 00 00 00	DB 000H,000H,000H,000H,000H,000H,000H ; TOP_HALF_00
0000 00 00 00 00 00 00	DB 000H,000H,000H,000H,000H,000H,000H ; BOTTOM_HALF_00
000E 00 00 7E 81 A5 81	DB 000H,000H,07EH,081H,045H,081H,081H,0BDH ; TH_01
0016 99 81 TE 00 00 00	DB 099H,081H,07EH,000H,000H,000H,000H ; BT_01
001C 00 00 7E FF DB FF	DB 000H,000H,07EH,0FFH,0BDH,0FFH,0FFH,0C3H ; TH_02
0024 67 FF TE 00 00 00	DB 0E7H,0FFH,07EH,000H,000H,000H,000H ; BT_02
002A 00 00 6C FE FE	DB 000H,000H,000H,06CH,0FEH,0FEH,0FEH,0FEH ; TH_03
0032 7C 38 10 00 00 00	DB 07CH,038H,010H,000H,000H,000H,000H ; BT_03
0038 00 00 10 38 7C	DB 000H,000H,000H,010H,038H,07CH,0FEH,07CH ; TH_04
0040 38 10 00 00 00 00	DB 038H,010H,000H,000H,000H,000H,000H ; BT_04
0040 00 00 18 3C E7	DB 000H,000H,016H,03CH,03CH,03CH,0E7H,0E7H,0E7H ; TH_05
004E 18 3C 00 00 00 00	DB 016H,018H,03CH,000H,000H,000H,000H ; BT_05
0054 00 00 18 3C 7E FF	DB 000H,000H,018H,03CH,07EH,0FFH,0FFH,0FFH ; TH_06
005C 18 18 3C 00 00 00	DB 018H,018H,03CH,000H,000H,000H,000H ; BT_06
0062 00 00 00 00 18 26	DB 000H,000H,000H,000H,018H,03CH,03CH ; TH_07
0C 3C 00 00 00 00 00	DB 018H,000H,000H,000H,000H,000H,000H ; BT_07
006A 18 00 00 00 00 30	DB 018H,000H,000H,000H,000H,000H,000H ; TH_08
0070 FF FF FF FF E7	DB 0E7H,0FFH,0FFH,0FFH,0E7H,0C3H,0C3H ; TH_08
0078 67 FF FF FF FF FF	DB 0E7H,0FFH,0FH,0FFH,0FH,0FFH,0FH,0FFH ; TH_08
007E 00 00 00 00 3C 66	DB 000H,000H,000H,03CH,066H,042H,042H ; TH_09
0086 66 3C 00 00 00 00	DB 0E6H,03CH,000H,000H,000H,000H,000H ; TH_09
008C 66 FF FF C3 99	DB 0E7H,0FFH,0FFH,0FFH,0C3H,099H,0BDH,0BDH ; TH_0A
0094 99 C3 FF FF FF	DB 099H,0C3H,0FFH,0FFH,0FFH,0FFH,0FFH ; BT_0A
009A 00 00 1E 0E 1A 32	DB 000H,000H,01EH,00EH,01AH,032H,078H,0C3H ; TH_0B
0042 CC CC 78 00 00 00	DB 0CCH,0CCH,078H,000H,000H,000H,000H ; BT_0B
0048 00 00 3C 66 66 66	DB 000H,000H,03CH,066H,066H,066H,03CH,018H ; TH_0C
0080 7C 18 00 00 00 00	DB 0E7H,018H,018H,000H,000H,000H,000H ; BT_0C
0086 7C 33 3F 33 3F 30	DB 000H,000H,03FH,033H,03FH,030H,030H ; TH_0D
008B 70 F0 E0 00 00 00	DB 070H,0F0H,0E0H,000H,000H,000H,000H ; BT_0D
00C4 00 00 7F 63 7F 63	DB 000H,000H,07FH,063H,07FH,063H,063H ; TH_0E
00C6 67 E7 E6 CO 00 00	DB 0E7H,0E7H,0E6H,0C0H,000H,000H,000H ; BT_0E
00D2 00 00 18 1B 3D C3	DB 000H,000H,018H,0D8H,0B3H,03CH,0E7H,03CH ; TH_0F
00DA D6 18 18 00 00 00	DB 0DBH,018H,018H,000H,000H,000H,000H ; BT_0F
00E0 00 00 80 CO EO F8	DB 000H,000H,080H,0C0H,0E0H,0F8H,0F8H ; TH_10
00E8 EO CO 80 00 00 00	DB 0E0H,0C0H,080H,000H,000H,000H,000H ; BT_10
00EE 00 02 06 0E 3E 59	DB 000H,000H,002H,006H,0E0H,03EH,03EH ; TH_11
00F6 0E 06 02 00 00 00	DB 0E0H,006H,002H,000H,000H,000H,000H ; BT_11
00FC 00 00 18 3C 7E 18	DB 000H,000H,018H,03CH,07EH,018H,018H,018H ; TH_12
0104 7E 3C 18 00 00 00	DB 07EH,03CH,018H,000H,000H,000H,000H ; BT_12
010A 00 00 66 66 66 66	DB 000H,000H,066H,066H,066H,066H,066H,066H ; TH_13

0112	00 66 66 00 00 00	66	DB	000H, 066H, 066H, 000H, 000H, 000H	; BT_13
0118	00 00 7F DB DB DB	67	DB	000H, 000H, 07FH, 0DBH, 0DBH, 0DBH, 018H	; TH_14
	7B 1B	68			
0120	00 7C C6 00 38 6C	69	DB	018H, 018H, 018H, 000H, 000H, 000H	; BT_14
0126	C6 00 38 6C 71	70	DB	000H, 07CH, 0C6H, 060H, 038H, 05CH, 0C6H, 0C6H	; TH_15
012E	6C 38 00 C6 70	72	DB	06CH, 038H, 0C6H, 0C6H, 07CH, 000H	; BT_15
0134	00 00 00 00 00 00	73	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_16
013C	FE FE FE 00 00 00	75	DB	0FEH, 0FEH, 0FEH, 000H, 000H, 000H	; BT_16
0142	00 00 1E 3C 7E 18	76	DB	000H, 000H, 018H, 03CH, 07EH, 018H, 018H, 018H	; TH_17
014A	18 1B 00 00 00 00	78			
0150	00 00 18 3C 7E 18	79	DB	07EH, 03CH, 018H, 07EH, 000H, 000H	; BT_17
	18 1B	81	DB	000H, 000H, 018H, 03CH, 07EH, 018H, 018H	; TH_18
0158	18 1B 00 00 00 00	82	DB	018H, 018H, 018H, 000H, 000H, 000H	; BT_18
015E	00 00 18 1B 18	83	DB	000H, 000H, 018H, 018H, 018H, 018H, 018H	; TH_19
0166	7E 3C 18 00 00 00	85	DB	07EH, 03CH, 018H, 000H, 000H, 000H	; BT_19
016C	00 00 00 18 OC	86	DB	000H, 000H, 000H, 000H, 018H, 00CH, 0FEH, 00CH	; TH_1A
	FE OC	87			
0174	00 00 00 00 00 00	88	DB	018H, 000H, 000H, 000H, 000H, 000H	; BT_1A
017A	00 00 30 30 60 60	89	DB	000H, 000H, 000H, 030H, 060H, 0FEH, 060H	; TH_1B
	FF 60	90			
0182	30 00 00 00 00 00	91	DB	030H, 000H, 000H, 000H, 000H, 000H	; BT_1B
0184	00 00 00 CO 92	92	DB	000H, 000H, 000H, 000H, 0COH, 0COH, 0COH	; TH_1C
	CO DC	93			
0190	00 00 00 00 00 00	94	DB	0FEH, 000H, 000H, 000H, 000H, 000H	; BT_1C
0196	00 00 00 28 6C	95	DB	000H, 000H, 000H, 028H, 05CH, 0FEH, 06CH	; TH_1D
0198	28 00 00 00 00 00	97	DB	028H, 000H, 000H, 000H, 000H, 000H	; BT_1D
01A4	28 00 00 18 38 3B	98	DB	000H, 000H, 000H, 010H, 038H, 038H, 07CH, 07CH	; TH_1E
	7C 7C	99			
01AC	FE FE 00 00 00 00	100	DB	0FEH, 0FEH, 000H, 000H, 000H, 000H	; BT_1E
01B1	00 00 FE FF 7C	101	DB	000H, 000H, 0FEH, 0FEH, 07CH, 07CH, 038H	; TH_1F
01BA	38 10 00 00 00 00	103	DB	038H, 010H, 000H, 000H, 000H, 000H	; BT_1F
	10 10	104			
01C0	00 00 00 00 00 00	105	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_20 SP
01C8	00 00 00 00 00 00	106	DB	000H, 000H, 000H, 000H, 000H, 000H	; BT_20 SP
01CE	00 18 3C 3C 3C	108	DB	000H, 000H, 018H, 03CH, 03CH, 03CH, 018H	; TH_21 !
	18 18	109			
01DE	08 18 00 00 00 00	110	DB	000H, 018H, 018H, 000H, 000H, 000H	; BT_21 !
01DC	08 66 66 24 00	110	DB	000H, 066H, 066H, 024H, 000H, 000H, 000H	; TH_22 !
	00 00	112			
01E4	00 00 00 00 00 00	113	DB	000H, 000H, 000H, 000H, 000H, 000H	; BT_22 "
01EA	00 00 00 6C FE 6C	114	DB	000H, 000H, 06CH, 06CH, 06CH, 06CH, 06CH	; TH_23 #
	6C 6C	115			
01F2	0E 6C 00 00 00 00	116	DB	0FEH, 06CH, 06CH, 000H, 000H, 000H	; BT_23 #
01F8	18 18 7C C6 C2 CO	117	DB	018H, 018H, 07CH, 0C6H, 0C2H, 0COH, 07CH, 006H	; TH_24 S
	7C 06	118			
0200	86 C6 7C 18 18 00	119	DB	086H, 06CH, 07CH, 018H, 018H, 000H	; BT_24 S
0206	00 00 00 C2 C6 10	120	DB	000H, 000H, 000H, 02CH, 0C6H, 00CH, 018H	; TH_25 %
	0C 18	121			
020E	30 66 C6 00 00 00	122	DB	030H, 066H, 066H, 000H, 000H, 000H	; BT_25 %'
0214	00 38 6C 6C 38	123	DB	000H, 038H, 06CH, 06CH, 038H, 076H, 00CH	; TH_26 &
	DC DC	125			
021C	CC 7C 76 00 00 00	125	DB	0CCH, 0CCN, 076H, 000H, 000H, 000H	; BT_26 &
0222	00 30 30 30 60 60	126	DB	000H, 030H, 030H, 030H, 060H, 000H, 000H, 000H	; TH_27 ?
	00 00	127			
022A	00 00 00 00 00 00	128	DB	000H, 000H, 000H, 000H, 000H, 000H	; BT_27 !
023A	00 18 30 30 30 30	129	DB	000H, 000H, 0OCH, 018H, 030H, 030H, 030H, 030H	; TH_28 {
	30 30	130			
0238	30 18 00 00 00 00	131	DB	030H, 018H, 00CH, 000H, 000H, 000H	; BT_28 {
023E	00 30 18 00 00 00	132	DB	000H, 030H, 018H, 00CH, 00CH, 00CH, 00CH	; TH_29 }
	18 18	133			
0246	18 30 00 00 00 00	134	DB	00CH, 018H, 030H, 000H, 000H, 000H	; BT_29 }
024C	00 00 00 66 3C	135	DB	000H, 000H, 000H, 000H, 066H, 03CH, OFFH, 03CH	; TH_2A *
	FF 3C	136			
0254	66 00 00 00 00 00	137	DB	066H, 000H, 000H, 000H, 000H, 000H	; BT_2A *
025A	00 00 00 18 18 18	138	DB	000H, 000H, 000H, 018H, 018H, 07EH, 018H	; TH_2B +
	18 18	139			
0262	18 00 00 00 00 00	140	DB	018H, 000H, 000H, 000H, 000H, 000H	; BT_2B +
0266	00 00 00 00 00 00	141	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H	; TH_2C ,
	00 00	142			
0270	18 18 18 30 00 00	143	DB	018H, 018H, 018H, 030H, 000H, 000H	; BT_2C ,
0276	00 00 00 00 00 00	144	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H	; TH_2D -
	FE 00	145			
027E	00 00 00 00 00 00	146	DB	000H, 000H, 000H, 000H, 000H, 000H	; BT_2D -
0284	00 00 00 00 00 00	147	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H	; TH_2E .
	00 00	148			
028C	00 18 18 00 00 00	149	DB	000H, 018H, 018H, 000H, 000H, 000H	; BT_2E .
0292	00 00 00 06 18 18	150	DB	000H, 000H, 002H, 006H, 00CH, 018H, 030H, 060H	; TH_2F /
	18 18	151			
029A	00 00 00 00 00 00	152	DB	0COH, 080W, 000H, 000H, 000H, 000H	; BT_2F /
	00 00	153			
02A0	00 00 7C C6 CE DE	154	DB	000H, 000H, 07CH, 0C6H, 0CEH, 0DEH, 0F6H, 0E6H	; TH_30 0
02A8	06 C6 7C 00 00 00	155	DB	0C6H, 0C6H, 07CH, 000H, 000H, 000H	; BT_30 0
02AE	00 00 18 38 78 18	157	DB	000H, 000H, 018H, 038H, 078H, 018H, 018H, 018H	; TH_31 1
	18 18	158			
02B6	18 7E 00 00 00 00	159	DB	018H, 018H, 07EH, 000H, 000H, 000H	; BT_31 1
02BC	18 7C C6 06 00 00	160	DB	000H, 000H, 07CH, 0C6H, 006H, 00CH, 018H, 030H	; TH_32 2
	18 30	161			
02C4	60 C6 FE 00 00 00	162	DB	060H, 0C6H, 0FEH, 000H, 000H, 000H	; BT_32 2
02CA	00 00 7C C6 06 06	163	DB	000H, 000H, 07CH, 0C6H, 006H, 006H, 03CH, 006H	; TH_33 3
	30 30	164			
02D2	06 C6 7C 00 00 00	165	DB	000H, 0C6H, 07CH, 000H, 000H, 000H	; BT_33 3
02D8	00 00 00 1C 3C 6C	166	DB	000H, 000H, 00CH, 01CH, 03CH, 05CH, 0CCH, 0FEH	; TH_34 4
	CC FE	167			
02E0	05 0C 1E 00 00 00	168	DB	00CH, 0C6H, 01EH, 000H, 000H, 000H	; BT_34 4
02E6	00 FE CO CO 00	169	DB	000H, 000H, 0FEH, 00CH, 0COH, 0COH, 0FCN, 006H	; TH_35 5
	FC 00	170			
02E8	06 C6 7C 00 00 00	171	DB	006H, 0C6H, 07CH, 000H, 000H, 000H	; BT_35 5
02F4	00 30 60 CO CO	172	DB	000H, 000H, 038H, 060H, 0COH, 0COH, 0FCN, 0C6H	; TH_36 6
	00 00	173			
02FC	C6 C6 7C 00 00 00	174	DB	0C6H, 0C6H, 07CH, 000H, 000H, 000H	; BT_36 6
0302	00 00 FE C6 00 0C	175	DB	000H, 000H, 0FEH, 0C6H, 006H, 00CH, 018H, 030H	; TH_37 7
	18 30	176			
030A	30 30 00 00 00 00	177	DB	030H, 030H, 030H, 000H, 000H, 000H	; BT_37 7
0310	00 00 7C C6 C6 00	178	DB	000H, 000H, 07CH, 0C6H, 0C6H, 0C6H, 07CH, 0C6H	; TH_38 8
	7C C6	179			
0318	C6 C6 7C 00 00 00	180	DB	0C6H, 0C6H, 07CH, 000H, 000H, 000H	; BT_38 8
031E	06 00 00 C6 C6 C6	181	DB	000H, 000H, 07CH, 0C6H, 0C6H, 0C6H, 07EH, 006H	; TH_39 9
	FE FE	182			
0326	06 C6 7C 00 00 00	183	DB	000H, 000H, 0C6H, 006H, 00CH, 018H, 000H, 000H	; BT_39 9
	00 00	184			
032C	06 00 00 18 18 00	185	DB	000H, 000H, 07CH, 0C6H, 0C6H, 0C6H, 07EH, 006H	; TH_3A :
	00 00	186			
0334	18 00 00 00 00 00	186	DB	018H, 018H, 000H, 000H, 000H, 000H	; BT_3A :
033A	00 00 18 18 18 00	187	DB	000H, 000H, 000H, 018H, 018H, 000H, 000H, 000H	; TH_3B :
	00 00	188			
0342	18 18 30 00 00 00	189	DB	018H, 018H, 030H, 000H, 000H, 000H	; BT_3B :
0348	00 00 06 00 18 30	190	DB	000H, 000H, 006H, 00CH, 018H, 030H, 030H	; TH_3C <
	60 30	191			

0350	18	0C	06	00	00	00	192	DB	018H, 0OCH, 006H, 000H, 000H, 000H ; BT_3C <
0356	00	00	00	00	00	7E	193	DB	000H, 0OCH, 006H, 000H, 000H, 000H, 000H ; TH_3D =
035E	00	00	00	00	00	00	194	DB	07EH, 000H, 000H, 000H, 000H, 000H ; BT_3D =
0364	00	00	60	30	18	0C	196	DB	000H, 0OCH, 060H, 030H, 018H, 0OCH, 006H, 00CH ; TH_3E >
036C	18	30	60	00	00	00	197	DB	018H, 030H, 060H, 000H, 000H, 000H ; BT_3E >
0372	00	00	7C	C6	0C	0C	199	DB	000H, 0OCH, 07CH, 0C6H, 06CH, 0OCH, 018H, 018H ; TH_3F =
037A	18	18	00	00	00	00	201	DB	000H, 018H, 018H, 000H, 000H, 000H ; BT_3F ?
0380	00	00	7C	C6	C6	DE	202	DB	000H, 0OCH, 07CH, 0C6H, 0C6H, 0DEH, 0OEH, 0DEH ; TH_40 @
0388	0C	C0	00	7C	00	00	205	DB	0DCH, 0OCH, 07CH, 000H, 000H, 000H ; BT_40 @
038E	00	00	10	38	6C	C6	206	DB	000H, 0OCH, 010H, 038H, 0C6H, 0C6H, 0FEH ; TH_41 A
0396	C6	C6	00	00	00	00	207	DB	0C6H, 0C6H, 0C6H, 000H, 000H, 000H ; BT_41 A
039C	00	00	7C	66	66	66	209	DB	000H, 0OCH, 07CH, 066H, 066H, 066H, 07CH, 066H ; TH_42 B
03A4	66	66	FC	00	00	00	211	DB	066H, 066H, 0FCH, 000H, 000H, 000H ; BT_42 B
03AA	00	00	30	66	62	C0	212	DB	000H, 0OCH, 030H, 066H, 0C2H, 0C0H, 0C0H, 0C0H ; TH_43 C
03B2	00	00	7C	00	00	00	214	DB	0C2H, 066H, 03CH, 000H, 000H, 000H ; BT_43 C
03B8	00	00	F8	6C	66	66	215	DB	000H, 0OCH, 0F8H, 06CH, 066H, 066H, 066H, 066H ; TH_44 D
03C0	66	66	FC	00	00	00	217	DB	066H, 06CH, 0F8H, 000H, 000H, 000H ; BT_44 D
03C6	00	00	7E	66	62	68	218	DB	000H, 0OCH, 07EH, 066H, 062H, 068H, 078H, 068H ; TH_45 E
03CE	62	66	FE	00	00	00	220	DB	062H, 066H, 0FEH, 000H, 000H, 000H ; BT_45 E
03DE	00	00	FE	66	62	68	221	DB	000H, 0OCH, 0FEH, 066H, 062H, 068H, 078H, 068H ; TH_46 F
03DC	60	60	FO	00	00	00	223	DB	060H, 060H, 0FOH, 000H, 000H, 000H ; BT_46 F
03E2	00	00	30	66	C2	C0	224	DB	000H, 0OCH, 030H, 066H, 0C2H, 0C0H, 0C0H, 0C0H ; TH_47 G
03EA	CO	DE	00	00	00	00	225	DB	0C6H, 066H, 03AH, 000H, 000H, 000H ; BT_47 G
03FO	00	00	3A	00	00	00	226	DB	000H, 0OCH, 03AH, 066H, 0C6H, 0C6H, 0FEH, 0C6H ; TH_48 H
03FB	FE	C6	00	00	00	00	227	DB	0C6H, 066H, 03CH, 000H, 000H, 000H ; BT_48 H
03FE	00	00	3C	18	18	18	229	DB	000H, 0OCH, 03CH, 018H, 018H, 018H, 018H, 018H ; TH_49 I
0406	18	18	3C	00	00	00	232	DB	018H, 018H, 03CH, 000H, 000H, 000H ; BT_49 I
0406	00	00	1E	OC	OC	OC	233	DB	000H, 0OCH, 01EH, 0OCH, 0OCH, 0OCH, 0OCH, 0OCH ; TH_4A J
0414	CC	OC	00	00	00	00	234	DB	0CCH, 0CCH, 078H, 000H, 000H, 000H ; BT_4A J
041A	CC	78	00	00	00	00	235	DB	000H, 0OCH, 0E6H, 066H, 0C6H, 0C6H, 078H, 0C6H ; TH_4B K
0422	6C	66	E6	00	00	00	238	DB	06CH, 066H, 0E6H, 000H, 000H, 000H ; BT_4B K
0428	00	00	F0	60	60	60	239	DB	000H, 0OCH, 0FOH, 060H, 060H, 060H, 060H, 060H ; TH_4C L
0430	62	66	FE	00	00	00	241	DB	062H, 066H, 0FEH, 000H, 000H, 000H ; BT_4C L
0436	00	00	66	FE	FE	FE	242	DB	000H, 0OCH, 066H, 0FEH, 0FEH, 066H, 0C6H ; TH_4D M
043E	00	00	C6	00	00	00	244	DB	0C6H, 0C6H, 0C6H, 000H, 000H, 000H ; BT_4D M
0444	00	00	C6	E6	FE	FE	245	DB	000H, 0OCH, 0C6H, 0E6H, 0F6H, 0FEH, 0DEH, 0C6H ; TH_4E N
044C	C6	C6	56	00	00	00	247	DB	0C6H, 0C6H, 0C6H, 000H, 000H, 000H ; BT_4E N
0452	00	00	38	6C	C8	C8	248	DB	000H, 0OCH, 038H, 06CH, 0C6H, 0C6H, 0C6H, 0C6H ; TH_4F O
045A	C6	GC	38	00	00	00	250	DB	0C6H, 06CH, 038H, 000H, 000H, 000H ; TH_4F O
0460	00	00	FC	66	66	66	251	DB	000H, 0OCH, 0FC, 066H, 066H, 066H, 07CH, 060H ; TH_50 P
0468	60	60	FO	00	00	00	254	DB	060H, 060H, 0FOH, 000H, 000H, 000H ; BT_50 P
046E	00	00	7C	C6	C6	C6	255	DB	000H, 0OCH, 07CH, 0C6H, 0C6H, 0C6H, 0C6H, 0D6H ; TH_51 Q
0476	00	00	FC	66	66	66	257	DB	00EH, 07CH, 00CH, 00EH, 000H, 000H ; BT_51 Q
047C	00	00	FC	66	66	66	259	DB	000H, 0OCH, 0FC, 066H, 066H, 066H, 07CH, 06CH ; TH_52 R
0484	66	66	E6	00	00	00	260	DB	066H, 066H, 0E6H, 000H, 000H, 000H ; BT_52 R
048A	00	00	7C	C6	C6	60	261	DB	000H, 0OCH, 07CH, 0C6H, 0C6H, 060H, 038H, 00CH ; TH_53 S
0492	C6	7C	00	00	00	00	263	DB	0C6H, 0C6H, 07CH, 000H, 000H, 000H ; BT_53 S
0498	00	00	7E	7C	5A	18	264	DB	000H, 0OCH, 07EH, 07EH, 05AH, 018H, 018H, 018H ; TH_54 T
04A0	00	00	3C	00	00	00	265	DB	018H, 018H, 03CH, 000H, 000H, 000H ; BT_54 T
04A6	00	00	C6	C6	C6	C6	267	DB	000H, 0OCH, 0C6H, 0C6H, 0C6H, 0C6H, 0C6H, 0C6H ; TH_55 U
04AE	C6	C6	7C	00	00	00	268	DB	0C6H, 0C6H, 07CH, 000H, 000H, 000H ; BT_55 U
04B4	00	00	C6	C6	C6	C6	269	DB	000H, 0OCH, 0C6H, 0C6H, 0C6H, 0C6H, 0C6H, 0C6H ; TH_56 V
04BC	38	10	00	00	00	00	272	DB	06CH, 038H, 010H, 000H, 000H, 000H ; BT_56 V
04C2	00	00	66	FE	C6	C6	273	DB	000H, 0OCH, 066H, 0C6H, 0C6H, 0D6H, 0D6H ; TH_57 W
04CA	00	00	6C	00	00	00	275	DB	0FEH, 07CH, 06CH, 000H, 000H, 000H ; BT_57 W
04D0	00	00	C6	C6	38	38	276	DB	000H, 0OCH, 0C6H, 0C6H, 0C6H, 038H, 038H ; TH_58 X
04D8	6C	C6	66	00	00	00	277	DB	06CH, 0C6H, 0C6H, 000H, 000H, 000H ; BT_58 X
04E0	00	00	66	66	66	66	278	DB	000H, 0OCH, 066H, 066H, 066H, 066H, 03CH, 018H ; TH_59 Y
04E6	18	18	3C	00	00	00	280	DB	018H, 018H, 03CH, 000H, 000H, 000H ; BT_59 Y
04EC	00	00	FE	C6	8C	18	281	DB	000H, 0OCH, 0FEH, 0C6H, 08CH, 018H, 030H, 060H ; TH_5A Z
04F4	00	00	32	FE	00	00	283	DB	0C2H, 0C6H, 0FEH, 000H, 000H, 000H ; BT_5A Z
04FA	00	00	3C	30	30	30	285	DB	000H, 0OCH, 03CH, 030H, 030H, 030H, 030H, 030H ; TH_5B [
0502	30	30	3C	00	00	00	287	DB	030H, 030H, 03CH, 000H, 000H, 000H ; BT_5B [
0508	00	00	80	EO	70	70	288	DB	000H, 0OCH, 080H, 0C0H, 0E0H, 070H, 038H, 01CH ; TH_5C \
0510	0E	06	02	00	00	00	290	DB	00EH, 006H, 002H, 000H, 000H, 000H ; BT_5C \
0516	00	00	3C	0C	0C	0C	291	DB	000H, 0OCH, 03CH, 00CH, 00CH, 00CH, 00CH, 00CH ; TH_5D ]
051E	00	00	3C	00	00	00	292	DB	00CH, 0OCH, 03CH, 000N, 000H, 000H ; BT_5D ]
0524	10	38	6C	00	00	00	294	DB	010H, 038H, 06CH, 0C6H, 000H, 000H, 000H, 000H ; TH_5E ]
052C	00	00	00	00	00	00	295	DB	000H, 0OCH, 000H, 000H, 000H, 000H, 000H, 000H ; BT_5E ]
0532	00	00	00	00	00	00	297	DB	000H, 0OCH, 000H, 000H, 000H, 000H, 000H, 000H ; TH_5F -
053A	00	00	00	FF	FF	00	298	DB	000H, 0OCH, 000H, 000H, 0FFH, 000H ; BT_5F -
0540	30	30	18	00	00	00	300	DB	030H, 030H, 018H, 000H, 000H, 000H, 000H, 000H ; TH_60 !
0548	00	00	00	00	00	00	302	DB	000H, 0OCH, 000H, 000H, 000H, 000H, 000H, 000H ; TH_60 !
054E	00	00	00	00	00	78	303	DB	000H, 0OCH, 000H, 000H, 000H, 000H, 000H, 000H ; TH_61 LOWER_CASE A
0556	00	00	7C	7C	00	00	305	DB	0CCH, 0CCH, 07CH, 000H, 000H, 000H ; BT_61 LOWER_CASE A
0556	00	00	60	60	78	307	DB	000H, 0OCH, 060H, 060H, 060H, 078H, 0C6H, 066H ; TH_62 L.C. B	
0564	66	66	7C	00	00	00	309	DB	066H, 066H, 07CH, 000H, 000H, 000H ; BT_62 L.C. B
056A	00	00	60	00	00	7C	310	DB	000H, 0OCH, 060H, 000H, 000H, 000H, 000H, 000H ; TH_63 L.C. C
0572	00	00	C6	7C	00	00	312	DB	0C6H, 0C6H, 07CH, 000H, 000H, 000H ; BT_63 L.C. C
0578	00	00	00	1C	00	0C	313	DB	000H, 0OCH, 000H, 0C6H, 00CH, 03CH, 06CH, 0CCH ; TH_64 L.C. D
0580	00	00	00	00	00	7C	314	DB	0CCH, 0CCH, 07CH, 000H, 000H, 000H ; BT_64 L.C. D
0586	00	00	00	00	00	7C	315	DB	000H, 0OCH, 000H, 000H, 000H, 000H, 000H, 000H ; TH_65 L.C. E
	C6	FE					317	DB	

058E	CD	C6	7C	00	00	00	318	DB	0C00,0C6H,07CH,000H,000H,000H,000H,000H ; BT_65 L.C. E
0594	F0	60	38	6C	64	00	320	DB	000H,000H,038H,06CH,064H,060H,0F0H,060H ; TH_66 L.C. F
059C	60	F0	00	00	00	00	321	DB	060H,060H,0F0H,000H,000H,000H ; BT_66 L.C. F
05A2	CC	CC	00	00	00	00	322	DB	000H,000H,000H,000H,000H,000H,076H,0CCH,0CCH ; TH_67 L.C. G
05AA	00	00	0C	CC	78	00	323	DB	0CCH,07CH,000H,0CCH,078H,000H ; BT_67 L.C. G
05B0	00	00	E0	60	60	6C	325	DB	000H,000H,0E0H,060H,060H,06CH,076H,066H ; TH_68 L.C. H
05B6	76	66	66	00	00	00	326	DB	066H,066H,0E6H,000H,000H,000H ; BT_68 L.C. H
05B8	66	E6	00	00	00	00	327	DB	000H,000H,018H,018H,000H,038H,018H,018H ; TH_69 L.C. I
05C1	00	00	18	18	00	38	328	DB	018H,018H,03CH,000H,000H,000H ; BT_70 L.C. I
05C6	18	18	3C	00	00	00	329	DB	000H,000H,000H,000H,000H,000H,000H,000H ; BT_71 L.C. J
05CC	00	00	06	00	0E	0E	331	DB	000H,000H,000H,000H,000H,000H,000H,000H ; BT_72 L.C. J
05D4	06	06	00	00	00	00	332	DB	006H,006H,066H,066H,03CH,000H ; BT_74 L.C. K
05DA	00	00	E0	60	60	6C	334	DB	000H,000H,0E0H,060H,060H,066H,06CH,078H ; TH_75 L.C. K
05E2	6C	78	00	00	00	00	335	DB	06CH,066H,066H,000H,000H,000H ; BT_76 L.C. K
05E8	00	00	38	18	18	38	336	DB	000H,000H,038H,018H,018H,000H,018H,018H ; TH_76 L.C. L
05F0	18	18	3C	00	00	00	339	DB	018H,018H,03CH,000H,000H,000H ; BT_76 L.C. L
05F6	00	00	00	00	00	EC	340	DB	000H,000H,000H,000H,000H,000H,000H,000H ; TH_76 L.C. M
05FE	FE	D6	00	00	00	00	341	DB	0D6H,0D6H,0C6H,0D6H,000H,000H ; BT_76 L.C. M
060N	00	00	C6	00	00	00	DC	DB	000H,000H,000H,000H,000H,000H,000H,000H ; TH_76 L.C. N
060C	66	66	66	00	00	00	345	DB	066H,066H,066H,000H,000H,000H ; BT_76 L.C. N
0612	00	00	00	00	00	7C	346	DB	000H,000H,000H,000H,000H,000H,07CH,0C6H,0C6H ; TH_76 L.C. O
061A	C5	C6	00	00	00	00	348	DB	0C6H,0C6H,07CH,000H,000H,000H ; BT_76 L.C. O
0620	00	00	00	00	00	DC	349	DB	000H,000H,000H,000H,000H,000H,000H,000H ; TH_76 L.C. P
0628	66	7C	60	60	60	FO	350	DB	066H,07CH,060H,060H,0F0H,000H ; BT_77 L.C. Q
062E	00	00	00	00	00	76	352	DB	000H,000H,000H,000H,000H,000H,076H,0CCH,0CCH ; TH_77 L.C. Q
0636	CC	7C	00	00	01	EE	355	DB	0CCH,07CH,000H,0CCH,01EH,000H ; BT_77 L.C. Q
063C	00	00	00	00	00	DC	357	DB	000H,000H,000H,000H,000H,000H,000H,000H ; TH_77 L.C. R
063D	76	66	00	00	00	00	357	DB	0CCH,07CH,000H,0CCH,01EH,000H ; BT_77 L.C. R
0644	60	60	FO	00	00	00	358	DB	060H,060H,0F0H,000H,000H,000H ; BT_77 L.C. S
064A	00	00	00	00	00	7C	359	DB	000H,000H,000H,000H,000H,000H,07CH,0C6H,070H ; TH_77 L.C. S
0652	C5	C6	00	00	00	00	361	DB	01CH,0C6H,07CH,000H,000H,000H ; BT_77 L.C. T
0658	00	00	10	30	30	FC	362	DB	000H,000H,010H,030H,030H,0FCH,030H,030H ; TH_77 L.C. T
0660	30	30	1C	00	00	00	363	DB	030H,036H,01CH,000H,000H,000H ; BT_77 L.C. T
0666	00	00	00	00	00	CC	364	DB	000H,000H,000H,000H,000H,000H,0CCH,0CCH,0CCH ; TH_77 L.C. U
066E	CC	CC	00	00	00	00	366	DB	0CCH,0CCH,076H,000H,000H,000H ; BT_77 L.C. U
0674	00	00	00	00	00	66	368	DB	000H,000H,000H,000H,000H,000H,066H,066H,066H ; TH_77 L.C. V
067C	66	3C	18	00	00	00	370	DB	066H,03CH,018H,000H,000H,000H ; BT_77 L.C. V
0682	00	00	00	00	00	C6	371	DB	000H,000H,000H,000H,000H,000H,0C6H,0C6H,006H ; TH_77 L.C. W
068A	C5	06	00	00	00	00	372	DB	066H,0FCH,06CH,000H,000H,000H ; BT_77 L.C. W
0690	00	00	00	00	00	C6	374	DB	000H,000H,000H,000H,000H,000H,038H,038H,038H ; TH_78 L.C. X
0696	6C	38	00	00	00	00	375	DB	038H,0C6H,0C6H,000H,000H,000H ; BT_78 L.C. X
0698	38	6C	00	00	00	00	376	DB	000H,000H,000H,000H,000H,000H,0C6H,0C6H,0C6H ; TH_78 L.C. X
069E	00	00	00	00	00	C6	377	DB	0CCH,0CCH,076H,000H,000H,000H ; BT_78 L.C. Y
06A6	C6	7E	06	0C	F8	00	379	DB	06CH,07EH,000H,000H,0F8H,000H ; BT_78 L.C. Z
06AC	00	00	00	00	00	FE	380	DB	000H,000H,000H,000H,000H,000H,0FCH,0CCH,018H ; BT_78 L.C. Z
06B4	CC	18	00	00	00	00	381	DB	030H,046H,0FCH,000H,000H,000H ; BT_78 L.C. Z
06BA	00	00	0E	18	18	18	382	DB	000H,000H,01CH,018H,018H,018H,018H,018H,018H ; BT_78 L.BRAK
06C2	18	18	0E	00	00	00	385	DB	018H,018H,0E0H,000H,000H,000H ; BT_78 L.BRAK
06C6	00	00	18	18	18	18	386	DB	000H,000H,018H,018H,018H,018H,018H,018H,018H ; TH_7C 1
06D0	18	18	00	00	00	00	388	DB	018H,018H,018H,000H,000H,000H ; BT_7C 1
06D6	00	00	70	18	18	18	389	DB	000H,000H,070H,018H,018H,018H,018H,018H,018H ; TH_7D R BRAK
06E1	00	00	00	70	00	00	390	DB	018H,018H,070H,000H,000H,000H ; BT_7D R BRAK
06E4	00	00	76	00	00	00	392	DB	000H,000H,076H,000H,000H,000H ; TH_7E TILDE
06E6	00	00	00	00	00	00	393	DB	000H,000H,000H,000H,000H,000H ; BT_7F DELTA
06E6	00	00	00	00	00	00	394	DB	0C6H,0FCH,000H,000H,000H,000H ; BT_7F DELTA
06F2	00	00	00	00	00	10	38	DB	000H,000H,000H,000H,000H,000H,000H,000H,000H ; TH_7F DELTA
06FA	C6	00	00	00	00	00	397	DB	000H,000H,000H,000H,000H,000H,000H,000H,000H ; TH_7F DELTA
0700	00	00	3C	66	CC	00	398	DB	000H,000H,03CH,066H,0C2H,0C0H,0C0H,0C2H ; TH_80
0708	66	3C	00	06	7C	00	401	DB	066H,03CH,000H,000H,000H,000H,000H,000H ; TH_80
070E	00	00	CC	00	CC	00	402	DB	000H,000H,0CCH,0CCH,000H,000H,0CCH,0CCH,0CCH ; TH_81
0716	CC	7C	00	00	00	00	403	DB	0CCH,0CCH,076H,000H,000H,000H ; BT_81
071C	00	00	18	30	30	7C	404	DB	000H,000H,018H,030H,030H,000H,07CH,0C6H,0FEN ; TH_82
0724	C5	C6	7C	00	00	00	407	DB	0CCH,0C6H,07CH,000H,000H,000H ; BT_82
072A	00	10	38	6C	00	78	408	DB	000H,000H,010H,038H,06CH,000H,078H,0CCH,07CH ; TH_83
0732	CC	CC	7C	00	00	00	410	DB	0CCH,0CCH,076H,000H,000H,000H ; BT_83
0738	00	00	CC	00	CC	7C	411	DB	000H,000H,0CCH,0CCH,000H,000H,078H,0CCH,07CH ; TH_84
0740	CC	7C	00	00	00	00	412	DB	0CCH,0CCH,076H,000H,000H,000H ; BT_84
0746	00	00	30	18	00	78	413	DB	000H,000H,030H,018H,06CH,000H,078H,0CCH,07CH ; TH_85
074E	CC	7C	00	00	00	00	415	DB	0CCH,0CCH,076H,000H,000H,000H ; BT_85
0754	00	00	38	3C	00	78	417	DB	000H,000H,038H,03C,000H,000H,078H,0CCH,07CH ; TH_86
075C	CC	7C	00	00	00	00	419	DB	0CCH,0CCH,076H,000H,000H,000H ; BT_86
0762	00	00	00	30	36	6C	420	DB	000H,000H,000H,030H,036,06CH,060H,066H ; TH_87
0764	60	66	00	00	00	00	421	DB	0CCH,0CCH,076H,000H,000H,000H ; BT_87
076A	00	00	06	3C	00	00	423	DB	03CH,00CH,006H,03CH,000H,000H ; BT_87
0770	00	10	38	6C	00	7C	423	DB	000H,010H,038H,06CH,000H,000H,07CH,0C6H,0FEN ; TH_88
0774	C6	FE	00	00	00	00	424	DB	0CCH,0C6H,07CH,000H,000H,000H ; BT_88
0778	00	00	3C	66	00	38	425	DB	000H,000H,038H,06CH,000H,000H,038H,018H,018H ; TH_88
077E	00	00	38	3C	00	7C	426	DB	0CCH,0C6H,07CH,000H,000H,000H ; BT_88
0786	CC	7C	00	00	00	00	428	DB	0CCH,0C6H,07CH,000H,000H,000H ; BT_89
078C	00	60	30	18	00	7C	429	DB	000H,060H,030H,018H,000H,000H,07CH,0C6H,0FEN ; TH_8A
0794	CC	7C	00	00	00	00	430	DB	0CCH,0C6H,07CH,000H,000H,000H ; BT_8A
079A	00	00	66	00	38	6C	432	DB	0CCH,0C6H,07CH,000H,000H,000H ; BT_8A
07A2	18	18	3C	00	00	00	434	DB	018H,018H,03CH,000H,000H,000H ; BT_8B
07AB	00	00	3C	66	00	38	435	DB	000H,018H,03CH,066H,000H,038H,018H,018H ; TH_8B
07B0	18	18	3C	00	00	00	437	DB	018H,018H,03CH,000H,000H,000H ; BT_8C
07B6	00	00	60	30	18	00	438	DB	000H,060H,030H,018H,000H,000H,038H,018H,018H ; TH_8B
07BE	00	00	3C	00	00	00	439	DB	018H,018H,03CH,000H,000H,000H ; BT_8D
07C4	00	00	C6	00	10	38	441	DB	000H,0C6H,0C6H,000H,000H,000H ; TH_8E
07CC	C6	C6	00	00	00	00	442	DB	0FEN,0C6H,0C6H,000H,000H,000H ; BT_8E
07CC	FE	C6	C6	00	00	00	443	DB	000H,000H,000H,000H,000H,000H,000H,000H,000H ; TH_8E

07D2	38	6C	38	00	38	6C	444	DB	038H, 06CH, 038H, 000H, 038H, 06CH, 0C6H, 0C6H ; TH_8F
07DA	FE	C6	C6	00	00	00	445	DB	0FEH, 06CH, 0C6H, 000H, 000H, 000H
07E0	18	30	00	FE	66	446	DB	018H, 030H, 060H, 000H, 0FEH, 066H, 060H, 07CH ; TH_90	
07E0	60	7C	00	00	00	447	DB	060H, 066H, 0FEH, 000H, 000H, 000H	
07E8	60	66	FE	00	00	449	DB	000H, 000H, 000H, 000H, 000H, 000H	
07EE	00	00	00	CC	76	450	DB	055H, 0AAH, 055H, 0AAH, 055H, 0AAH, 055H, 0AAH ; TH_91	
07F0	36	7E	00	00	00	452	DB	008H, 0B8H, 0E6H, 000H, 000H, 000H	
07FC	00	3E	6C	CC	CC	454	DB	000H, 03EH, 06CH, 0CCH, 0CCH, 0FEH, 0CCH	
0800	CC	CC	CE	00	00	455	DB	0CCH, 0CCH, 0CEH, 000H, 000H, 000H	
0800	A0	00	10	38	6C	456	DB	000H, 010H, 038H, 06CH, 000H, 07CH, 0C6H, 0C6H ; TH_93	
0804	00	00	00	00	00	458	DB	0C6H, 0C6H, 07CH, 000H, 000H, 000H	
0812	CC	6C	7C	00	00	459	DB	000H, 000H, 000H, 000H, 000H, 000H	
0818	00	00	C6	C6	00	460	DB	0C6H, 0C6H, 07CH, 000H, 000H, 000H	
0820	00	00	00	7C	00	462	DB	000H, 060H, 030H, 018H, 000H, 07CH, 0C6H, 0C6H ; TH_95	
0826	00	00	30	18	00	463	DB	0C6H, 0C6H, 07CH, 000H, 000H, 000H	
082E	C6	C6	7C	00	00	464	DB	000H, 000H, 000H, 000H, 000H, 000H	
0834	00	00	78	CC	00	465	DB	0C6H, 0C6H, 07CH, 000H, 000H, 000H	
083C	CC	CC	7C	00	00	467	DB	000H, 000H, 000H, 000H, 000H, 000H	
083C	CC	CC	76	00	00	468	DB	0C6H, 0C6H, 07CH, 000H, 000H, 000H	
0842	00	00	30	18	00	469	DB	000H, 000H, 000H, 000H, 000H, 000H	
0844	CC	CC	76	00	00	470	DB	0C6H, 0C6H, 07CH, 000H, 000H, 000H	
0850	00	00	C6	C6	00	472	DB	000H, 000H, 000H, 000H, 000H, 000H	
0858	C6	C6	7E	06	00	473	DB	0C6H, 0C6H, 07CH, 000H, 000H, 000H	
085E	00	00	38	6C	CC	474	DB	000H, 000H, 000H, 000H, 000H, 000H	
0866	CC	CC	6C	38	00	476	DB	0C6H, 0C6H, 07CH, 000H, 000H, 000H	
0866	00	00	C6	C6	00	477	DB	000H, 000H, 000H, 000H, 000H, 000H	
0874	CC	CC	76	00	00	479	DB	0C6H, 0C6H, 07CH, 000H, 000H, 000H	
087A	00	00	18	3C	66	481	DB	000H, 000H, 000H, 000H, 000H, 000H	
087A	00	00	60	66	00	482	DB	0C6H, 0C6H, 07CH, 000H, 000H, 000H	
0882	3C	18	18	00	00	483	DB	03CH, 018H, 018H, 000H, 000H, 000H	
0888	00	00	6C	64	60	485	DB	000H, 03CH, 060H, 064H, 060H, 0F0H, 060H, 060H ; TH_9C	
0890	00	00	E6	FC	00	486	DB	060H, 06EH, 0FCH, 000H, 000H, 000H	
0896	00	00	66	66	18	487	DB	000H, 000H, 066H, 066H, 03CH, 018H, 07EH, 018H ; TH_90	
089E	00	00	18	00	00	488	DB	07EH, 018H, 018H, 000H, 000H, 000H	
08A0	00	00	F8	CC	F8	489	DB	000H, 0F8H, 0CCH, 0CCH, 0F8H, 0C4H, 0CCH, 0DEH ; TH_9E	
08AC	CC	CC	CC	00	00	491	DB	0CCH, 0CCH, 0C6H, 000H, 000H, 000H	
08B2	00	00	18	18	18	492	DB	000H, 000H, 018H, 018H, 018H, 018H, 018H, 018H ; TH_9F	
08B8	7E	18	18	00	00	494	DB	018H, 018H, 018H, 000H, 000H, 000H	
08BA	18	18	18	D8	70	495	DB	000H, 000H, 000H, 000H, 000H, 000H	
08C0	00	18	30	00	70	496	DB	07EH, 018H, 018H, 000H, 000H, 000H	
08C8	CC	CC	76	00	00	498	DB	000H, 000H, 000H, 000H, 000H, 000H	
08CE	00	18	30	00	30	500	DB	000H, 000H, 000H, 000H, 000H, 000H	
08D6	00	18	3C	00	00	502	DB	018H, 018H, 03CH, 000H, 000H, 000H	
08DC	00	18	30	00	00	503	DB	000H, 018H, 030H, 060H, 000H, 07CH, 0C6H, 0C6H ; TH_A2	
08E4	C6	C6	7C	00	00	504	DB	0C6H, 0C6H, 07CH, 000H, 000H, 000H	
08E4	00	00	30	00	00	505	DB	000H, 018H, 030H, 060H, 000H, 000H, 000H	
08F2	CC	CC	7C	00	00	507	DB	0CCH, 0CCH, 07CH, 000H, 000H, 000H	
08F8	00	00	76	00	00	508	DB	000H, 000H, 000H, 000H, 000H, 000H	
0900	66	66	66	00	00	509	DB	066H, 066H, 066H, 000H, 000H, 000H	
0906	76	00	00	C6	F6	511	DB	076H, 076H, 000H, 000H, 000H, 000H	
0906	FE	DE	DE	00	00	513	DB	066H, 066H, 066H, 000H, 000H, 000H	
090E	CE	CC	C6	00	00	514	DB	0CEH, 0C6H, 0C6H, 000H, 000H, 000H	
0914	00	00	3C	6C	3C	516	DB	000H, 03CH, 06CH, 06CH, 03CH, 000H, 07EH, 000H ; TH_A6	
0912	00	00	38	6C	38	517	DB	000H, 000H, 000H, 000H, 000H, 000H	
0924	00	00	00	00	00	519	DB	000H, 000H, 000H, 000H, 000H, 000H	
0930	00	00	30	30	00	520	DB	000H, 000H, 030H, 030H, 000H, 030H, 030H, 060H ; TH_A8	
0938	6C	CC	7C	00	00	521	DB	06CH, 06CH, 07CH, 000H, 000H, 000H	
093E	00	00	00	00	00	522	DB	000H, 000H, 000H, 000H, 000H, 000H	
0946	C0	00	00	00	00	523	DB	0C0H, 0C0H, 000H, 000H, 000H, 000H	
094C	00	00	00	00	00	524	DB	000H, 000H, 000H, 000H, 000H, 000H	
0954	00	00	06	00	00	525	DB	000H, 000H, 000H, 000H, 000H, 000H	
0954	00	00	C0	C6	CC	526	DB	000H, 000H, 000H, 000H, 000H, 000H	
0962	DC	80	0C	18	3E	527	DB	0DCH, 086H, 00CH, 018H, 03EH, 000H	
0968	00	00	CC	CC	00	528	DB	000H, 000H, 000H, 000H, 000H, 000H	
0970	36	66	66	00	00	529	DB	060H, 060H, 060H, 000H, 000H, 000H	
0976	CE	9E	3E	00	00	530	DB	0CEH, 09EH, 03EH, 000H, 000H, 000H	
097E	00	00	18	18	00	531	DB	000H, 000H, 000H, 000H, 000H, 000H	
0984	00	00	00	36	6C	532	DB	000H, 000H, 000H, 000H, 000H, 000H	
098C	20	00	00	00	00	533	DB	036H, 036H, 000H, 000H, 000H, 000H	
0992	00	00	00	00	6C	534	DB	000H, 000H, 000H, 000H, 000H, 000H	
0994	D8	00	00	00	00	535	DB	0D8H, 0D8H, 000H, 000H, 000H, 000H	
09A0	11	44	11	44	11	536	DB	011H, 044H, 011H, 044H, 011H, 044H ; TH_B0	
09A8	11	44	11	44	11	537	DB	011H, 044H, 011H, 044H, 011H, 044H ; TH_B0	
09AE	55	AA	55	AA	55	538	DB	055H, 0AAH, 055H, 0AAH, 055H, 0AAH ; TH_B1	
0986	55	AA	55	AA	55	539	DB	055H, 0AAH, 055H, 0AAH, 055H, 0AAH ; TH_B1	
098C	DD	77	DD	77	DD	540	DB	0DDH, 077H, 00DH, 077H, 00DH, 077H ; TH_B2	
0992	00	00	00	00	00	541	DB	000H, 000H, 000H, 000H, 000H, 000H	
09A4	00	00	00	00	00	542	DB	000H, 000H, 000H, 000H, 000H, 000H	
09A9	00	00	00	00	00	543	DB	000H, 000H, 000H, 000H, 000H, 000H	
09E4	00	00	00	00	00	544	DB	000H, 000H, 000H, 000H, 000H, 000H	
09A0	11	44	11	44	11	545	DB	011H, 044H, 011H, 044H, 011H, 044H ; TH_B0	
09A8	11	44	11	44	11	546	DB	011H, 044H, 011H, 044H, 011H, 044H ; TH_B0	
09AE	55	AA	55	AA	55	547	DB	055H, 0AAH, 055H, 0AAH, 055H, 0AAH ; TH_B1	
0986	55	AA	55	AA	55	548	DB	055H, 0AAH, 055H, 0AAH, 055H, 0AAH ; TH_B1	
098C	DD	77	DD	77	DD	549	DB	0DDH, 077H, 00DH, 077H, 00DH, 077H ; TH_B2	
0992	00	00	00	00	00	550	DB	000H, 000H, 000H, 000H, 000H, 000H	
09E4	00	00	00	00	00	551	DB	000H, 000H, 000H, 000H, 000H, 000H	
09A0	11	44	11	44	11	552	DB	011H, 044H, 011H, 044H, 011H, 044H ; TH_B0	
09A8	11	44	11	44	11	553	DB	011H, 044H, 011H, 044H, 011H, 044H ; TH_B0	
09AE	55	AA	55	AA	55	554	DB	055H, 0AAH, 055H, 0AAH, 055H, 0AAH ; TH_B1	
0986	55	AA	55	AA	55	555	DB	055H, 0AAH, 055H, 0AAH, 055H, 0AAH ; TH_B1	
098C	DD	77	DD	77	DD	556	DB	0DDH, 077H, 00DH, 077H, 00DH, 077H ; TH_B2	
0992	00	00	00	00	00	557	DB	000H, 000H, 000H, 000H, 000H, 000H	
09D2	18	18	18	18	18	558	DB	018H, 018H, 018H, 018H, 018H, 018H ; TH_B4	
09D8	18	18	18	18	18	559	DB	018H, 018H, 018H, 018H, 018H, 018H ; TH_B4	
09E0	18	18	18	18	18	560	DB	018H, 018H, 018H, 018H, 018H, 018H ; TH_B4	
09E6	18	18	18	18	F8	561	DB	018H, 018H, 018H, 018H, 018H, 0F8H ; TH_B5	
09E6	18	18	18	18	18	562	DB	018H, 018H, 018H, 018H, 018H, 018H ; TH_B5	
09E6	18	18	18	18	18	563	DB	018H, 018H, 018H, 018H, 018H, 018H ; TH_B5	
09F4	00	36	36	36	36	564	DB	000H, 036H, 036H, 036H, 036H, 036H ; TH_B6	
09F4	36	F6	36	36	36	565	DB	036H, 036H, 036H, 036H, 036H, 036H ; TH_B6	
09FC	36	36	36	36	36	566	DB	036H, 036H, 036H, 036H, 036H, 036H ; TH_B6	
0402	00	00	00	00	00	567	DB	000H, 000H, 000H, 000H, 000H, 000H ; TH_B7	
09A0	36	36	36	36	36	568	DB	036H, 036H, 036H, 036H, 036H, 036H ; TH_B7	
09E6	36	36	36	36	36	569	DB	036H, 036H, 036H, 036H, 036H, 036H ; TH_B7	

OA10	00	18	00	00	00	F8	570	DB	000H,000H,000H,000H,000H,0F8H,018H,0F8H ; TH_B8
OA18	51	18	18	18	18	18	571	DB	018H,018H,018H,018H,018H,018H,036H,036H ; BT_B8
OA1E	36	36	36	36	36	F6	573	DB	036H,036H,036H,036H,036H,036H,036H,036H ; TH_B9
OA26	36	36	36	36	36	36	575	DB	036H,036H,036H,036H,036H,036H,036H,036H ; BT_B9
OA2C	36	36	36	36	36	36	576	DB	036H,036H,036H,036H,036H,036H,036H,036H ; TH_BA
OA31	36	36	36	36	36	36	578	DB	036H,036H,036H,036H,036H,036H,036H,036H ; BT_BA
OA3A	00	00	00	00	00	FE	579	DB	000H,000H,000H,000H,000H,000H,0F8H,006H,0F6H ; TH_BB
OA42	36	36	36	36	36	F6	580	DB	036H,036H,036H,036H,036H,036H,036H,036H,0F6H ; TH_BC
OA48	36	36	36	36	36	F6	582	DB	036H,036H,036H,036H,036H,036H,036H,036H,0F6H ; TH_BC
OA50	00	00	00	00	00	00	583	DB	000H,000H,000H,000H,000H,000H,000H,000H,0F6H ; TH_BD
OA56	36	36	36	36	36	36	585	DB	036H,036H,036H,036H,036H,036H,036H,036H,0F6H ; TH_BD
OA5E	00	00	00	00	00	00	587	DB	000H,000H,000H,000H,000H,000H,000H,000H,0F6H ; TH_BD
OA64	18	18	18	18	18	F8	588	DB	018H,018H,018H,018H,018H,018H,0F8H,018H,0F8H ; TH_BE
OA6C	00	00	00	00	00	00	589	DB	000H,000H,000H,000H,000H,000H,000H,000H,0F8H ; TH_BF
OA72	00	00	00	00	00	00	591	DB	000H,000H,000H,000H,000H,000H,000H,000H,0F8H ; TH_BF
DATA	18	18	18	18	18	18	593	DB	018H,018H,018H,018H,018H,018H,018H,018H,018H ; TH_BF
OA80	18	18	18	18	18	18	594	DB	018H,018H,018H,018H,018H,018H,018H,018H,01FH ; TH_CO
OA86	18	18	18	18	18	18	595	DB	018H,018H,018H,018H,018H,018H,018H,018H,01FH ; TH_CO
OA8B	00	00	00	00	00	00	597	DB	000H,000H,000H,000H,000H,000H,000H,000H,0FFH ; TH_C0
OA8E	18	18	18	18	18	18	598	DB	018H,018H,018H,018H,018H,018H,018H,018H,0FFH ; TH_C1
OA96	00	00	00	00	00	00	600	DB	000H,000H,000H,000H,000H,000H,000H,000H,0FFH ; TH_C1
OA9C	00	00	00	00	00	00	601	DB	000H,000H,000H,000H,000H,000H,000H,000H,0FFH ; TH_C2
OA9A	00	FF	00	00	00	00	602	DB	000H,000H,000H,000H,000H,000H,000H,000H,0FFH ; TH_C2
OAA1	18	18	18	18	18	18	603	DB	018H,018H,018H,018H,018H,018H,018H,018H,01FH ; TH_C2
OAAA	18	18	18	18	18	18	604	DB	018H,018H,018H,018H,018H,018H,018H,018H,01FH ; TH_C3
OABA	18	18	18	18	18	18	605	DB	018H,018H,018H,018H,018H,018H,018H,018H,01FH ; TH_C3
OAB2	18	18	18	18	18	18	606	DB	018H,018H,018H,018H,018H,018H,018H,018H,01FH ; TH_C3
OABB	00	00	00	00	00	00	607	DB	000H,000H,000H,000H,000H,000H,000H,000H,0FFH ; TH_C4
OAC0	00	00	00	00	00	00	608	DB	000H,000H,000H,000H,000H,000H,000H,000H,0FFH ; TH_C4
OAC6	18	18	18	18	18	18	610	DB	018H,018H,018H,018H,018H,018H,018H,018H,0FFH ; TH_C5
OACE	18	18	18	18	18	18	611	DB	018H,018H,018H,018H,018H,018H,018H,018H,0FFH ; TH_C5
OAD4	18	18	18	18	18	1F	612	DB	018H,018H,018H,018H,018H,018H,018H,01FH,01FH ; TH_C6
OAE1	18	18	18	18	18	1F	613	DB	018H,018H,018H,018H,018H,018H,018H,01FH,01FH ; TH_C6
OADC	18	18	18	18	18	18	615	DB	018H,018H,018H,018H,018H,018H,018H,018H,01FH ; TH_C6
OAE2	36	36	36	36	36	36	616	DB	036H,036H,036H,036H,036H,036H,036H,036H,037H ; TH_C7
OAE3	36	36	36	36	36	36	617	DB	036H,036H,036H,036H,036H,036H,036H,036H,037H ; TH_C7
OAE4	36	36	36	36	36	36	618	DB	036H,036H,036H,036H,036H,036H,036H,036H,037H ; TH_C7
OAF0	36	36	36	36	36	37	619	DB	036H,036H,036H,036H,036H,036H,036H,037H,03FH ; TH_C8
OAF8	00	00	00	00	00	00	620	DB	000H,000H,000H,000H,000H,000H,000H,000H,0FFH ; TH_C8
OAF0	00	00	00	00	00	00	620	DB	000H,000H,000H,000H,000H,000H,000H,000H,03FH ; TH_C8
OB06	36	36	36	36	36	36	624	DB	036H,036H,036H,036H,036H,036H,036H,036H,0FFH ; TH_C9
OB0C	36	36	36	36	36	36	625	DB	036H,036H,036H,036H,036H,036H,036H,036H,0FFH ; TH_C9
OB14	00	00	00	00	00	00	626	DB	000H,000H,000H,000H,000H,000H,000H,000H,0FFH ; TH_C9
OB1A	00	00	00	00	00	00	627	DB	000H,000H,000H,000H,000H,000H,000H,000H,0FFH ; TH_C9
OB22	00	F7	00	00	00	00	629	DB	000H,000H,000H,000H,000H,000H,000H,000H,0FFH ; TH_C9
OB28	36	36	36	36	36	36	630	DB	036H,036H,036H,036H,036H,036H,036H,036H,0FFH ; TH_C9
OB32	36	36	36	36	36	37	631	DB	036H,036H,036H,036H,036H,036H,036H,037H,0FFH ; TH_C9
OB30	36	36	36	36	36	36	632	DB	036H,036H,036H,036H,036H,036H,036H,036H,0FFH ; TH_CC
OB36	00	00	00	00	00	00	634	DB	000H,000H,000H,000H,000H,000H,000H,000H,0FFH ; TH_CD
OB3E	00	00	00	00	00	00	635	DB	000H,000H,000H,000H,000H,000H,000H,000H,0FFH ; TH_CD
OB44	36	36	36	36	36	F7	637	DB	036H,036H,036H,036H,036H,036H,036H,037H,0FFH ; TH_CE
OB4C	36	36	36	36	36	36	638	DB	036H,036H,036H,036H,036H,036H,036H,036H,0FFH ; TH_CE
OB52	18	18	18	18	18	FF	640	DB	018H,018H,018H,018H,018H,018H,018H,018H,0FFH ; TH_CF
OB5A	00	00	00	00	00	00	641	DB	000H,000H,000H,000H,000H,000H,000H,000H,0FFH ; TH_CD
OB60	36	36	36	36	36	36	642	DB	036H,036H,036H,036H,036H,036H,036H,036H,0FFH ; TH_CD
OB66	36	36	36	36	36	36	644	DB	036H,036H,036H,036H,036H,036H,036H,036H,0FFH ; TH_CD
OB68	00	00	00	00	00	00	646	DB	000H,000H,000H,000H,000H,000H,000H,000H,0FFH ; TH_D0
OB6E	00	00	00	00	00	00	647	DB	000H,000H,000H,000H,000H,000H,000H,000H,0FFH ; TH_D0
OB76	18	18	18	18	18	18	649	DB	018H,018H,018H,018H,018H,018H,018H,018H,0FFH ; TH_D1
OB7C	00	00	00	00	00	00	650	DB	000H,000H,000H,000H,000H,000H,000H,000H,0FFH ; TH_D2
OB84	36	36	36	36	36	36	652	DB	036H,036H,036H,036H,036H,036H,036H,036H,0FFH ; TH_D2
OB8A	36	36	36	36	36	36	653	DB	036H,036H,036H,036H,036H,036H,036H,036H,0FFH ; TH_D3
OB92	00	00	00	00	00	00	655	DB	000H,000H,000H,000H,000H,000H,000H,000H,0FFH ; TH_D3
OB98	18	18	18	18	18	1F	656	DB	018H,018H,018H,018H,018H,018H,018H,01FH,01FH ; TH_D4
OBAA	00	00	00	00	00	00	658	DB	000H,000H,000H,000H,000H,000H,000H,000H,0FFH ; TH_D4
OBAG	00	00	00	00	00	01	659	DB	000H,000H,000H,000H,000H,000H,000H,000H,03FH ; TH_D5
OBAE	18	18	18	18	18	18	660	DB	018H,018H,018H,018H,018H,018H,018H,018H,01FH ; TH_D5
OBAA	00	00	00	00	00	00	661	DB	000H,000H,000H,000H,000H,000H,000H,000H,03FH ; TH_D6
OBBD	00	00	00	00	00	00	662	DB	000H,000H,000H,000H,000H,000H,000H,000H,03FH ; TH_D6
OBDE	18	18	18	18	18	18	663	DB	018H,018H,018H,018H,018H,018H,018H,018H,03FH ; TH_D6
OBBC	36	36	36	36	36	36	664	DB	036H,036H,036H,036H,036H,036H,036H,036H,0FFH ; TH_D6
OB2C	36	36	36	36	36	36	665	DB	036H,036H,036H,036H,036H,036H,036H,036H,0FFH ; TH_D7
OBDA	36	36	36	36	36	36	666	DB	036H,036H,036H,036H,036H,036H,036H,036H,0FFH ; TH_D7
OBCE	18	18	18	18	18	18	667	DB	018H,018H,018H,018H,018H,018H,018H,018H,0FFH ; TH_D7
OBEC	00	00	00	00	00	00	674	DB	000H,000H,000H,000H,000H,000H,000H,000H,0FFH ; TH_D9
OBF4	18	18	18	18	18	18	676	DB	018H,018H,018H,018H,018H,018H,018H,018H,0FFH ; TH_DA
OBFA	FF	FF	FF	FF	FF	FF	677	DB	0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH ; TH_DB
OBFD	FF	FF	FF	FF	FF	FF	678	DB	0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH ; TH_DB
OC02	FF	FF	FF	FF	FF	FF	679	DB	0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH ; TH_DC
OC08	00	00	00	00	00	00	680	DB	000H,000H,000H,000H,000H,000H,000H,000H,0FFH ; TH_DC
OC10	FF	FF	FF	FF	FF	FF	681	DB	0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH ; TH_DC
OC16	FF	FF	FO	FO	FO	FO	682	DB	0FFH,0FFH,0FOH,0FOH,0FOH,0FOH,0FOH,0FOH,0FOH ; TH_DD
OC1E	FO	FO	FO	FO	FO	FO	683	DB	0FOH,0FOH,0FOH,0FOH,0FOH,0FOH,0FOH,0FOH,0FOH ; TH_DD
OC24	OF	OF	OF	OF	OF	OF	684	DB	0FOH,0FOH,0FOH,0FOH,0FOH,0FOH,0FOH,0FOH,0FOH ; TH_DD
OC2C	OF	OF	OF	OF	OF	OF	685	DB	0FOH,0FOH,0FOH,0FOH,0FOH,0FOH,0FOH,0FOH,0FOH ; TH_DD
OC32	FF	FF	FF	FF	FF	FF	686	DB	0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH ; TH_DF
OC3A	FF	FF	FF	FF	FF	FF	687	DB	0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH ; TH_DF
OC40	00	00	00	00	00	00	76	DB	000H,000H,000H,000H,000H,000H,000H,000H,0FFH ; TH_E0
DC	DC	DC	DC	DC	DC	DC	694	DB	000H,000H,000H,000H,000H,000H,000H,000H,0FFH ; TH_E0
OC48	DB	DC	76	00	00	00	695	DB	0DB8H,0DCDH,076H,000H,000H,000H,000H,000H,000H ; TH_E0

OC4E	00 00 00 00 7C C6	696	DB	000H, 000H, 000H, 000H, 07CH, OC6H, OFCH, OC6H ; TH_E1	
OC56	C6 CC CO C0 40 00	697	DB	006H, OFCH, OC0H, OC0H, 040H, 000H ; BT_E1	
OC5C	CO CO FE C6 C0 C0	698	DB	000H, 000H, 0FEH, OC6H, OC6H, OC0H, OC0H, OC0H ; TH_E2	
OC64	CO CO C0 00 00 00	699	DB	0C0H, OC0H, OC0H, 000H, 000H, 000H ; BT_E2	
OC6A	00 00 00 00 FE 6C	700	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_E3	
OC6C	6C C0	703	DB	0C6H, OC6H, OC6H, 000H, 000H, 000H ; BT_E3	
OC72	6C 6C 6C 00 00 00	704	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_E4	
OC78	00 00 FE FE 60 30	705	DB	018H, 018H, 018H, 000H, 000H, 000H, 000H, 000H ; TH_E5	
OC80	00 00 00 FE 00 00	706	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_E6	
OC86	00 00 00 00 00 7E	707	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_E5	
	D8 D0	709	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_E5	
OC8E	D8 D0 70 00 00 00	710	DB	0D8H, 0DBH, 070H, 000H, 000H, 000H ; BT_E5	
OC94	00 00 00 00 66 66	711	DB	000H, 000H, 000H, 000H, 066H, 066H, 066H ; TH_E6	
OC9C	7C 60 60 00 00 00	712	DB	07CH, 060H, 060H, OC0H, 000H, 000H ; BT_E6	
OCAA	00 00 00 00 76 DC	713	DB	000H, 000H, 000H, 000H, 076H, OC0H, 018H, 018H ; TH_E7	
OCAB	18 18 18 00 00 00	714	DB	018H, 018H, 018H, 000H, 000H, 000H ; BT_E7	
OCB0	00 00 7E 18 3C 66	715	DB	000H, 000H, 000H, 000H, 000H, 000H, 03CH, 066H, 066H ; TH_E8	
	66 66	716	DB	000H, 000H, 000H, 000H, 000H, 000H, 03CH, 066H, 066H ; TH_E8	
OCBB	3C 18 7E 00 00 00	717	DB	03CH, 018H, 07EH, 000H, 000H, 000H ; BT_E8	
OCBE	00 00 00 38 6C C6	718	DB	000H, 000H, 038H, 06CH, 0C8H, OC6H, 0FEH, OC6H ; TH_E9	
	FE C6	719	DB	000H, 000H, 038H, 06CH, 0C8H, OC6H, 0FEH, OC6H ; TH_E9	
OCCE	C6 6C 38 00 00 00	720	DB	006H, 06CH, 038H, 000H, 000H, 000H ; BT_E9	
OCCC	00 00 38 6C C6 C6	721	DB	000H, 000H, 038H, 06CH, 0C6H, OC6H, 06CH, 06CH ; TH_EA	
OCDD	6C 6C EE 00 00 00	722	DB	000H, 000H, 000H, 000H, 000H, 000H ; BT_EA	
OCDA	00 00 1E 30 18 OC	723	DB	000H, 000H, 01EH, 030H, 018H, 000H, 03EH, 066H ; TH_EB	
	3E 66	724	DB	000H, 000H, 01EH, 030H, 018H, 000H, 03EH, 066H ; TH_EB	
OCE2	66 66 3C 00 00 00	725	DB	066H, 066H, 03CH, 000H, 000H, 000H ; BT_EB	
OCE8	00 00 00 00 00 7E	726	DB	000H, 000H, 000H, 000H, 000H, 000H, 07EH, ODBH, ODBH ; TH_EC	
	OFC0	7E 00 00 00 00 00	727	DB	07EH, 000H, 000H, 000H, 000H, 000H ; BT_EC
OFC6	00 00 03 06 7E DB	728	DB	000H, 000H, 003H, 006H, 07EH, ODBH, ODBH, OF3H ; TH_ED	
OFCF	FE 60 C0 00 00 00	729	DB	07EH, 060H, OC0H, 000H, 000H, 000H ; BT_ED	
OFO4	00 00 1C 30 60 00	730	DB	060H, 030H, 01CH, 000H, 000H, 000H ; BT_EE	
OD12	00 00 00 00 7C C6	731	DB	000H, 000H, 000H, 000H, 07CH, OC6H, OC6H, OC6H ; TH_EF	
OD1A	C6 C6 C6 00 00 00	732	DB	006H, 06CH, OC6H, 000H, 000H, 000H ; BT_EF	
OD20	00 00 00 FE 00 00	733	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F0	
	FE 00	734	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F0	
OFCF	FE 60 C0 00 00 00	735	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F1	
OD34	00 00 00 00 18 7E	736	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F2	
OD3C	00 00 30 18 OC 06	737	DB	000H, 000H, 030H, 018H, 000H, 000H, 000H, 000H ; TH_F2	
	OC 18	738	DB	000H, 000H, 030H, 018H, 000H, 000H, 000H, 000H ; TH_F2	
OD44	30 00 7E 00 00 00	739	DB	030H, 000H, 07EH, 000H, 000H, 000H ; BT_F2	
OD4A	00 00 00 18 30 60	740	DB	000H, 000H, 000H, 018H, 030H, 060H, 030H, 018H ; TH_F3	
	30 18	741	DB	000H, 000H, 000H, 018H, 030H, 060H, 030H, 018H ; TH_F3	
OD52	00 00 00 00 00 00	742	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F4	
OD58	00 00 00 0E 1B 1B 1B	743	DB	000H, 000H, 000H, 01BH, 01BH, 01BH, 01BH, 01BH, 01BH ; TH_F4	
OD60	18 18 18 18 18 18	744	DB	018H, 018H, 018H, 018H, 018H, 018H ; BT_F4	
OD66	18 18 18 18 18 18	745	DB	018H, 018H, 018H, 018H, 018H, 018H ; BT_F4	
	18 18	746	DB	018H, 018H, 018H, 018H, 018H, 018H ; BT_F4	
OD6E	D8 D8 00 00 00 00	747	DB	000H, 000H, 000H, 000H, 000H, 000H ; BT_F5	
OFC4	00 00 00 00 18 00 00	748	DB	000H, 000H, 000H, 018H, 000H, 000H, 018H, 018H ; TH_F2	
	7E 00	749	DB	000H, 000H, 000H, 018H, 000H, 000H, 018H, 018H ; TH_F2	
OD7C	18 18 00 00 00 00	750	DB	018H, 018H, 000H, 000H, 000H, 000H ; BT_F6	
OD82	00 00 00 00 76 DC	751	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F7	
OD8A	DC 00 00 00 00 00	752	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F7	
OD90	00 38 6C 38 00 00	753	DB	000H, 038H, 06CH, 06CH, 038H, 000H, 000H, 000H ; TH_F8	
OD98	00 00 00 00 00 00	754	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F8	
OD9E	00 00 00 00 00 00	755	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F9	
ODA6	00 00 00 00 00 00	756	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F9	
ODAC	00 00 00 00 00 00	757	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_FA	
ODBA	00 00 00 00 00 00	758	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_FA	
ODC2	00 3C 1C 00 00 00	759	DB	06CH, 03CH, 01CH, 000H, 000H, 000H ; BT_FC	
ODCB	6C 60 6C 6C 6C	760	DB	000H, 06BH, 06CH, 06CH, 06CH, 06CH, 000H, 000H ; TH_FC	
ODD0	00 00 00 00 00 00	761	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; BT_FD	
ODD6	00 70 00 30 60 C8	762	DB	000H, 070H, 0DBH, 030H, 060H, OC8H, OF8H, 000H ; BT_FD	
ODDE	00 00 00 00 00 00	763	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_FE	
ODE4	00 00 00 00 00 00	764	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_FE	
ODEC	00 70 00 00 00 00	765	DB	07CH, 07CH, 000H, 000H, 000H, 000H, 000H, 000H ; BT_FE	
ODF2	00 00 00 00 00 00	766	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_FF	
ODFA	00 00 00 00 00 00	767	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_FF	
	FF 00	768	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_FF	
OE00	00 00 00 00 00 00	769	CODE	000H, 000H, 000H, 000H, 000H, 000H ; BT_FF	
		791	END		

PAGE 120  
 START MONOCHROME CHARACTER GENERATOR - ALPHA SUPPLEMENT  
 CODE SEGMENT PUBLIC CGMN\_FDG LABEL BYTE  
 :: STRUCTURE OF THIS FILE  
 :: XXH WHERE XX IS THE HEX CODE FOR THE FOLLOWING CHAR  
 :: [BYTES 0 - 13 OF THAT CHARACTER]  
 :: DB 00H INDICATES NO MORE REPLACEMENTS TO BE DONE  
 14 DB 01DH  
 15 DB 000H, 000H, 000H, 000H, 024H, 066H, OFFH, 066H ; TH\_1D  
 16 DB 024H, 000H, 000H, 000H, 000H, 000H ; BT\_1D  
 17 DB 022H  
 18 DB 000H, 063H, 063H, 022H, 000H, 000H, 000H ; TH\_22 "  
 19 DB 000H, 063H, 063H, 022H, 000H, 000H, 000H ; TH\_22 "  
 20 DB 000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; BT\_22 "  
 21 DB 000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; BT\_22 "  
 22 DB 028H  
 23 DB 000H, 000H, 018H, 018H, 018H, 018H, 0FFH, 018H ; TH\_2B +  
 24 DB 000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH\_2B +  
 25 DB 018H, 018H, 000H, 000H, 000H, 000H, 000H, 000H ; TH\_2B +  
 26 DB 020H  
 27 DB 000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH\_2D -

0036	00 00 00 00 00 00	29	DB	000H, 000H, 000H, 000H, 000H, 000H	; BT_20 -
003C	4D	30	DB	01DH	
003D	00 00 C3 E7 FF DB	31	DB	000H, 000H, 0C3H, 0E7H, 0FFH, 0DBH, 0C3H, 0C3H	; TH_40 M
C3	C3 C3 00 00 00	32	DB	0C3H, 0C3H, 0C3H, 000H, 000H, 000H	; BT_40 M
0048	54	34	DB	05AH	
004C	00 00 FF DB 99 18	35	DB	000H, 000H, 0FFH, 0DBH, 099H, 018H, 018H, 018H	; TH_54 T
18 18		36	DB	018H, 018H, 03CH, 000H, 000H, 000H	; BT_54 T
0054	18 18 3C 00 00 00	37	DB	056H	
005A	56	38	DB	000H, 000H, 0C3H, 0C3H, 0C3H, 0C3H, 0C3H, 0C3H	; TH_56 V
005B	00 00 C3 C3 C3 C3	39	DB	C3 C3	
C3	C3 40	40	DB	066H, 03CH, 018H, 000H, 000H, 000H	; BT_56 V
0063	66 3C 18 00 00 00	41	DB	057H	
0065	00 00 C3 C3 C3 C3	43	DB	000H, 000H, 0C3H, 0C3H, 0C3H, 0C3H, 0DBH, 0DBH	; TH_57 W
DB	DB	44	DB		
0072	FF 66 66 00 00 00	45	DB	0FFH, 066H, 066H, 000H, 000H, 000H	; BT_57 W
0078	56	46	DB	058H	
0079	00 00 C3 C3 66 3C	47	DB	000H, 000H, 0C3H, 0C3H, 066H, 03CH, 018H, 03CH	; TH_58 X
18 3C		48	DB	066H, 03CH, 03CH, 000H, 000H, 000H	; BT_58 X
0081	66 C3 C3 00 00 00	49	DB	059H	
0087	59	50	DB	000H, 000H, 0C3H, 0C3H, 0C3H, 066H, 03CH, 018H	; TH_59 Y
0088	00 00 C3 C3 C3 66	51	DB	3C 18	
0090	66 18 3C 00 00 00	52	DB	018H, 018H, 03CH, 000H, 000H, 000H	; BT_59 Y
0096	5A	53	DB	05AH	
0097	00 00 FF C3 86 OC	54	DB	000H, 000H, 0FFH, 0C3H, 086H, 00CH, 018H, 030H	; TH_5A Z
86 30		55	DB	061H, 03CH, 0FFH, 000H, 000H, 000H	; BT_5A Z
009F	61 C3 FF 00 00 00	57	DB	060H	
00A5	60	58	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_60 L.C. M
00A6	00 00 00 00 E6	59	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_60 L.C. M
DB	DB DB 00 00 00 00	60	DB	0DBH, 0DBH, 0DBH, 000H, 000H, 000H	; BT_60 L.C. M
00B4	76	62	DB	076H	
00B5	00 00 00 00 C3	63	DB	000H, 000H, 000H, 000H, 000H, 0C3H, 0C3H, 0C3H	; TH_76 L.C. V
C3 C3		64	DB	066H, 03CH, 018H, 000H, 000H, 000H	; BT_76 L.C. V
00BD	3C 18 00 00 00	65	DB	077H	
00C3	77	66	DB	000H, 000H, 000H, 000H, 000H, 0C3H, 0C3H, 0DBH	; TH_77 L.C. W
00C4	00 00 00 00 C3	67	DB	000H, 000H, 000H, 000H, 000H, 0C3H, 0C3H, 0DBH	; TH_77 L.C. W
CB	DB	68	DB	0DBH, 0FFH, 066H, 000H, 000H, 000H	; BT_77 L.C. W
00CC	FF F7 66 00 00 00	69	DB	091H	
00D2	91	70	DB	000H, 000H, 000H, 000H, 000H, 03BH, 018H, 07EH	; TH_91
00D3	00 00 00 00 6E 3B	71	DB	18 7E	
00DE	DB DC 77 00 00 00	72	DB	0DBH, 0DCH, 077H, 000H, 000H, 000H	; BT_91
00E1	71	73	DB	098H	
00E2	00 18 7E C3 C0	74	DB	000H, 018H, 018H, 07EH, 0C3H, 0COH, 0COH, 0C3H	; TH_98
CO C3		75	DB	07EH, 018H, 018H, 000H, 000H, 000H	; BT_98
00EA	75 18 18 00 00 00	77	DB	090H	
00FO	78	78	DB	000H, 000H, 0C3H, 066H, 03CH, 018H, 0FFH, 018H	; TH_90
00F7	00 00 C3 63 C3 18	79	DB	0DBH, 0FFH, 066H, 000H, 000H, 000H	; BT_90
FF 18		80	DB	09EH	
00F9	FF 18 18 00 00 00	81	DB	000H, 0CMB, 066H, 066H, 07CH, 062H, 066H, 06FH	; TH_9E
FF 18		82	DB	066H, 03FH, 000H, 000H, 000H, 000H	; BT_9E
0100	60 FC 66 66 7C 62	83	DB	07FH	
66 6F		84	DB	000H, 0CMB, 066H, 066H, 07CH, 062H, 066H, 06FH	; TH_9E
0108	66 6F F3 00 00 00	85	DB	066H, 066H, 03FH, 000H, 000H, 000H	; BT_9E
010E	F1	86	DB	07FH	
010F	00 00 18 18 18 FF	87	DB	000H, 000H, 018H, 018H, 018H, 018H, 0FFH, 018H	; TH_F1
18 18		88	DB	018H, 000H, 0FFH, 000H, 000H, 000H	; BT_F1
0117	18 00 FF 00 00 00	89	DB	06FH	
011D	F6	90	DB	000H, 000H, 018H, 018H, 018H, 000H, 000H, 0FFH, 000H	; TH_F6
011E	FF 00 00 18 18 00 00	92	DB	000H, 018H, 018H, 000H, 000H, 000H, 000H, 000H	; BT_F6
0126	00 18 18 00 00 00	93	DB	000H, 018H, 018H, 000H, 000H, 000H	; BT_F6
012C	00	94	DB	000H	
012D	95	95	CODE	ENDS	
		96	CODE	END	

1	PAGE 120				
2	SUBTITL DOUBLE DOT CHARACTER GENERATOR				
3	CODE SEGMENT PUBLIC CGDDOT, INT_1F_1				
4	CGDDOT LABEL BYTE				
5					
0000	00 00 00 00 00 00	6	DB	000H, 000H, 000H, 000H, 000H, 000H	; DOUBLE DOT
0000	00 00	8	DB	000H, 000H, 000H, 000H, 000H, 000H	; _D_00
0008	7E 81 A5 81 B9 99	10	DB	07EH, 018H, 0A5H, 081H, 0B9H, 099H, 081H, 07EH	; _D_01
81 7E		11	DB	07EH, 0FFH, 0DBH, 0FFH, 0C3H, 0E7H, 0FFH, 07EH	; _D_02
0010	7E F7 DB FF C3 E7	12	DB	06CH, 0FEH, 0FEH, 0FEH, 07CH, 038H, 010H, 000H	; _D_03
6C FE FE FE 7C 38		13	DB	010H, 038H, 07CH, 0FEH, 07CH, 038H, 010H, 000H	; _D_04
0020	10 38 7C FE 7C 38	14	DB	038H, 07CH, 038H, 0FEH, 07CH, 038H, 07CH	; _D_05
10 38		15	DB	010H, 010H, 038H, 07CH, 0FEH, 07CH, 038H, 07CH	; _D_06
0028	38 7C 38 FE FE 7C	17	DB	000H, 000H, 018H, 03CH, 03CH, 018H, 000H, 000H	; _D_07
38 7C		18	DB	0FFH, 0FFH, 07EH, 0C3H, 03CH, 018H, 000H, 000H	; _D_08
0030	10 38 7C FE 7C 7C	19	DB	0FFH, 0FFH, 07EH, 0C3H, 03CH, 018H, 000H, 000H	; _D_08
0038	00 00 18 3C 3C 18	21	DB	018H, 000H, 000H, 000H, 000H, 000H	; _D_09
0040	FF FF E7 C3 C3 E7	23	DB	0FFH, 0FFH, 07EH, 0C3H, 03CH, 018H, 000H, 000H	; _D_08
0048	00 3C 66 42 42 66	24	DB	000H, 03CH, 066H, 042H, 042H, 066H, 03CH, 000H	; _D_09
3C 00		25	DB	0FFH, 03CH, 099H, 08DH, 08DH, 099H, 0C3H, 0FFH	; _D_0A
0050	FF C3 99 BD BB 99	27	DB	00FH, 00TH, 00FH, 07DH, 0CCH, 0CCH, 07FH	; _D_0B
C3 F7		28	DB	03FH, 066H, 066H, 066H, 03CH, 018H, 07EH, 018H	; _D_0C
0058	00 00 F7 D0 CC C0	29	DB	03FH, 033H, 03FH, 030H, 030H, 070H, 0F0H, 0E0H	; _D_0D
CC 78		30	DB	07FH, 063H, 07FH, 063H, 063H, 063H, 063H, 063H	; _D_0E
0060	3C 66 66 66 3C 18	31	DB	099H, 05AH, 03CH, 0E7H, 0E7H, 03CH, 05AH, 099H	; _D_0F
3C 18		32	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H	
0068	3E 32 3F 30 30 70	33	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H	
3E 30		34	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H	
0070	7F 63 7F 63 63 67	35	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H	
7F 63		36	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H	
0078	95 5A 3C E7 3C 3C	37	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H	
95 5A		38	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H	
0080	80 E0 FE FE FE EO	39	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H	
80 00		40	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H	
0088	02 DE 3E FE 3E 0E	41	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H	
0090	18 3C 7E 18 18 7E	42	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H	
18 3C		43	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H	
0098	66 66 66 66 66 00	44	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H	
66 66		45	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H	
00A0	7F DB 7B 1B 1B 48	46	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H	
7B 1B		47	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H	
00A8	3E 63 3B 6C 66 38	48	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H	
3E 63		49	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H	
00B0	00 00 00 00 7E 7E	50	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H	
7E 7E		51	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H	
00B8	18 3C 7E 18 7E 3C	52	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H	
18 3C		53	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H	
00C0	18 3C 7E 18 18 18	54	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H	
18 3C		55	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H	
		56	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H	

00C8	18 00	57	DB	018H,018H,018H,018H,07EH,03CH,018H,000H ; D_19
	18 00	58	DB	000H,018H,00CH,0FEH,00CH,018H,000H,000H ; D_1A
00D0	00 18 OC FE 0C 18	60	DB	000H,030H,060H,0FEH,060H,030H,000H,000H ; D_1B
	00 00	61	DB	000H,000H,0COH,0COH,0COH,0COH,0FEH,000H,000H ; D_1C
00DE	00 30 60 FE 60 30	62	DB	000H,024H,066H,0FFH,066H,024H,000H,000H ; D_1D
	00 00	63	DB	000H,018H,03CH,07EH,0FFH,0FFH,000H,000H ; D_1E
00E0	00 C0 C0 C0 FE	64	DB	000H,000H,0FFH,0FFH,0FFH,0FFH,000H,000H ; D_1F
	00 00	65		
00E8	00 24 66 FF 66 24	66		
	00 00	67		
00F0	00 18 3C 7E FF FF	68		
	00 00	69		
00F8	00 FF FF 7E 3C 18	70		
	00 00	71		
		72		
0100	00 00 00 00 00 00	73	DB	000H,000H,000H,000H,000H,000H,000H,000H ; SP_D_20
		74	DB	030H,078H,078H,030H,030H,000H,030H,000H ; ! D_21
0108	30 78 78 30 30 00	75	DB	06CH,06CH,06CH,000H,000H,000H,000H,000H ; " D_22
	30 00	76		
0110	CC CC 6C 00 00 00	77	DB	06CH,06CH,0FEH,06CH,0FEH,06CH,06CH,000H ; # D_23
	00 00	78		
0118	6C 6C FE 6C FE 6C	79		
	6C 00	80		
0120	00 C0 78 0C F8	81		
	30 00	82		
0128	00 C6 CC 18 30 66	83	DB	000H,0C6H,0CCH,018H,030H,066H,0C6H,000H ; PER CENT_D_25
	C6 00	84		
0130	30 00 38 76 DC CC	85	DB	038H,0C6H,038H,076H,0DCH,0CCH,076H,000H ; & D_26
	16 00	86		
0138	60 60 C0 00 00 00	87	DB	060H,060H,0COH,000H,000H,000H,000H,000H ; ' D_27
	10 00	88		
0140	18 30 60 60 60 30	89	DB	018H,030H,060H,060H,060H,030H,018H,000H ; ( D_28
	18 00	90		
0148	60 30 18 18 18 30	91	DB	060H,030H,018H,018H,030H,060H,000H ; ) D_29
	60 00	92		
0150	00 66 3C FF 3C 66	93	DB	000H,066H,03CH,0FFH,03CH,066H,000H,000H ; * D_2A
	00 00	94		
0158	00 30 30 FC 30 30	95	DB	000H,030H,030H,0FCH,030H,030H,000H,000H ; + D_2B
	00 00	96		
0160	00 00 00 00 00 30	97	DB	000H,000H,000H,000H,000H,000H,000H,000H ; , D_2C
	30 00	98		
0168	00 00 00 FC 00 00	99	DB	000H,000H,000H,0FCH,000H,000H,000H,000H ; - D_2D
	00 00	100		
0170	00 00 00 00 00 30	101	DB	000H,000H,000H,000H,000H,000H,000H,000H ; . D_2E
	30 00	102		
0178	06 0C 18 30 60 C0	103	DB	006H,00CH,018H,030H,060H,0COH,080H,000H ; / D_2F
	80 00	104		
		105		
0180	7C C6 CE DE F6 E6	106	DB	07CH,0C6H,0CEH,0DEH,0F6H,0E6H,07CH,000H ; O_D_30
	7C 00	107	DB	030H,070H,030H,030H,030H,030H,0FCH,000H ; 1 D_31
0188	10 30 30 30 30 30	108	DB	078H,0CCH,0OCH,038H,060H,0CCH,0FCH,000H ; 2 D_32
	FC 00	109		
0190	7C CC 3C 38 60 CC	110	DB	078H,0CCH,0OCH,038H,060H,0CCH,078H,000H ; 3 D_33
	FC 00	111		
0198	00 3C 38 0C CC	112	DB	01CH,03CH,0C6H,0CCH,0FEH,0OCH,01EH,000H ; 4 D_34
	78 00	113		
01A0	1C 3C 6C CC FE OC	114	DB	0FCFH,0COH,0F8H,00CH,0OCH,0CCH,078H,000H ; 5 D_35
	1E 00	115		
01A8	00 F8 0C OC CC	116	DB	038H,060H,0COH,0F8H,0CCH,0CCH,078H,000H ; 6 D_36
	78 00	117		
01B0	38 60 CO F8 CC CC	118	DB	0FCFH,0OCH,0OCH,018H,030H,030H,030H,000H ; 7 D_37
	78 00	119		
01B8	FC CC OC 18 30 30	120	DB	078H,0CCH,0OCH,018H,030H,030H,030H,000H ; 8 D_38
	FC 00	121		
01C0	7C CC 7C CC 7C CC	122	DB	078H,0CCH,0CCH,07CH,0OCH,0CCH,078H,000H ; 9 D_39
	78 00	123		
01C8	78 CC CC 7C OC 18	124	DB	000H,030H,030H,000H,000H,030H,030H,000H ; : D_3A
	78 00	125		
01D0	00 30 30 00 00 30	126	DB	000H,030H,030H,000H,000H,030H,030H,000H ; : D_3B
	30 00	127		
01D8	00 30 30 00 00 30	128	DB	018H,030H,060H,0COH,060H,030H,018H,000H ; < D_3C
	30 00	129		
01E0	00 60 60 60 60 30	130	DB	000H,000H,0FCH,000H,000H,0FCH,000H,000H ; = D_3D
	18 00	131		
01E8	00 00 FC 00 00 FC	132	DB	060H,030H,018H,0OCH,018H,030H,060H,000H ; > D_3E
	00 00	133		
01F0	60 00 18 OC 18 30	134	DB	078H,0CCH,0OCH,018H,030H,000H,030H,000H ; ? D_3F
	50 00	135		
01F8	38 CC OC 18 30 00	136		
	30 00	137		
		138		
0200	7C C6 DE DE DE C0	139	DB	07CH,0C6H,0DEH,0DEH,0DEH,0COH,078H,000H ; @ D_40
	78 00	140	DB	030H,078H,0CCH,0CCH,0FCH,0CCH,0CCH,000H ; A_D_41
0208	30 78 CC CC FC CC	141		
	CC 00	142		
0210	3C 66 7C 66 66 66	143	DB	0FCFH,066H,066H,07CH,066H,066H,0FCFH,000H ; B_D_42
	FC 00	144		
0218	36 C6 CO CO 00 66	145	DB	03CH,066H,0COH,0COH,0COH,066H,03CH,000H ; C_D_43
	30 00	146		
0220	FE 6C 66 66 66 6C	147	DB	0F8H,0C6H,066H,066H,066H,06CH,0F8H,000H ; D_D_44
	18 00	148		
0228	FE 62 68 78 68 62	149	DB	0FEH,062H,068H,078H,068H,062H,0FEH,000H ; E_D_45
	FE 00	150		
0230	FE 62 68 78 66 60	151	DB	0FEH,062H,068H,078H,068H,060H,0FOH,000H ; F_D_46
	FE 00	152		
0238	3C 66 CO CO 00 66	153	DB	03CH,066H,0COH,0COH,0CEH,066H,03EH,000H ; G_D_47
	3E 00	154		
0240	CC CC CC FC CC CC	155	DB	0CCH,0CCH,0CCH,0FCH,0CCH,0CCH,0CCH,000H ; H_D_48
	CC 00	156		
0248	18 30 30 30 30 30	157	DB	078H,030H,030H,030H,030H,030H,078H,000H ; I_D_49
	78 00	158		
0250	18 OC OC OC CC CC	159	DB	01EH,0OCH,0OCH,0OCH,0OCH,0CCH,078H,000H ; J_D_4A
	78 00	160		
0258	6C 78 6C 66 66 66	161	DB	0E6H,066H,06CH,078H,06CH,066H,0E6H,000H ; K_D_4B
	E6 00	162		
0260	FO 60 60 60 62 66	163	DB	0FOH,060H,060H,062H,066H,0FEH,000H ; L_D_4C
	FE 00	164		
0268	1C FE FE D6 C6	165	DB	0C6H,0EEH,0FEH,0FEH,0D6H,0C6H,0C6H,000H ; M_D_4D
	C6 00	166		
0270	C6 E6 FE DE CE C6	167	DB	0C6H,0E6H,0F6H,0DEH,0CEH,0C6H,0C6H,000H ; N_D_4E
	C6 00	168		
0278	38 6C C6 C6 C6 6C	169	DB	038H,0C6H,0C6H,0C6H,0C6H,0C6H,038H,000H ; O_D_4F
	38 00	170		
		171		
0280	FC 66 7C 60 60	172	DB	0FCFH,066H,066H,07CH,060H,060H,0FOH,000H ; P_D_50
	FO 00	173	DB	078H,0CCH,0CCH,0CCH,0CCH,0CCH,078H,01CH,000H ; Q_D_51
0286	CC CC CC DC 78	174		
	1C 00	175		
0290	E6 66 7C 6C 66	176	DB	0FCFH,066H,066H,07CH,06CH,066H,0E6H,000H ; R_D_52
	E6 00	177		
0298	CC CC EO 70 1C CC	178	DB	078H,0CCH,0E0H,070H,01CH,0CCH,078H,000H ; S_D_53
	78 00	179		
02A0	FC B4 30 30 30 30	180	DB	0FCFH,0B4H,030H,030H,030H,030H,078H,000H ; T_D_54
	78 00	181		
02A8	CC CC CC CC CC CC	182	DB	0CCH,0CCH,0CCH,0CCH,0CCH,0CCH,0CCH,0FCFH,000H ; U_D_55

0280	FC 00	183	DB	OCCH, OCCH, OCCH, OCCH, OCCH, 078H, 030H, 000H ; V D_56
CC CC	CC CC CC CC	184	DB	0C6H, OC6H, OC6H, OC6H, OFEH, OEH, OC6H, 000H ; W D_57
30 00	C6 D6 FE EE	185	DB	0C6H, OC6H, OC6H, 038H, 038H, 06CH, 0C6H, 000H ; X D_58
C6 00		186	DB	OCCH, OCCH, OCCH, 078H, 030H, 030H, 078H, 000H ; Y D_59
02C0	C6 38 38 6C	188	DB	0FEH, OC6H, 08CH, 018H, 032H, 066H, OFEH, 000H ; Z D_5A
CC CC	CC 78 30 30	189	DB	078H, 060H, 060H, 060H, 060H, 060H, 078H, 000H ; [ D_5B
78 00		190	DB	0COH, 060H, 030H, 00CH, 00CH, 002H, 000H ; BACKSLASH D_5C
02D0	FE C6 8C 18 32 66	191	DB	078H, 018H, 018H, 018H, 018H, 078H, 000H ; ] D_5D
FE 00		192	DB	010H, 038H, 06CH, OC6H, 000H, 000H, 000H, 000H ; CIRCUMFLEX D_5E
02D8	78 60 60 60 60	193	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 0FFH ; _ D_5F
78 00		194		
02E0	CO 60 30 18 0C 06	195		
02E0	02 00	196		
02E8	78 18 18 18 18	197		
78 00		198		
02F0	01 18 6C C6 00 00	199		
00 00	00 00 00 00 00	200		
02F8	00 FF	201		
		202		
		203		
		204		
0300	30 30 18 00 00 00	205	DB	030H, 030H, 018H, 000H, 000H, 000H, 000H, 000H ; ' D_60
00 00		206	DB	000H, 000H, 078H, 00CH, 07CH, OCCH, 076H, 000H ; LOWER CASE A D_61
0308	00 00 78 0C 7C CC	207	DB	0E0H, 060H, 060H, 07CH, 066H, 066H, 0DCH, 000H ; L.C. B D_62
76 00		208	DB	000H, 000H, 078H, OCCH, 0COH, OCCH, 078H, 000H ; L.C. C D_63
0310	00 00 60 7C 66 66	209	DB	01CH, OCCH, 00CH, 07CH, OCCH, OCCH, 076H, 000H ; L.C. D D_64
DC 00		210	DB	000H, 000H, 078H, OCCH, OFCH, 0COH, 078H, 000H ; L.C. E D_65
0318	00 00 78 CC CO CC	211	DB	038H, OC6H, 060H, OFOH, 060H, 060H, 0FOH, 000H ; L.C. F D_66
78 00		212	DB	000H, 000H, 076H, OCCH, 00CH, 07CH, 000H, 078H, 000H ; L.C. G D_67
0320	10 00 0C 7C CC CC	213	DB	0E0H, 060H, 060H, 07CH, 066H, 066H, 0DCH, 000H ; L.C. H D_68
76 00		214	DB	030H, 000H, 070H, 030H, 030H, 030H, 078H, 000H ; L.C. I D_69
0328	00 00 78 CC FC CO	215	DB	00CH, 000H, 00CH, 00CH, 00CH, 00CH, 00CH, 078H ; L.C. J D_6A
78 00		216	DB	0E0H, 060H, 060H, 060H, 060H, 060H, 060H, 060H ; L.C. K D_6B
0330	38 6C 60 FO 60 60	217	DB	070H, 030H, 030H, 030H, 030H, 030H, 078H, 000H ; L.C. L D_6C
70 00		218	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 078H ; L.C. M D_6D
0338	00 00 76 CC CC 7C	219	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 078H ; L.C. N D_6E
DC F8		220	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 078H ; L.C. O D_6F
0340	E0 60 6C 76 66 66	221		
16 00		222		
0348	00 00 70 30 30 30	223		
78 00		224		
0350	0C 00 0C 0C 0C CC	225		
CC 78		226		
0358	00 00 66 6C 78 6C	227		
F6 00		228		
0360	70 30 30 30 30 30	229		
78 00		230		
0368	00 00 CC FE FE D6	231		
C6 00		232		
0370	00 00 FB CC CC CC	233		
CC 00		234		
0378	00 00 78 CC CC CC	235		
78 00		236		
		237		
0380	00 00 DC 66 66 7C	238		
60 FO		239		
0388	00 00 76 CC CC 7C	240		
OC 1E		241		
0390	00 00 DC 7C 66 60	242		
F0 00		243		
0398	00 00 00 7C CO 78 0C	244		
F8 00		245		
03A0	10 30 7C 30 30 34	246		
18 00		247		
03A8	00 00 CC CC CC CC	248		
CC 00		249		
03B0	00 00 CC CC CC 7C	250		
30 00		251		
03B8	00 00 C6 D6 FE FE	252		
6C 00		253		
03C0	00 00 C6 6C 38 6C	254		
C6 00		255		
03C8	00 00 CC CC CC 7C	256		
OC F8		257		
03D0	00 00 FC 98 30 64	258		
FC 00		259		
03D8	1C 30 30 EO 30 30	260		
IC 00		261		
03E0	18 18 18 00 18 18	262		
18 00		263		
03E8	E0 30 30 1C 30 30	264		
E0 00		265		
03F0	76 DC 00 00 00 00	266		
00 00		267		
03FB	00 10 38 6C C6 C6	268		
FE 00		269		
		270		
0400		271		
		272		
0400	78 CC CO CC 78 18	273	INT_1F_1	LABEL BYTE
OC 78		274	DB	078H, OCCH, 0COH, OCCH, 078H, 018H, 00CH, 078H ; D_80
0408	00 CC 00 CC CC CC	275	DB	000H, OCCH, 000H, OCCH, OCCH, OCCH, 07EH, 000H ; D_81
0410	1C 00 7C CC FC CO	276	DB	01CH, 000H, 078H, OCCH, OFCH, 0COH, 078H, 000H ; D_82
78 00		277	DB	07EH, 0C3H, 03CH, 006H, 03EH, 066H, 03FH, 000H ; D_83
0418	7E C3 06 3C 06 66	278	DB	0CCH, 000H, 078H, 00CH, 07CH, OCCH, 07EH, 000H ; D_84
0420	CO 00 7C 0C 7C CC	279	DB	0E0H, 000H, 078H, 00CH, 07CH, OCCH, 07EH, 000H ; D_85
7E 00		280	DB	030H, 030H, 078H, 00CH, 07CH, OCCH, 07EH, 000H ; D_86
0428	E0 00 78 CC FC CO	281	DB	000H, 000H, 078H, 00CH, 07CH, OCCH, 07EH, 000H ; D_87
7E 00		282	DB	07CH, 0C3H, 03CH, 006H, 07EH, 060H, 03CH, 000H ; D_88
042E	00 00 78 0C 7C CC	283	DB	0CCH, 000H, 078H, 00CH, 07CH, OCCH, 07EH, 000H ; D_89
7E 00		284	DB	0E0H, 000H, 078H, 00CH, 07CH, OCCH, 07EH, 000H ; D_8A
0430	30 00 78 0C 7C CC	285	DB	0CCH, 000H, 070H, 030H, 030H, 030H, 078H, 000H ; D_8B
7E 00		286	DB	07CH, 0C6H, 038H, 018H, 018H, 018H, 03CH, 000H ; D_8C
0438	00 00 78 CO 78	287	DB	0E0H, 000H, 070H, 030H, 030H, 030H, 078H, 000H ; D_8D
OC 38		288	DB	01CH, 030H, 030H, 030H, 030H, 030H, 078H, 000H ; D_8E
0440	7E C3 3C 66 7E 60	289	DB	000H, 000H, 070H, 030H, 030H, 030H, 078H, 000H ; D_8F
CC 00		290	DB	07CH, 0C6H, 038H, 018H, 018H, 018H, 03CH, 000H ; D_8G
0448	CC 00 78 CC FC CO	291	DB	0CCH, 000H, 078H, 00CH, 07CH, OCCH, 07EH, 000H ; D_8H
78 00		292	DB	0E0H, 000H, 078H, 00CH, 07CH, OCCH, 07EH, 000H ; D_8I
0450	E0 00 78 CC FC CO	293	DB	030H, 030H, 078H, 00CH, 07CH, OCCH, 07EH, 000H ; D_8J
7E 00		294	DB	07CH, 0C6H, 038H, 018H, 018H, 018H, 03CH, 000H ; D_8K
0458	CC 00 70 30 30 30	295	DB	0CCH, 000H, 070H, 030H, 030H, 030H, 078H, 000H ; D_8L
78 00		296	DB	07CH, 0C6H, 038H, 018H, 018H, 018H, 03CH, 000H ; D_8M
0460	7C C6 38 18 18 18	297	DB	0E0H, 000H, 070H, 030H, 030H, 030H, 078H, 000H ; D_8N
7E 00		298	DB	01CH, 030H, 030H, 030H, 030H, 030H, 078H, 000H ; D_8O
0468	EO 00 70 30 30 30	299	DB	000H, 000H, 070H, 030H, 030H, 030H, 078H, 000H ; D_8P
78 00		300	DB	06CH, 038H, 06CH, 0C6H, OFEH, 0C6H, 0C6H, 000H ; D_8Q
0470	C6 38 6C C6 FE C6	301	DB	030H, 030H, 000H, 078H, 00CH, 07CH, OCCH, 07EH, 000H ; D_8R
C6 00		302	DB	000H, 000H, 078H, 00CH, 07CH, OCCH, 07EH, 000H ; D_8S
0478	30 30 00 78 CC FC	303	DB	01CH, 030H, 030H, 000H, 078H, 00CH, 07CH, OCCH, 07EH, 000H ; D_8T
CC 00		304	DB	000H, 000H, 078H, 00CH, 07CH, OCCH, 07EH, 000H ; D_8U
		305	DB	01CH, 030H, 030H, 000H, 078H, 00CH, 07CH, OCCH, 07EH, 000H ; D_8V
0480	1C 00 FC 60 78 60	306	DB	000H, 000H, 078H, 00CH, 07CH, OCCH, 07EH, 000H ; D_8W
FC 00		307	DB	000H, 000H, 078H, 00CH, 07CH, OCCH, 07EH, 000H ; D_8X
0488	00 00 7F 0C 7F CC	308	DB	000H, 000H, 078H, 00CH, 07CH, OCCH, 07EH, 000H ; D_8Y

0490	7F 00	309	DB	03EH, 06CH, 0CCH, 0FEH, 0CCH, 0CCH, 0CEH, 000H : D_92
	3E 6C CC FE CC CC	310		
	CE 00	311		
0498	78 CC 00 78 CC CC	312	DB	078H, 0CCH, 000H, 078H, 0CCH, 0CCH, 078H, 000H : D_93
04A0	78 00 00 78 CC CC	313	DB	000H, 0CCH, 000H, 078H, 0CCH, 0CCH, 078H, 000H : D_94
	00 00	314		
04A8	00 E0 00 78 CC CC	315	DB	000H, 0E0H, 000H, 078H, 0CCH, 0CCH, 078H, 000H : D_95
04B0	78 CC 00 CC CC CC	316	DB	078H, 0CCH, 000H, 0CCH, 0CCH, 0CCH, 07EH, 000H : D_96
04B8	00 E0 00 CC CC CC	317	DB	000H, 0E0H, 000H, 0CCH, 0CCH, 0CCH, 07EH, 000H : D_97
	7E 00	318		
04C0	00 CC 00 CC TC	320	DB	000H, 0CCH, 000H, 0CCH, 0CCH, 0CCH, 07CH, 000H, 0F8H : D_98
	321	322		
04C8	C3 18 3C 66 66 3C	323	DB	0C3H, 018H, 03CH, 066H, 066H, 03CH, 018H, 000H : D_99
	18 00	324		
04D0	CC 00 CC CC CC CC	325	DB	0CCH, 000H, 0CCH, 0CCH, 0CCH, 0CCH, 0CCH, 078H, 000H : D_9A
04D8	78 18 7E CO 00 7E	327	DB	018H, 018H, 07EH, 0COH, 0COH, 07EH, 018H, 018H : D_9B
04E0	38 6C 64 F0 60 E6	328	DB	038H, 06CH, 064H, 0F0H, 0F0H, 0E6H, 0FCH, 000H : D_9C
04E8	FC 00 00 78 FC 30 FC	329	DB	0CCH, 0CCH, 078H, 0FCH, 030H, 0FCH, 030H, 030H : D_9D
	00 30	330		
04F0	F8 CC FA C6 CF	331	DB	0F8H, 0CCH, 0CCH, 0FAH, 0C6H, 0CFH, 0C6H, 0C7H : D_9E
04F8	0E 1B 18 3C 18 18	332	DB	00EH, 01BH, 018H, 03CH, 018H, 018H, 0DBH, 070H : D_9F
	08 70	333		
0500	1C 00 00 78 OC 7C CC	334	DB	01CH, 000H, 078H, 0OCH, 07CH, 0CCH, 07EH, 000H : D_A0
0508	7E 00 70 30 30 30	340	DB	038H, 000H, 070H, 030H, 030H, 030H, 078H, 000H : D_A1
0510	00 1C 00 78 CC CC	341		
	78 00	342		
0518	00 00 00 CC CC CC	343	DB	000H, 01CH, 000H, 0CCH, 0CCH, 0CCH, 07EH, 000H : D_A3
	00 00	344		
0520	00 F8 00 F8 CC CC	345	DB	000H, 0F8H, 000H, 0F8H, 0CCH, 0CCH, 0CCH, 000H : D_A4
	CC 00	346		
0528	FC 00 CC EC FC DC	347	DB	0FCH, 000H, 0CCH, 0ECH, 0FCH, 0CCH, 0CCH, 000H : D_A5
0530	3C 6C 6C 3E 00 7E	348	DB	03CH, 06CH, 06CH, 03EH, 000H, 07EH, 000H, 000H : D_A6
	00 00	349		
0538	38 6C 6C 38 00 7C	350	DB	038H, 06CH, 06CH, 038H, 000H, 07CH, 000H, 000H : D_A7
0540	30 00 30 60 CO CC	351	DB	030H, 000H, 030H, 060H, 0COH, 0CCH, 078H, 000H : D_A8
	78 00	352		
0548	00 00 00 FC CO CO	353	DB	000H, 000H, 000H, 0FCH, 0CCH, 0COH, 000H, 000H : D_A9
0550	00 00 00 FC OC OC	354	DB	000H, 000H, 000H, 0FCH, 0COH, 0COH, 000H, 000H : D_AA
0558	C3 6C CC DE 33 66	355	DB	0C3H, 06CH, 0CCH, 0DEH, 033H, 066H, 0CCH, 00FH : D_AB
0560	C3 CC DB 37 6F	356	DB	0C3H, 06CH, 0CCH, 0DBH, 037H, 06FH, 0CCH, 003H : D_AC
	00 03	357		
0568	18 00 18 18 18 18	358	DB	018H, 018H, 000H, 018H, 018H, 018H, 018H, 000H : D_AD
0570	00 33 66 CC 66 33	359	DB	000H, 033H, 066H, 0CCH, 066H, 033H, 000H, 000H : D_AE
0578	00 CC 66 33 66 CC	360	DB	000H, 0CCH, 066H, 033H, 066H, 0CCH, 000H, 000H : D_AF
	00 00	361		
0580	22 88 22 88 22 88	362	DB	022H, 088H, 022H, 088H, 022H, 088H, 022H, 088H : D_B0
0588	55 AA 55 AA 55 AA	363	DB	055H, 0AAH, 055H, 0AAH, 055H, 0AAH, 055H, 0AAH : D_B1
0590	00 00 DB EE DB 77	364	DB	0DBH, 077H, 0DBH, 0EEH, 0DBH, 077H, 0DBH, 0EEH : D_B2
	DB EE	365		
0598	18 18 18 18 18 18	366	DB	018H, 018H, 018H, 018H, 018H, 018H, 018H, 018H : D_B3
05A0	18 18 18 18 F8 18	367	DB	018H, 018H, 018H, 018H, 0F8H, 018H, 018H, 018H : D_B4
05A8	18 18 F8 18 F8 18	368	DB	018H, 018H, 0F8H, 018H, 0F8H, 018H, 018H, 018H : D_B5
05B0	36 36 36 36 F6 36	369	DB	036H, 036H, 036H, 036H, 0F6H, 036H, 036H, 036H : D_B6
	36 36	370		
05B8	00 00 00 FE 36	371	DB	000H, 000H, 000H, 000H, 0FEH, 036H, 036H, 036H : D_B7
05C0	00 00 FB 18 FB 18	372	DB	000H, 000H, 0FBH, 018H, 0FBH, 018H, 018H, 018H : D_B8
05C8	18 18 F6 06 F6 36	373	DB	036H, 036H, 0F6H, 006H, 0F6H, 036H, 036H, 036H : D_B9
05D0	36 36 36 36 36 36	374	DB	036H, 036H, 036H, 036H, 036H, 036H, 036H, 036H : D_BA
05D8	00 00 FE 06 F6 36	375	DB	000H, 000H, 0FEH, 006H, 0F6H, 036H, 036H, 036H : D_BB
05E0	36 36 F6 06 FE 00	376	DB	036H, 036H, 0F6H, 006H, 0FEH, 000H, 000H, 000H : D_BC
	00 00	377		
05E8	36 36 36 36 FE 00	378	DB	036H, 036H, 036H, 036H, 0FEH, 000H, 000H, 000H : D_BD
05F0	18 18 FB 18 FB 00	379	DB	018H, 018H, 0F8H, 018H, 0F8H, 000H, 000H, 000H : D_BE
05F8	00 00 00 00 FE 18	380	DB	000H, 000H, 000H, 000H, 0FBH, 018H, 018H, 018H : D_BF
	18 18	381		
0600	18 18 18 18 1F 00	382	DB	018H, 018H, 018H, 018H, 01FH, 000H, 000H, 000H : D_C0
0608	18 18 18 18 FF 00	383	DB	018H, 018H, 018H, 018H, 0FFH, 000H, 000H, 000H : D_C1
0610	00 00 00 00 FF 18	384	DB	000H, 000H, 000H, 000H, 0FFH, 018H, 018H, 018H : D_C2
0618	18 18 18 18 1F 18	385	DB	018H, 018H, 018H, 018H, 01FH, 018H, 018H, 018H : D_C3
0620	00 00 00 00 FF 00	386	DB	000H, 000H, 000H, 000H, 0FFH, 000H, 000H, 000H : D_C4
0628	18 18 18 18 FF 18	387	DB	018H, 018H, 018H, 018H, 0FFH, 018H, 018H, 018H : D_C5
0630	18 18 1F 18 1F 18	388	DB	018H, 018H, 01FH, 018H, 01FH, 018H, 018H, 018H : D_C6
0638	36 36 36 37 36 36	389	DB	036H, 036H, 036H, 036H, 037H, 036H, 036H, 036H : D_C7
0640	36 36 37 30 3F 00	390	DB	036H, 036H, 037H, 030H, 03F, 000H, 000H, 000H : D_C8
	00 00	391		
0648	00 00 3F 30 37 36	392	DB	000H, 000H, 003F, 030H, 037H, 000H, 000H, 000H : D_C9
0650	36 36 F7 00 FF 00	393	DB	036H, 036H, 0F7H, 000H, 0FFH, 000H, 000H, 000H : D_CA
	36 36	394		
0658	00 00 FF 00 FF 36	395	DB	000H, 000H, 0FFH, 000H, 0FFH, 000H, 036H, 036H : D_CB
0660	36 36 37 30 37 36	396	DB	036H, 036H, 037H, 030H, 037H, 036H, 036H, 036H : D_CC
0668	00 00 FF 00 FF 00	397	DB	000H, 000H, 0FFH, 000H, 0FFH, 000H, 000H, 000H : D_CD
0670	36 36 F7 00 FF 36	398	DB	036H, 036H, 0F7H, 000H, 0FFH, 000H, 036H, 036H : D_CE
	36 36	399		

0678	18	18	FF	00	FF	00	435	DB	018H,018H,0FFH,000H,0FFH,000H,000H,000H ; D_CF	
	00	00					436			
0680	36	36	36	36	36	FF	00	438	DB	036H,036H,036H,036H,0FFH,000H,000H,000H ; D_D0
	00	00	FF	00	FF	18		439		
0688	09	09	FF	00	FF	18		440		
0690	00	00	00	FF	36		441			
	36	36					442			
0698	36	36	36	36	3F	00	443	DB	000H,000H,000H,000H,0FFH,036H,036H,036H ; D_D2	
	00	00	FF	18	1F	00	444			
06A0	18	18	1F	18	FF	00	445	DB	036H,036H,036H,036H,0FFH,000H,000H,000H ; D_D3	
	00	00					446			
06AB	00	00	1F	18	1F	18	447	DB	018H,018H,01FH,018H,01FH,000H,000H,000H ; D_D4	
							448			
06B0	18	18	00	00	00	3F	36	449	DB	000H,000H,000H,000H,03FH,036H,036H,036H ; D_D5
	36	36					450			
06B8	36	36	36	36	36	FF	36	451		
	36	36					452			
06C0	18	18	FF	18	FF	18		453	DB	036H,036H,036H,036H,0FFH,036H,036H,036H ; D_D7
	18	18					454			
06C8	18	18	18	18	F8	00	455	DB	018H,018H,018H,018H,018H,018H,000H,000H ; D_D9	
	00	00					456			
06D0	00	00	00	00	00	FF	18	457	DB	000H,000H,000H,000H,01FH,018H,018H,018H ; D_DA
							458			
06D8	FF	FF	FF	FF	FF	FF		459	DB	0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH ; D_DB
	FF	FF					460			
06E0	00	00	00	00	00	FF	FF	461	DB	000H,000H,000H,000H,000H,0FFH,0FFH,0FFH,0FFH ; D_DC
							462			
06E8	FO	FO	FO	FO	FO	FO		463	DB	0FOH,0FOH,0FOH,0FOH,0FOH,0FOH,0FOH,0FOH ; D_DD
	FO	FO					464			
06F0	FO	FO	FO	FO	FO	FO		465	DB	0OFH,0OFH,0OFH,0OFH,0OFH,0OFH,0OFH,0OFH ; D_DE
	FO	FO					466			
06F8	FO	FO	FF	FF	FF	00	467	DB	0FFH,0FFH,0FFH,0FFH,000H,000H,000H,000H ; D_DF	
	FO	FO					468			
0700	00	00	76	DC	C8	DC	469			
	76	00					470			
0708	00	00	8	CC	F8	CC		471	DB	000H,000H,076H,0DC8,0C8H,0DC8,076H,000H ; D_E0
	00	00					472			
0710	00	00	FC	CC	CO	CO	473	DB	000H,078H,0CC8,0F8H,0CC8,0F8H,0COH,0COH ; D_E1	
	00	00					474			
0718	00	00	6C	6C	6C	6C	475	DB	000H,0FC8,0COH,0COH,0COH,0COH,0COH,0COH ; D_E2	
	00	00					476			
0720	FC	CC	60	30	60	CC	477	DB	000H,0FEH,06CH,06CH,06CH,06CH,06CH,000H ; D_E3	
	CC	00					478			
0728	00	00	7E	DB	D8	D8	479	DB	0FC8,0CC8,060H,030H,060H,0C8H,0FC8,000H ; D_E4	
	76	00					480			
0730	00	66	66	66	66	7C	481	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_E5	
	66	00					482			
0738	60	CO	30	18	18	18	483	DB	000H,066H,066H,066H,066H,07CH,060H,0COH ; D_E6	
	30	FC	30	78	CC	78	484			
0740	30	FC	30	78	CC	78	485	DB	000H,076H,0DC8,018H,018H,018H,000H,000H ; D_E7	
	38	6C	6C	6C	6C	6C	486	DB	0FC8,030H,078H,0CC8,0C8H,078H,030H,0FC8 ; D_E8	
0748	38	6C	6C	FE	C6	6C	487	DB	038H,06CH,0C6H,0FEH,0C6H,06CH,038H,000H ; D_E9	
	38	00					488			
0750	38	00	CC	C6	6C	6C	489	DB	038H,06CH,0C6H,0C6H,0C6H,06CH,06CH,000H ; D_EA	
	38	00					490			
0758	1C	30	18	7C	CC	CC	491	DB	01CH,030H,018H,07CH,0C8H,0C8H,078H,000H ; D_EB	
	18	00					492			
0760	00	00	7E	DB	DB	7E	493	DB	000H,000H,07EH,0DBH,0DBH,07EH,000H,000H ; D_EC	
	00	00					494			
0768	00	06	DC	7E	DB	7E	495	DB	006H,00CH,07EH,0DBH,0DBH,07EH,060H,0COH ; D_ED	
	00	30	60	CO	F8	CO	496			
0770	38	60	CO	F8	CO	60	497	DB	038H,060H,0COH,0F8H,0COH,060H,038H,000H ; D_EE	
	78	00					498			
0778	78	CC	CC	CC	CC	CC	499	DB	078H,0CC8,0CC8,0CC8,0CC8,0CC8,0CC8,000H ; D_EF	
	CC	00					500			
0780	00	FC	00	FC	00	FC	501			
	00	00					502			
0788	30	30	FC	30	30	00	503	DB	000H,0FC8,000H,0FC8,000H,0FC8,000H,000H ; D_F0	
	30	30					504			
0790	60	18	30	30	60	00	505	DB	030H,030H,018H,030H,018H,030H,000H,000H ; D_F1	
	30	FC	00	00	00	00	506			
0798	18	30	60	30	18	00	507	DB	018H,030H,060H,030H,018H,000H,000H,000H ; D_F2	
	30	FC	00	00	00	00	508			
07A0	0E	18	18	18	18	18	509	DB	018H,030H,060H,030H,018H,000H,000H,000H ; D_F3	
	18	18					510			
07A8	18	18	18	18	18	DB	511	DB	00EH,018H,018H,018H,018H,018H,018H,018H ; D_F4	
	18	18					512			
07B0	30	30	00	FC	00	30	513	DB	018H,018H,018H,018H,018H,0DBH,0DBH,070H ; D_F5	
	30	30					514			
07B8	00	76	DC	00	76	DC	515	DB	000H,030H,000H,0FC8,000H,030H,030H,000H ; D_F6	
	76	DC	00	00	00	00	516			
07C0	38	6C	6C	38	00	00	517	DB	000H,076H,0DC8,000H,076H,0DC8,000H,000H ; D_F7	
	38	00					518			
07C8	00	00	00	18	18	00	519	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_F8	
	00	00					520			
07D0	00	00	00	00	18	00	521	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_F9	
	00	00					522			
07D8	00	00	00	00	00	18	523	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA	
	00	00					524			
07E0	78	6C	6C	6C	6C	6C	525	DB	00FH,000H,00CH,00CH,00CH,0ECH,06CH,03CH,01CH ; D_FB	
	78	6C	00	00	00	00	526			
07E8	70	18	30	60	78	00	527	DB	078H,06CH,06CH,06CH,06CH,06CH,000H,000H,000H ; D_FC	
	70	18					528			
07F0	00	00	3C	3C	3C	3C	529	DB	070H,018H,030H,060H,078H,000H,000H,000H,000H ; D_FD	
	00	00					530			
07F8	00	00	00	00	00	00	531	DB	000H,000H,03CH,03CH,03CH,03CH,03CH,000H,000H ; D_FE	
	00	00					532			
0800	00	00	00	00	00	00	533	DB	000H,000H,000H,000H,000H,000H,000H,000H,000H ; D_FF	
	00	00					534			
							535			
							536			
							537			

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