





















Interface	Signal	Pin
EMAC	EMAC_MDC_out EMAC_MDL.In EMAC_MDD_out EMAC_CRS_out EMAC_COL_out	Any GPIO
I2C	I2CEXTO_SCL_in I2CEXTO_SDL_in I2CEXTS_SCL_in I2CEXTS_SCL_in I2CEXTS_SCL_out I2CEXTS_SCL_out I2CEXTS_SCL_out I2CEXTS_SCL_out I2CEXTS_SCL_out	Any GPIO
General Purpose SPI	HSPIQ_In/_out HSPIQ_In/_out HSPICLK_In/_out HSPI_CSO_In/_out HSPI_CSS_out YSPIQ_In/_out YSPIQ_In/_out YSPIC_IN/_out YSPIC_ISO_In/_out YSPI_CSO_In/_out YSPI_CSS_out	Any GPIO

Mounting Holes	MTDI/GPI012:	Pull-Down
	GPI00:	Pull-Up
GND ← MH2 GND ← MH3	GPI02:	Pull-Down
	GP104:	Pull-Down
	MTDO/GPI015:	Pull-Up
	GPI05:	Pull-Up

		Voltage of Intern	al L00(V00,50I0)			
Pio	Default	3.3V	1.89			
GPI012/WTDI	Pull-Down	0	1	Bootstrapping Pins		
		Beotin	g Mode			
#fo	Default	SPI Flash Boot	Download Boot	== Set	Settings	
GPI00	Pull-Up	1	0			
GPI02	Pull-Down	Don't-care				
		Debugging Log on I	JOTXD During Booting			
Pie	Default	USTXD Toggifing	UOTXD Silent			
GP1015/MT00	Pull-Up					
		Timing of	SDIO Stave			
Pie	Default	Felling-edge:Output	Falling-edgesinput Rising-edgesOutput	Rising-edgesinput Falling-edge:Output	Rising-edgerinput Rising-edgerOutput	
GPI015/WT00	Pull-Up		0	1	1	
GPI05	Pull-Up		1			

