High Performance Computing in Web Browsers

Henning Lohse

Abstract—
Index Terms—

I. INTRODUCTION

TODAY'S computational applications originate from broad interdisciplinary fields. These include meteorology, astro physics and molecular biology in scientific computing, as well as fluid dynamics, crash tests and distributed databases in commercial areas, or online games for consumers.

They share high demands to the computational platform. Results are prefered to be achieved as fast, precise and energy efficient as possible or required. Large data sets have to be handled accordingly. Techniques to meet these demands have been explored and aggregated in the field of High Performance Computing. (HPC)

Runtime and storage requirements of HPC applications traditionally exceed the capabilities of a single processor or machine. Therefore, parallel computing became the major factor for performance optimizations. Today, multiple heterogeneous processors can be used in interconnected machines. When designing an HPC application, the following aspects have to be considered:

- Processor. Determine which application parts should be executed on a distinct processor. This can be a core of a multicore CPU.
- Intra-node processing. A machine node might contain multiple sockets for CPU, where each CPU provides multiple cores. The application design should benefit from synergetic effects of caching, memory access patterns and locality while minimizing communication overhead. For programming, pthreads or OpenMP can be used.
- Inter-node processing. To meet HPC applications' requirements, multiple nodes are usually connected via network. Inter-node communication is orders of magnitude slower then intra-node communication. While synergetic effects to be considered here are similar to intra-node, communication overhead induces a more significant penalty, sockets or MPI can be used for implementations.
- Coprocessors. Complementary processors like GPUs can
 be used to improve performance for certain types of algorithms. GPUs for example are highly optimized for SIMD
 workloads with preferably short runtimes per data point
 on a huge set of the latter. CUDA or other coprocessordependent libraries can be utilized.

These aspects are tightly coupled. Designs considered for each one are dependent on the others. Furthermore, when implementing the HPC application, optimizations are usually done for a specific platform or range of platforms. Applications come as compiled binaries. While these approaches

provides the best performance, portability is not available or time-consuming.

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A. Motivation

Consumer electronics today are heterogeneous multiprocessor systems. Notebooks, Tablets and Smartphones are equipped with multicore CPUs, GPUs and wireless network technologies. While they run different and diverse operating systems hindering portability of native applications, each platform comes with a web browser.

Modern web browsers can be considered application platforms. They provide GUI rendering, network communication, user interaction and dynamic content control. The development of HTML5 introduced several improvements and new features, like threads or client-server sockets. Plenty of frameworks allowing developers to connect the browser frontend with a server backend exist. The JavaScript engines are constantly improved for faster script execution. And all these features work on virtually any platform with a modern web browser, resulting in inherent application portability.

The topic of this work is to present the current state of web browser technologies regarding HPC application design aspects processor, intra-node and inter-node processing. As coprocessors can be diverse and specialized, this work focuses on GPUs.

B. Section Overview

Section II gives a brief overview of the JavaScript language used in HTML scripts. Important concepts and constructs for further sections are discussed. The following section III introduces asm.js as an annotation-based JavaScript subset for performance optimizations. Intra-node processing capabilities come with HTML5 Web Workers in section IV. Section V presents WebRTC DataChannel and its inter-node processing capabilities. The state of GPUs as coprocessors using WebCL and OpenGL Compute Shaders is shown in section VI. Finally, conclusions are drawn in section VII.

II. JAVASCRIPT

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III. INTRODUCTION

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IV. INTRODUCTION

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V. INTRODUCTION

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VI. INTRODUCTION

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VII. CONCLUSION

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