CSE477
VLSI Digital Circuits
Fall 2002

Lecture 14: Midterm Review

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[Adapted from Rabaey's *Digital Integrated Circuits*, ©2002, J. Rabaey et al.]

Next Lecture and Reminders

- Midterm exam
    - Closed book and notes; formulas needed will be provided
    - Bring your calculator (and a *pencil*, please)
    - Sample practice midterm now posted on the web (both with and without solutions)

- Next lecture (after midterm)
    - Dynamic logic
        - Reading assignment – Rabaey, et al, 6.3

- Reminders
    - Project prototypes due October 29th
    - HW4 due October 31st
    - Evening midterm exam scheduled
        - Wednesday, October 16th from 8:15 to 10:15pm in 260 Willard
        - Only one midterm conflict filed for so far